Issues in Design and Development of Reliable Configurable Distributed Checkout and Launch System for Mission Critical Aero-Space Flight Vehicles

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Abstract: Role of Configurable Distributed Checkout and Launch System (CDCLS) is pivotal in carrying out quick health checks and launching of Aerospace Flight Vehicles. Configurable Distributed Architecture provides flexibility for connecting nodes and scaling Distributed System. Different configurations can be derived from the Master Configuration. Since, Ultra high reliability and infallible performance of the CDCLS is of paramount importance, Safety criticality and Mission criticality analysis needs to be carried out for determination of mission critical parameters. These critical parameters need to be addressed by required fault tolerant architecture, which can be implemented in Hardware and Software for achieving system reliability objective (Say, 0.99).

Keywords: Distributed System, Launch System, Reliability, Fault Tolerance, Automated Testing, System validation.

I. INTRODUCTION

Automated testing of complex technical equipment involves challenges due to multi-disciplinary nature of the system, inadequacy of technical equipment, cost and schedules. However, such test equipment is highly essential to ensure required reliability and quality of the system under test within the stringent time deadlines of delivery, in addition to test time reduction[1].

The Checkout and Launch System is an automated test equipment for aerospace vehicles. It performs comprehensive health checks of Sub-Systems and integrated Missile Systems/space vehicles and Launch operations. In view of mission criticality, technological complexity, high value, enormous efforts put in towards realization of Aerospace Flight Vehicles (Satellite System, Missile System, Aircrafts, etc.), due attention needs to be paid towards optimal design of Configurable Distributed Checkout and Launch System. Distributed Checkout and Launch System is interfaced with Nodes, Digital Controller, Missile Sub-Systems and Launch Systems.

II. EXISTING SOLUTIONS

The history of Automated Test Equipment was discussed in [2] and technology prediction was presented in [3]. The trends in ATE technology were brought out in [4] in detail. A detailed introduction to the next generation ATE systems is given in [5].
Various attempts have been made by researchers and industry in development of such automated systems in past for various complex systems like [6] [9] [7] [8] and they clearly brought out the advantages of using automated test equipment. These systems are mainly meant for electrical and electronic industry. There were attempts made by industry for, development of checkout and launch, systems for aerospace vehicles [10] [11].

These systems are not configurable systems. An architecture of a configurable ATE is presented in [12]. An open architecture for test systems was presented in [13].

The issues and challenges involved in development of Automated Test Equipment for such complex systems were discussed in [14] [15] [16] [17] [15].

However, these papers did not address the issues and challenges involved in developing a distributed configurable automated test equipment. In this paper we are presenting those issues and challenges involved in development of a distributed configurable checkout and launch system in this paper.

III. DISTRIBUTED ARCHITECTURE OF CONFIGURABLE CHECKOUT AND LAUNCH SYSTEM

The requirements of automated test equipment were discussed in depth in [18] [19]. Various architectures available in the literature were evaluated [20]. The following important parameters are motivating factors for design of Distributed Checkout and Launch System.

- System Reliability (Mission Criticality)
- Flexibility in connectivity and integration
- Modularity
- Maintainability
- Interface Management (Interface definition)
- Horizontal Scalability
- Configurable design

Operational requirements necessitate communication channels which work reliably over longer distances with low latency, low propagation delays, tolerable communication errors and low deployment time. The Design of the System caters for aforesaid requirements through connectivity of widely separated heterogeneous and hybrid digital Systems with digital interfaces (RS422, RS485, 1553B, Ethernet).

Launch Operations of Flight Vehicles (Missiles) can be carried out remotely. Each Node/Computer connected through Bus is an autonomous System with adequate internal computational /processing capability and local intelligence. This Architecture facilitates minimizing inter-dependence thereby providing high degree of independence and modularity as shown in fig. 1.

The above critical parameters have been implemented in the design of the CDCLS for Missile System.

IV. MODULAR AND CONFIGURABLE DESIGN

Design approaches for automated test equipment have been discussed in depth in the available literature [21] [22] [23]. Emphasis was given on modular and configurable design of the CDCLS. Modular design is useful from the point of view of testing and evaluation, expansion of system capabilities and maintenance.

The System design was configurable in nature so as to enable the system for configuring it for different users and for different variants of device under test through test settings and ATE configuration. Hardware and Software dependencies related to a particular type of System are localized within periphery of the System. There is dedicated software controlled configuration definition mechanism defining the Configuration of a particular System. The configurable design provides high level of flexibility to connect different types of systems in different configurations and the same system can be employed for several similar systems.

V. SYSTEM RELIABILITY

High degree of reliability is essential for the Mission critical System such as the system under discussion as they System operation involves man-in-loop. Various system reliability models and design for reliability were discussed in [24] [25]. Analysis of fault coverage to increase the system reliability was presented in [26]. Required System reliability was achieved by implementing fault tolerance at hardware level and software level as discussed in following subsections.

A. Hardware Fault Tolerance by Design

As part of criticality analysis of complete system, identification of hardware critical components is essential during design phase. Critical components may be related to risk, mission or safety. Mission critical system is required to be designed such that it continues functioning as intended even in presence of physical fault (hardware defect) and software design fault.

Therefore, comprehensive approach, to fault management, which includes fault avoidance, fault detection, fault removal and fault tolerance, is necessary to achieve required high system reliability. Based on criticality analysis of the system, critical hardware components should be provided with necessary hardware double or triple fault tolerance as shown in fig. 2.

Based on high criticality of hardware components, fault tolerance is incorporated by employing redundancy in hardware components. Based on criticality analysis of the system, critical hardware components should be provided with necessary hardware reliability. If single component does not meet reliability requirement, necessary fault tolerance needs to be incorporated.
Design Implementation of fault-tolerance for a mission critical hardware component viz. C1 with individual component hardware reliability of 0.80 and reliability objective (0.99) is determined as shown in fig. 2. The Reliability of 3Nos Components connected in parallel is computed as follows:

\[ R = 1 - (1 - R_{cl})^3 \]

Thus over all reliability is increased from 0.80 to 0.99. Mission critical parameters viz. System Flap Open/Closing Sense, Launcher Platform Elevation Angle, Onboard Battery voltages, Onboard Air bottle pressure, etc. were designed to have fault tolerance for achieving high performance.

B. Software Reliability by Design

The complexity of software, in general, is very high for mission critical distributed systems [27]. Various software architectures were studied [28] [29] and middleware were evaluated [30] [31] to arrive at a reliable software architecture for distributed [32] checkout and launch system application. Response time analysis was performed on the real-time task set [33].

High Software reliability is achieved by implementing fault tolerance for highly critical software components (Modules) through N-Version programming (i.e. multi-version software) and not through software redundancy. Software redundancy could be failure-prone, because, in such case, existing software fault would get propagated in redundant part also and hence this may lead to software failure with serious consequences.

N-Version software is achieved through independent generation of N2 functionally equivalent programs called versions by independent software development Groups.

C. Design of Fault Tolerant Application Software Communication Protocols

The communication protocol between the checkout and device under test need to be fault tolerant [34]. Due to long length and capacitive effect of data communication lines between CDSLS and Onboard Computer, data may get corrupted specially at higher bit-rate and hence byte command is not recognized as a valid command thereby affecting system performance and mission. Therefore, it is strongly proposed that command bytes and mission critical data be sent thrice in consecutive manner. Critical Commands/Data should be accepted based on 2/3 logic. Timeout based communication mechanism provides deterministic behavior of the Checkout and Launch System.

VI. DESIGN OF SOFTWARE RELIABILITY ASSESSMENT MODEL

Assessment and determination of CDCLS software reliability is very important. Software reliability model described in fig. 3 is used for knowing confidence level of software reliability.

VII. TESTING AND VALIDATION

The system is tested elaborately at various levels- unit level, integrated level and system level considering various failure scenarios [35] [38] after performing defect analysis [36].
The test configuration is given in fig. 4. Various levels of tests that were conducted to ensure required reliability of the system are discussed in the following subsections.

A. Testing and Validation of Software Reliability in Standalone Mode

Well-planned comprehensive testing and evaluation of the Software is the main mechanism to achieve high reliability. Software faults are design defects, which, are harder to visualize, classify, detect, and correct. Therefore, in order to minimize software defects (syntactic and semantic), System level Test and Validation Setup simulating all modes of operation, failure modes, interlocks, time critical system behavior, graceful degradation in operating environment plays decisive role for achieving high system reliability. All test scenarios were simulated through the test configuration shown in fig. 4. Special attention needs to be paid to testing of mission/flight critical software modules, logical paths, operating modes.

B. Integrated System Validation Testing

Final validation testing of the integrated system in final operational configuration for ensuring functional compliance is essential. Over-all performance of the System in integrated mode is evaluated and systems level performance issues are addressed for achieving high system performance. Maximum possible failure modes and system interlocks (safety, operational, etc.) are simulated [37].

VIII. CONCLUSION

In view of mission criticality of the System, design of highly reliable CDCLS for Flight Vehicles/Missiles is, indeed, challenging. The system was successfully tested and proven for test scenarios. The system could be effectively used for performing the checkout of various variants of device under test through configuration parameters, without modifying the system.

The design of the present CDCLS has achieved high level of maturity in terms of the important parameters like high reliability, modularity, usability, ease of reconfiguration, maintainability, expandability. Extremely successful performance has been achieved during Checkout and testing at Lab-level and Launch Missions at Field level.

REFERENCES

24. ML1-HDBK-189C.


AUTHORS PROFILE

Shivpal Singh, received his M.Tech in Computer Science from IIT, Madras. He has been working in DRDL as scientist for 30 years. His areas of defence research include design and development of mission critical checkout and launch control systems for aerospace flight vehicles, configurable distributed systems, reliability engineering, system simulators and automated test equipment. He has more than 20 publications in national/international conferences Mission critical systems received by him have been successfully inducted into service by armed forces. He has received DRDO path-breaking research award.

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