

A PID and PIDA Controller Design for an AVR System using Frequency Response Matching

Anand Kumar, Md Nishat Anwar

Abstract: A proportional integral derivative (PID) and proportional integral derivative acceleration (PIDA) controller have been designed for voltage regulation in power system. The controller (i.e. PID and PIDA) has been proposed via frequency response matching of desired reference model with that of system model transfer function. The proposed PID controller has been designed using one point frequency response matching as well as pole placement technique, while PIDA controller has been designed using two point frequency response matching by equating desired set-point closed loop reference model with that of closed loop transfer function of system model. The response of the proposed PIDA controller shows improved performance for automatic voltage regulator (AVR) system in comparison with recently available literature. The proposed PID and PIDA controllers provide fast and smooth response for an AVR system. The advantages associated with the PIDA controller for an AVR system is to reduce rise time, percentage overshoot and improved robustness, stability margin.

keywords: Frequency response matching, AVR, PID controller, PIDA controller, robustness, integral of square error (ISE).

I. INTRODUCTION

Automatic voltage regulator (AVR) system is required to maintain the terminal voltage and stability of power system through excitation control of synchronous generator [1]. The PIDA controller is used to provide fast and smooth response as well as more suitable for higher order plant, which is used to maintain voltage regulation and reactive power generation of generator and load within specified limits. Day-by-day complexity in power system is increasing, so that terminal voltage of an alternator along with reactive power is varying rapidly and sometimes it become unstable. A lots of controller has been proposed for AVR system via different control approach like fractional order (FO) PID (FOPID) [2–7], PIDA [8–10], direct synthesis (DS) technique [11], Fuzzy logic controller (FLC) [12–14], sliding mode control [15], artificial intelligence technique [16–20].

FOPID controller has been proposed based on particle swarm optimization (PSO) for voltage regulation of power system by M. Zamini et al. [2] and a novel performance criterion has been proposed to obtain FOPID controller gain parameter based on time domain and frequency domain. Tang et al. [3] presents FOPID controller based on chaotic ant swarm optimization technique for an AVR system.

Revised Manuscript Received on August 05, 2019

Anand Kumar, Department of Electrical Engineering, National Institute of technology, Patna-800005, India.

Md Nishat Anwar, Department of Electrical Engineering, National Institute of technology, Patna-800005, India.

Camacho and Mermoud [4] have been proposed a fractional order model reference adaptive controller (FOMRAC) design and considered genetic algorithm (GA) optimization technique for FOMRAC controller gain parameter in an AVR system. Das and Pan [5] have been proposed FOPID controller for voltage regulation of power system based on frequency domain design. Zeng et al. [6] has been designed FOPID controller using multi objective external optimization technique to obtain improved performance for an AVR system and considered integral of absolute error (IAE) as objective function in optimization technique to obtain PID controller gain parameter. Sikander et al. [7] has been proposed cuckoo search algorithm based on FOPID controller for AVR system and provides better accuracy and robustness of the system.

PIDA controller has been implemented in parallel form by Jung and Dorf [8] and controller gain parameters has been obtained by using location of root-locus. PIDA controller design has been proposed by Puangdownreong [9] based on a metaheuristic current search algorithm for an AVR system. Second order derivative PID controller design has been proposed for AVR system and its controller gain parameter has been obtained using PSO algorithm by Sahib [10]. PID controller design for an AVR system has been proposed by Anwar and Pan [11] based on direct synthesis approach.

Mukherjee and Ghoshal [12] present fuzzy PID controller design for an AVR system and the parameter of PID controller is optimized using intelligent particle swarm algorithm. Fuzzy P plus Fuzzy I plus Fuzzy D (FP+FI+FD) controller has been designed for an AVR system based on Fuzzy logic and its controller gain parameter is optimized using hybrid of GA and PSO (HGAPSO) algorithm by Shayeghi et al. [13]. A PID controller design has been developed for an AVR system based on a combined GA, radial basis function neural network (RBF-NN) and Sugeno fuzzy logic technique by Gizi et al. [14]. Ribeiro et al. [15] presents sliding mode control technique for an AVR and this technique is able to maintain stability and terminal voltage of synchronous generator.

Chaotic ant swarm (CAS) algorithm has been proposed to design PID controller for regulation of terminal voltage in synchronous generator by Zhu et al. [16]. Godze and Taplamacioglu [17] have been proposed optimal PID controller for voltage regulation of power system utilizing artificial bee colony optimization technique. The PID controller has been proposed by Hasanien [18] using Taguchi combined genetic algorithm technique for an AVR system. Optimal PID

A PID and PIDA Controller Design for an AVR System using Frequency Response Matching

controller has been designed for an AVR system by Chatterjee and Mukherjee [19] based on teaching learning optimization technique and robustness of controller is also analyzed. PID controller design has been presented for an AVR system based on a combination of ant colony optimization and Nelder-Mead approach by Blondin et al [20]. Maslo et al. [21] proposed generalized model for excitation systems and also reduced higher order model to lower order standard model.

PIDA controller is better alternative for AVR system to exiting controller design in this area. The parameter of PID and PIDA controller is easily obtained by tuning the pole of the desired closed loop transfer function (CLTF) and also eliminate the problem associated with higher order process. The major contribution of the paper work are as following.

- PID controller design for AVR system using frequency response matching with desired pole placement.
- PIDA controller has been proposed via two point frequency response matching for an AVR system.
- Analysis of gain margin (GM) and phase margin (PM) has been considered to ensure stability of AVR system.
- Maximum sensitivity and system parameter uncertainty has been analyzed to confirm the robustness of PID and PIDA controller.
- Analysis of Integral of square error (ISE) w.r.t time constant of desired closed loop transfer function for an AVR system.
- The comparative analysis of the proposed PID and PIDA controller has been compared with available standard literature.

The paper work is well organized in five section. In section 1 consist of introduction of AVR system along with literature survey and main contribution of the paper. Dynamic of AVR system is discussed in section 2. Section 3 well described the PID and PIDA controller design methodology for an AVR system, while simulation results and its comparative analysis is demonstrated in section 4. Finally, conclusions of present work is explain.

II. DYNAMICS OF AVR SYSTEM

The AVR is used to control terminal voltage of an alternator to a pre-specified value. It consist of mainly four components which are amplifier, exciter, generator and sensor, which is shown in Fig. 3. [1]. The transfer function of each parameters of AVR system taken from Anwar and Pan [11], which is shown in table 1.

The unity negative feedback control structure as shown in Fig. 1 has been considered to control the terminal voltage of synchronous generator. The PID and PIDA controller have been designed to regulate the terminal voltage within specified limit with the following expression.

$$K_C^{PID}(s) = k_p + \frac{k_i}{s} + k_d s \quad (1)$$

$$K_C^{PIDA}(s) = k_p + \frac{k_i}{s} + k_d s + k_a s^2 \quad (2)$$

Eq. 2. Shows PIDA controller is a third order system, which is similar as PID controller except it has one additional acceleration gain parameter (k_a). The PID and PIDA controller takes the error signal e , and produces controller output signal u , to maintain output of the plant y with reference input r , while LD is the load disturbance of plant. $K_C(s)$, $H_P(s)$ and $H_S(s)$ are the controller, plant and feedback transfer function, respectively. The major objective of the proposed controller is to reduce voltage regulation due to step load disturbance.

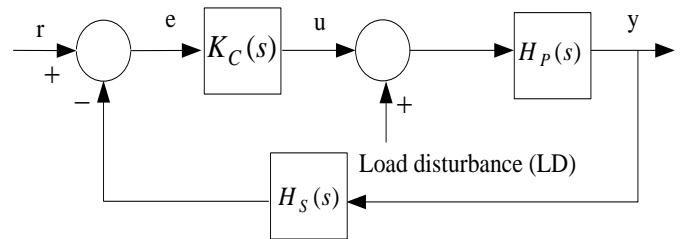
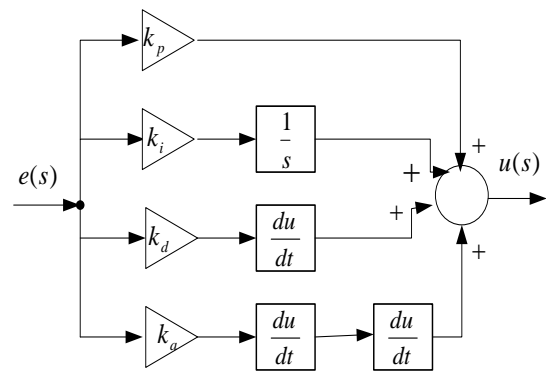
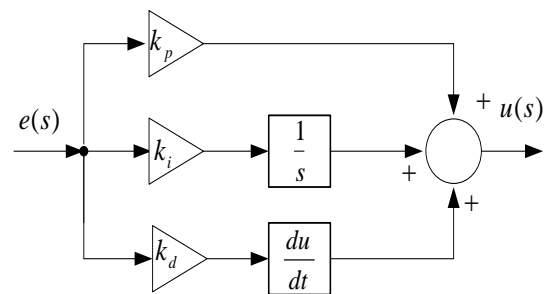


Figure 1. Unity Negative Feedback Control System



(a)PIDA controller



(b) PID controller

Figure 2. Structure of controller (a) PIDA (b) PID

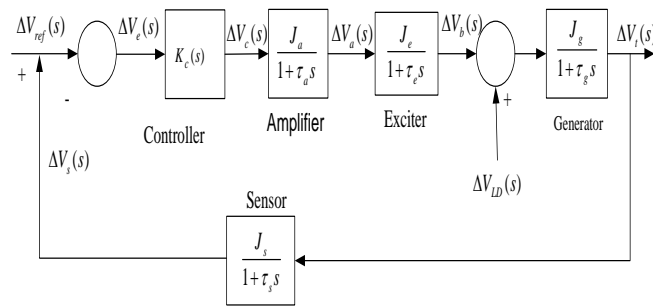


Figure 3. Schematic Diagram Of AVR System Model With Controller

Table 1. The AVR System Parameters

Component	Transfer function	Parameter limits
Amplifier	$H_A(s) = \frac{\Delta V_a(s)}{\Delta V_c(s)} = \frac{J_a}{1 + \tau_a s}$	$10 \leq J_a \leq 400$ $0.02 \leq \tau_a \leq 0.1s$
Exciter	$H_E(s) = \frac{\Delta V_b(s)}{\Delta V_a(s)} = \frac{J_e}{1 + \tau_e s}$	$1 \leq J_e \leq 10$ $0.4 \leq \tau_e \leq 1.0s$
Generator	$H_G(s) = \frac{\Delta V_t(s)}{\Delta V_b(s)} = \frac{J_g}{1 + \tau_g s}$	$1.0s \leq \tau_g \leq 2.0s$ J_e depend on load (0.7-1.0)
Sensor	$H_S(s) = \frac{\Delta V_s(s)}{\Delta V_t(s)} = \frac{J_s}{1 + \tau_s s}$	$J_s = 1$ $0.001s \leq \tau_s \leq 0.06s$

I. CONTROLLER DESIGN METHODOLOGY

The controllers $K_C(s)$ (PID & PIDA) have been designed using frequency response matching technique. In frequency response matching technique actual CLTF of the system model is approximated with desired CLTF at low frequency points. In that case, obtained controller is not physically realizable and need some suitable approximation to obtain controller like PI and PID form. The PID and PIDA controllers are used to improve stability and response of the terminal voltage of synchronous generator. The controller are able to reduce the settling time, maximum overshoot, rise time and improve steady state error.

A. PID controller design method:

Consider $H_{R,Y}(s)$ as the closed loop transfer function from $\Delta V_{ref}(s)$ to $\Delta V_t(s)$ is expressed as

$$H_{R,Y}(s) = \frac{\Delta V_t(s)}{\Delta V_{ref}(s)} = \frac{K_C(s)H_A(s)H_E(s)H_G(s)}{1 + K_C(s)H_A(s)H_E(s)H_G(s)H_S(s)} \quad (3)$$

The desired reference model $M_{R,Y}(s)$ for AVR system as given by

$$M_{R,Y}(s) = \frac{N_{R,Y}(s)}{D_{R,Y}(s)} = \frac{1}{(1 + \lambda s)^r} \quad (4)$$

Where $\frac{N_{R,Y}(s)}{D_{R,Y}(s)}$ is a transfer function in rational form, r

is the order of desired CLTF, the time constant λ is the tuning parameter for AVR system.

The characteristics equation of closed loop transfer function as in Eq. (3) may be written as

$$1 + K_C(s)H_A(s)H_E(s)H_G(s)H_S(s) = 0 \quad (5)$$

The controller parameter is designed in frequency response matching technique by equating the closed loop transfer function $H_{R,Y}(s)$ with that of desired closed loop transfer function $M_{R,Y}(s)$, which may be written as

$$H_{R,Y}(s) \cong M_{R,Y}(s) \quad (6)$$

The following aspects have been considered to achieve desired performance of AVR system using frequency response matching:

1. To achieve desired transient response of AVR system, the pole of the desired reference model at $s = -1/\lambda$ may be considered as the pole of the closed-loop system which gives the following characteristics equation.

$$1 + K_C(s)H_A(s)H_E(s)H_G(s)H_S(s) = 0 \text{ for } s = -1/\lambda \quad (7)$$

$$\text{Or, } K_C(s) = \frac{-1}{H_A(s)H_E(s)H_G(s)H_S(s)} \quad (8)$$

By using Eq. (1), Eq. (8) may be written as

$$k_p - \lambda k_i - \frac{k_d}{\lambda} = \left[-\frac{1}{H_A(s)H_E(s)H_G(s)H_S(s)} \right]_{s=-1/\lambda} = X \quad (9)$$

2. To obtain better steady state performance of the system by equating the frequency response of two system at very low frequency point (say $\omega = 0.001$ rad/s) which results in the following equation.

$$H_{R,Y}(j\omega) \cong M_{R,Y}(j\omega) \quad (10)$$

Eq. (10) may be written as



$$\frac{K_C(j\omega)H_A(j\omega)H_E(j\omega)H_G(j\omega)}{1 + K_C(j\omega)H_A(j\omega)H_E(j\omega)H_G(j\omega)H_S(j\omega)} \cong M_{R,Y}(j\omega) \quad (11)$$

The expression of the controller parameter is obtained by using Eq. (1) and (11) as given by

$$k_p + \frac{k_i}{j\omega} + k_d j\omega \cong \frac{1}{M_{R,Y}(j\omega)H_A(j\omega)H_E(j\omega)H_G(j\omega)} - \frac{H_S(j\omega)}{H_A(j\omega)H_E(j\omega)H_G(j\omega)} \quad (12)$$

Assuming

$$Z = \frac{1}{M_{R,Y}(j\omega)H_A(j\omega)H_E(j\omega)H_G(j\omega)} - \frac{H_S(j\omega)}{H_A(j\omega)H_E(j\omega)H_G(j\omega)},$$

the Eq. (12) may be written as

$$k_p + j(k_d\omega - \frac{k_i}{\omega}) = \text{Re}[Z] + j \text{Im}[Z] \quad (13)$$

By separating real and imaginary parts of Eq. (13) following two equations are obtained.

$$k_p = \text{Re}[Z] \quad (14)$$

$$k_d\omega + k_i(\frac{-1}{\omega}) = \text{Im}[Z] \quad (15)$$

The Eqns. (9), (14) and (15) can be rearranged in matrix form as given by

$$\begin{bmatrix} 1 & -\lambda & -1/\lambda \\ 1 & 0 & 0 \\ 0 & -1/\omega & \omega \end{bmatrix} \begin{bmatrix} k_p \\ k_i \\ k_d \end{bmatrix} = \begin{bmatrix} X \\ \text{Re}[Z] \\ \text{Im}[Z] \end{bmatrix} \quad (16)$$

The PID controller gain will be obtained by solving the Eq. (16).

B. PIDA Controller design method

The PIDA controller has been designed using frequency response matching and the desired reference model transfer function $M_{R,Y}(s)$ is considered as same as in Eq. (4). The controller is designed by equating the closed-loop set-point response transfer function with that of the desired reference model and it is expressed in mathematical form as

$$H_{R,Y}(s) = M_{R,Y}(s) \quad (17)$$

The expression for the controller parameter may be obtained from Eq. (17) as given by

$$K_C(s) = \frac{1}{M_{R,Y}(s)H_A(s)H_E(s)H_G(s)} - \frac{H_S(s)}{H_A(s)H_E(s)H_G(s)} \quad (18)$$

The controller $K_C^{PIDA}(s)$ as in Eq. (2) is approximated with direct synthesis (DS) controller $K_C(s)$ as in Eq. (18) using the low frequency point matching method proposed by Anwar and Pan [11] and In brief, the method is discussed here. The controller $K_C^{PIDA}(s)$ and $K_C(s)$ is approximated at very low frequency point (say $\omega_0 = 0.001$ rad/s and $\omega_1 = 0.002$ rad/s) and it may be mathematically written as

$$K_C^{PIDA}(s)|_{s=j\omega} \cong K_C(s)|_{s=j\omega} \quad (19)$$

Eq. (19) is elaborated by using Eq. (2) and (18), which is written as

$$k_p + \frac{k_i}{j\omega} + k_d j\omega + k_a(j\omega)^2 \cong \frac{1}{M_{R,Y}(j\omega)H_A(j\omega)H_E(j\omega)H_G(j\omega)} - \frac{H_S(j\omega)}{H_A(j\omega)H_E(j\omega)H_G(j\omega)} \quad (20)$$

Assuming

$$Z = \frac{1}{M_{R,Y}(j\omega)H_A(j\omega)H_E(j\omega)H_G(j\omega)} - \frac{H_S(j\omega)}{H_A(j\omega)H_E(j\omega)H_G(j\omega)},$$

the Eq. (20) may be written as

$$(k_p - k_a\omega^2) + j(k_d\omega - \frac{k_i}{\omega}) \cong \text{Re}[Z] + j \text{Im}[Z] \quad (21)$$

By separating real and imaginary parts of Eq. (21) the following two equations are obtained.

$$k_p - k_a\omega^2 = \text{Re}[Z] \quad (22)$$

$$k_d\omega + k_i(\frac{-1}{\omega}) = \text{Im}[Z] \quad (23)$$

With the two low-frequency points (ω_0 and ω_1), four equations are obtained using Eq. (22) and (23) as given below.

$$\begin{bmatrix} 1 & 0 & 0 & -\omega_0^2 \\ 0 & -1/\omega_0 & \omega_0 & 0 \\ 1 & 0 & 0 & -\omega_1^2 \\ 0 & -1/\omega_1 & \omega_1 & 0 \end{bmatrix} \begin{bmatrix} k_p \\ k_i \\ k_d \\ k_a \end{bmatrix} = \begin{bmatrix} \text{Re}[Z_0] \\ \text{Im}[Z_0] \\ \text{Re}[Z_1] \\ \text{Im}[Z_1] \end{bmatrix} \quad (24)$$

The solution of Eq. (24) will yield the parameter of the PIDA controller.

II. SIMULATION RESULTS AND DISCUSSIONS

Case study 1. An AVR system parameters taken from [11] as given by:

$$J_a = 10, \tau_a = 0.1, J_e = 1, \tau_e = 0.4, J_g = 1, \tau_g = 1.0, J_s = 1, \tau_s = 0.01.$$

The terminal voltage unit step response of an AVR system without controller for case study 1 is as shown in Fig. 4 and observe that the percent overshoot (M_p) is 50.6 %, the settling time (t_s) is 6.98 s, and steady state error (e_s) is 9.09 %.

The reference model has been considered for an AVR system at $\lambda=0.2$ and $r=1$. The proposed PID and PIDA controller has been obtained as

$$K_C^{PID}(s) = 0.714 + \frac{0.476}{s} + 0.257s \quad \text{and} \\ K_C^{PIDA}(s) = 0.714 + \frac{0.476}{s} + 0.257s + 0.019s^2, \text{ respectively.}$$

A test signal step input is applied at $t=0$ sec, while load disturbance step input is applied at $t=5$ sec in the AVR system for case study 1. Fig. 5 shows that the ISE of an AVR system w.r.t time constant of desired closed loop transfer function. Fig. 6 demonstrate that the terminal voltage response for case study 1 with proposed PIDA controller is much better than the blondin et al. [20] and Anwar and Pan [11]. To estimate the performance of proposed controller in terms of maximum sensitivity ($M_s = \max_{0 \leq \omega \leq \infty} |1/[1+k(j\omega)p(j\omega)]|$), percent overshoot (M_p), and settling time (t_s), the integral of square error (ISE), GM and PM are considered.

The proposed PIDA controller has no overshoot and stability margin is very high than others design method as observe from Fig. 6 and table 2.

To check the robustness of controller, maximum sensitivity and uncertainty in system parameter is considered. For case study 1 with +50% variation in J_g and τ_g are considered and obtained simulation response for terminal voltage as shown in Fig. 8 and table 3 shows that the proposed PIDA has better performance in comparison with blondin et al. [20] and Anwar and Pan [11].

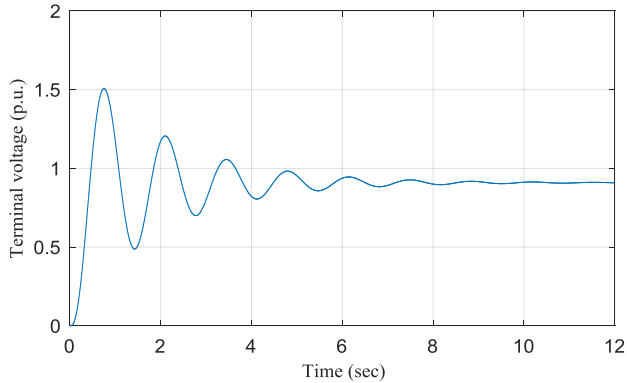


Figure 4. Generator Terminal Voltage Of An AVR System Without A Controller

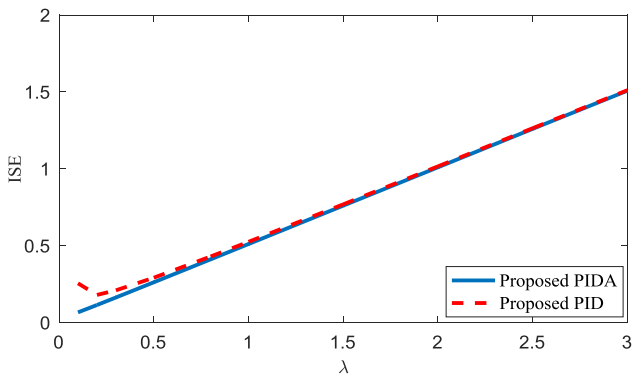


Figure 5. ISE Versus Lambda In Case Study 1

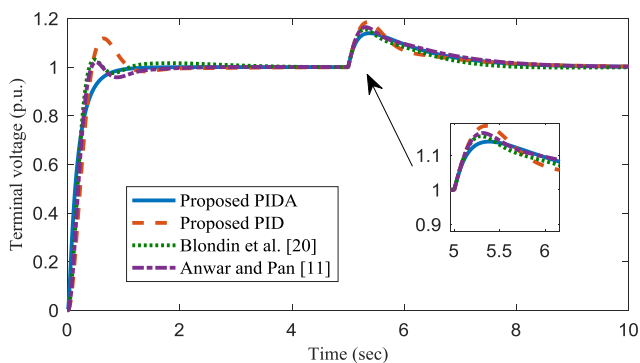


Figure 6. Terminal Voltage Of Generator For Case Study 1

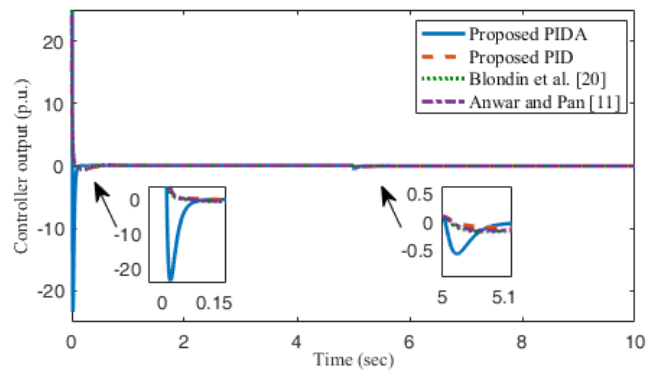


Figure 7. Controller Output For Case Study 1

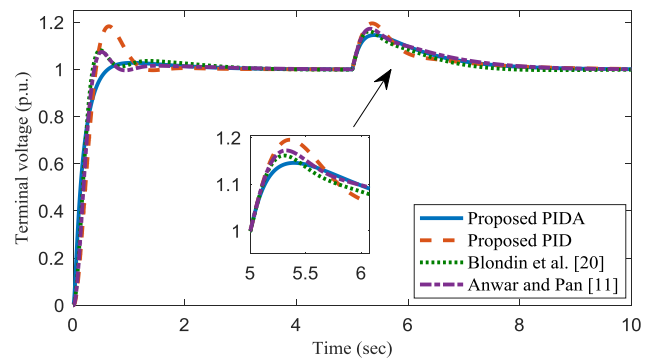


Figure 8. Terminal Voltage Of Generator For Case Study 1 With +50% Variations In J_g And τ_g

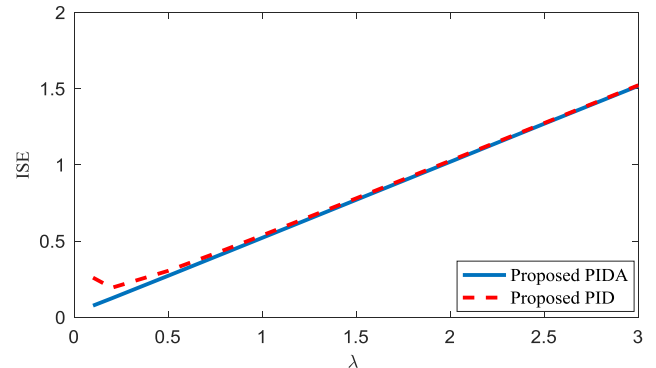


Figure 9. ISE Versus Lambda In Case Study 2

Case study 2. An AVR system parameters taken from [11] as given below:

$$J_a = 12, \tau_a = 0.09, J_e = 10, \tau_e = 0.5, J_g = 0.1, \tau_g = 1.0, J_s = 1, \tau_s = 0.02.$$

The proposed PID controller has been designed with desired reference model at $\lambda=0.2$ and $r=1$ is $K_C^{PID}(s) = 0.640 + \frac{0.378}{s} + 0.168s$. The proposed PIDA controller has been designed with desired reference model at $\lambda=0.2$ and $r=1$ is $K_C^{PIDA}(s) = 1.175 + \frac{0.694}{s} + 0.485s + 0.036s^2$. The performance of AVR system is shown in Figs. 10, 11 and table 4 due step input test signal apply at $t=0$ sec and step disturbance test signal apply at 3 sec.

A PID and PIDA Controller Design for an AVR System using Frequency Response Matching

The proposed PIDA shows much improved performance than the proposed PID, Anwar and Pan [11] and Zhu [16] in terms of set-point (percent overshoot, settling time), load–disturbance (percent overshoot, settling time), ISE, GM and PM for nominal AVR system in case study 2.

To emphasize the robustness of proposed PID and PIDA controller, maximum sensitivity and +50% variation in J_g and τ_g are considered. From Fig. 12 and table 5 observe that the terminal voltage of perturbed AVR system is stable and provide better performance than proposed PID, Anwar and Pan [11], Zhu [16].

III. CONCLUSION

In this paper, the PID and PIDA controllers have been proposed for an AVR system using frequency response matching. The maximum sensitivity, ISE, t_s , %OS, GM, PM has been considered to demonstrate the performance of the proposed controller. The proposed PIDA controller results show much better than the prevalent design methods.

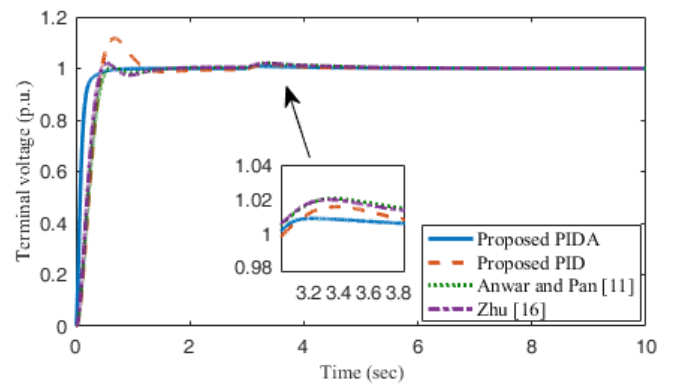


Figure 10. Terminal Voltage Of Generator For Case Study 2

Table 2. Comparative Analysis For Case Study 1

Method	K_p	K_i	K_d	K_a	M_s	Set point response		GM	PM	Load–disturbance response		ISE
						$M_p(\%)$	$t_s(s)$			$M_p(\%)$	$t_s(s)$	
Proposed PIDA	0.714	0.476	0.257	0.019	1.03	0	0.8	Inf	87.2	13.9	2.58	0.132
Proposed PID	0.714	0.476	0.161	-	1.35	11.7	1	27.8	57.4	18.4	2.54	0.200
Blondin et al. [20]	0.673	0.595	0.262	-	1.33	3.06	0.91	25.4	66.9	15.5	2.07	0.158
Anwar and Pan [11]	0.652	0.434	0.236	-	1.32	2.0	1.22	26.1	67.7	16.4	2.80	0.173

Table 3. Comparative analysis for case study 1 with +50% variation in K_g and T_g

Method	K_p	K_i	K_d	K_a	M_s	Set point response		GM	PM	Load–disturbance response		ISE
						$M_p(\%)$	$t_s(s)$			$M_p(\%)$	$t_s(s)$	
Proposed PIDA	0.714	0.476	0.257	0.019	1.03	2.82	1.63	Inf	87.2	14.6	2.49	0.130
Proposed PID	0.714	0.476	0.161	-	1.35	18.32	1.14	27.8	57.4	19.5	2.46	0.206
Blondin et al. [20]	0.673	0.595	0.262	-	1.33	8.2	2.28	25.4	66.9	16.1	2.00	0.159
Anwar and Pan [11]	0.652	0.434	0.236	-	1.32	7.4	0.73	26.1	67.7	17.2	2.7	0.173

Table 4. Comparison performance for case study 2

Method	K_p	K_i	K_d	K_a	M_s	Set point response		GM	PM	Load–disturbance response	ISE
						$M_p(\%)$	$t_s(s)$				

Proposed PIDA	1.175	0.694	0.485	0.036	1.10	0	0.4	Inf	81.9	0.8	0	0.076
Proposed PID	0.640	0.378	0.168	-	1.35	11.7	1.1	23.5	57.7	1.55	0	0.193
Anwar and Pan [11]	0.509	0.330	0.200	-	1.29	0.4	0.51	23.3	70.1	2.0	1.4	0.183
Zhu [16]	0.561	0.367	0.232	-	1.32	2	1.1	22.1	68.2	1.97	1.4	0.167

Table 5. Comparison Performance For Case Study 2 With +50% Variation In K_G And T_G

Method	K_p	K_i	K_d	K_a	M_s	Set point response		GM	PM	Load-disturbance response		ISE
						$M_p(\%)$	$t_s(s)$			$M_p(\%)$	$t_s(s)$	
						Proposed PIDA	1.175			0.694	0.485	
Proposed PID	0.640	0.378	0.168	-	1.35	18.1	1.26	23.5	57.7	2.0	0.45	0.198
Anwar and Pan [11]	0.509	0.330	0.200	-	1.29	6.2	2.15	23.3	70.1	2.5	0.70	0.181
Zhu [16]	0.561	0.367	0.232	-	1.32	7.4	2	22.1	68.2	2.4	0.55	0.166

REFERENCES

- Kundur P. power system stability and control., (1994)
- Zamani, M.; Karimi-ghartemani, M.; Sadati, N.; Parniani, M. Design of a fractional order PID controller for an AVR using particle swarm optimization. 2009, 17, 1380–1387. doi:10.1016/j.conengprac.2009.07.005
- Tang, Y.; Cui, M.; Hua, C.; Li, L.; Yang, Y. Optimum design of fractional order PID controller for AVR system using chaotic ant swarm. Expert Syst. Appl., 2012, 39, 6887–6896. doi:10.1016/j.eswa.2012.01.007
- Aguila-Camacho, N.; Duarte-Mermoud, M.A. Fractional adaptive control for an automatic voltage regulator. ISA Trans., (2013) 52, 807–815. doi:10.1016/j.isatra.2013.06.005.
- Das, S.; Pan, I. On the Mixed Loop-Shaping Tradeoffs in Fractional-Order Control of the AVR System., (2014), 10, 1982–1991.
- Zeng, G.Q.; Chen, J.; Dai, Y.X.; Li, L.M.; Zheng, C.W.; Chen, M.R. Design of fractional order PID controller for automatic regulator voltage system based on multi-objective extremal optimization. Neurocomputing., 2015, 160, 173–184. doi:10.1016/j.neucom.2015.02.051.
- Sikander, A.; Thakur, P.; Bansal, R.C.; Rajasekar, S. A novel technique to design cuckoo search based FOPID controller for AVR in power systems. Comput. Electr. Eng., 2017, 0, 1–14. doi:10.1016/j.compeleceng.2017.07.005
- Jung, S.; Dorf, R.C. Analytic PIDA Controller Design Technique for A Third Order. IFAC Proc. 1996, Vol. 29, 2–7. doi:10.1016/S1474-6670(17)57819-2.
- Puangdownreong, D. Application of Current Search to Optimum PIDA Controller Design. Intell. Control Autom., 2012, 03, 303–312. doi:10.4236/ica.2012.34035.
- Sahib, M.A. A novel optimal PID plus second order derivative controller for AVR system., 2015, Eng. Sci. Technol. an Int. J. 18, 194–206. doi:10.1016/j.jestech.2014.11.006.
- Anwar, M.N.; Pan, S. A Frequency Domain Design of PID Controller for an AVR System., 2014, J. Zhejiang Univ. C (Computers Electron. 15, 293–299. doi:10.1631/jzus.C1300218.
- Mukherjee, V.; Ghoshal, S.P. Intelligent particle swarm optimized fuzzy PID controller for AVR system., 2007, 77, 1689–1698. doi:10.1016/j.ejpr.2006.12.004.
- Shayeghi, H.; Younesi, A.; Hashemi, Y. Optimal design of a robust discrete parallel FP + FI + FD controller for the Automatic Voltage Regulator system., 2015, Int. J. Electr. Power Energy Syst. 67, 66–75. doi:10.1016/j.ijepes.2014.11.013.
- Al, A.J.H.; Mustafa, M.W.; Al-geelani, N.A.; Alsaedi, M.A. Sugeno fuzzy PID tuning, by genetic-neutral for AVR in electrical power generation., 2015, 28, 226–236.
- Ribeiro, R.L.A.; Neto, C.M.S.; Costa, F.B.; Rocha, T.O.A.; Barreto, R.L. A sliding-mode voltage regulator for salient pole synchronous generator. 2015, 129, 178–184.
- Zhu, H.; Li, L.; Zhao, Y.; Guo, Y.; Yang, Y. CAS algorithm-based optimum design of PID controller in AVR system., 2009, Chaos, Solitons

and Fractals. 42, 792–800. doi:10.1016/j.chaos.2009.02.006.

- Gozde, H.; Taplamacioglu, M.C. Comparative performance analysis of artificial bee colony algorithm for automatic voltage regulator (AVR) system., 2011, J. Franklin Inst. 348, 1927–1946. doi:10.1016/j.jfranklin.2011.05.012.
- Hasanien, H.M.; Member, S. Design Optimization of PID Controller in Automatic Voltage Regulator System Using Taguchi Combined Genetic Algorithm Method., 2013, 7, 825–831.
- Chatterjee, S.; Mukherjee, V. PID controller for automatic voltage regulator using teaching-learning based optimization technique., 2016, Int. J. Electr. Power Energy Syst. 77, 418–429. doi:10.1016/j.ijepes.2015.11.010.
- Blondin, M.J.; Sanchis, J.; Sicard, P.; Herrero, J.M. New optimal controller tuning method for an AVR system using a simplified Ant Colony Optimization with a new constrained Nelder–Mead algorithm., 2018 Appl. Soft Comput. 62, 216–229. doi:10.1016/j.asoc.2017.10.007.
- Máslo, K.; Kasembe, A.; Kolcun, M. Simplification and unification of IEEE standard models for excitation systems., 2016, Electr. Power Syst. Res. 140, 132–138. doi:10.1016/j.ejpr.2016.06.030.

AUTHORS PROFILE



international conferences.

ANAND KUMAR: He completed Bachelor of Engineering (B.E) degree in Electrical and Electronic Engineering from OIST, Bhopal, India, in 2013. Presently pursuing M.Tech-PhD dual degree program from NIT, Patna since 2015 in Department of Electrical Engineering. His area of interest includes linear control theory, power system stability control, PID controller, PIDA controller. List of publication includes 2



international conferences and 5 papers in national conferences.

MD NISHAT ANWAR: He received B.Tech and M.Tech degree in Electrical Engineering from AMU, Aligarh, India. He received Ph.D. from IIT (ISM), Dhanbad, India in 2015. He has 5 years teaching experience and presently working as an Assistant Professor in the Department of Electrical Engineering, NIT Patna, India. His area of interest includes advanced control system, digital control system, PID controller. he guided several M.tech scholars and 5 PhD scholars working under him. His publication includes 5 papers in reputed journals, 14 papers in international conferences and 5 papers in national conferences.