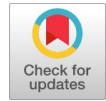


Simulation and Performance Enhancement of High Efficiency Video Coding Standard for Video Compression



Kavita Monpara, Bhavin Sedani, Ved Vyas Dwivedi

Abstract: In the era of modern communication, the transmission of video is the most demanded feature and that makes the bandwidth issues crucial. The only solution to fight with is the video compression techniques/ standards. The High efficiency video coding standard (H.265/ HEVC) is newly evolved standard that is popularly used. This standard is better in saving bandwidth giving more compression. This research work deals with narrates the steps of implementation, simulation with MATLAB and the results obtained. From the obtained results, the 4G technique for wireless communication has been obtained in an enhanced way from the compression perspective.

Keywords : Entropy Coding, HEVC, Intra picture prediction; Inter picture prediction; In loop filtering, Integer transform and Quantization, Prediction mode, Video Compression.

I. INTRODUCTION

The high efficiency video coding standard- HEVC offers a similar fundamental incentive about multiplying compression ability like the previous standard h.264/AVC. A video can be compress about twice using the new standard HEVC as compared to previous standard h.264/AVC without yielding quality, on the other hand, be utilized to empower delivery of higher resolution and picture rates—or different types of higher quality, for example, a higher power range or higher exactness for improved color quality. It additionally comes at some other point when new video services are developing—for UHDTV, more power range, and more extensive color scale.

II. HIGH EFFICIENCY VIDEO CODING STANDARD OVERVIEW

The High Efficiency Video Coding (HEVC) standard defines particular steps with the guideline of square-based hybrid video coding. According to that guideline, a picture is first divided into squares and afterward, each square is anticipated by utilizing either intra-picture or inter-picture prediction. A decoded samples inside a similar

picture are utilized in a previous coding standard, while a dislodged squares of decoded pictures are utilized in a new standard as a kind of perspective. The inter-picture prediction commonly makes up a motion vector for the motion between frames of a video sequence. The prediction would be done using motion between two frames which is illustrated as the motion-compensated prediction. While the spatial redundancy of consecutive squares of an image can be removed using the intra-picture prediction. In both prediction the subsequent prediction error is shaped by calculating the difference between the original square and its forecast, which is forwarded using transform coding which is responsible to remove spatial redundancy but inside a square and furthermore a linear transform, scalar quantization of the transform coefficients and entropy coding of the subsequent transform are applied to individual square.

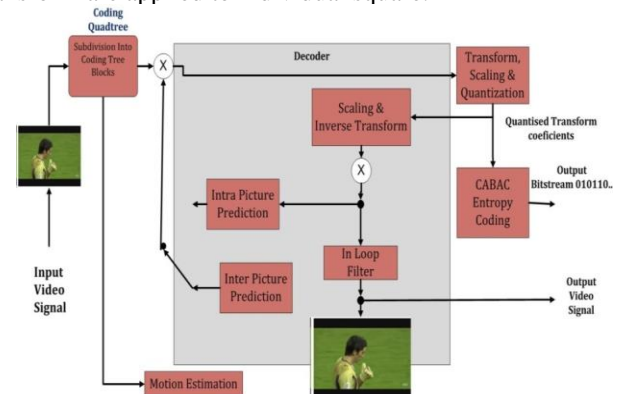


Fig. 1. High efficiency video coding standard-A general block diagram

A. Square Partitioning for Prediction

Consider that the transform squares select indistinguishable to the comparing coding squares are not expressly set apart in the figure 2.

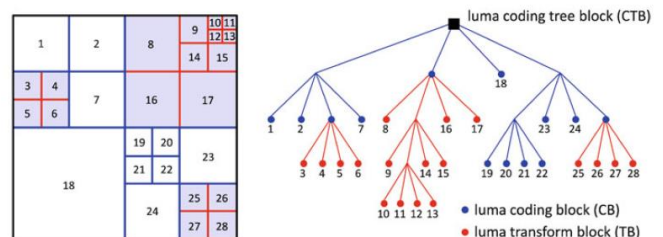


Fig. 2. Square partitioning

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A case for the dividing of a 64 x 64 luma coding tree block (black) into coding squares (blue) and transform squares (red). In the portrait on the right, the blue lines demonstrate the relating coding tree with the coding tree square at its root and the coding squares (blue circles) at its leaf hubs, the red lines demonstrate the non-declined leftover quad trees with the transform squares (red circles) as leaf hubs. The numbers demonstrate the coding request of the transform squares.

B. Intra Picture Prediction

In the HEVC, the intra prediction strategies grouped in two types. The primary classification is angular prediction structure which gives the codec a likelihood to precisely show structures with directional borders. The planar prediction and DC prediction, a second classification, provide indicators predicting flat picture content. The HEVC supports total intra prediction modes is 35, the complete distribution of modes are summarize in table 1. The adjacent reproduced squares are used as a reference samples by all intra prediction modes. The intra prediction is worked at the prediction of square sizes 4 x 4 to 32 x 32 samples.

Table- I: Distribution of Intra prediction modes

Intra Prediction mode	Mode name
0	PLANAR
2	DC
2.....34	ANGULAR

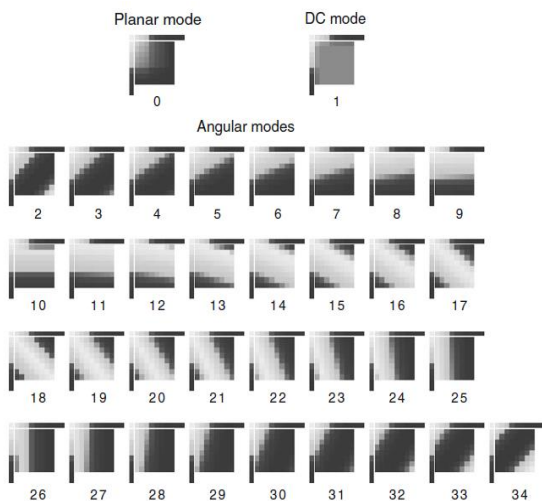


Fig. 3. Intra prediction different modes of 8 x 8 luma squares

Figure 3 shows the typical attributes of the diverse intra prediction modes. The 35 HEVC Intra expectations modes are specified in the figure which elaborates to the final prediction when utilizing reference tests. The modes 10 and 26 are used for the DC prediction and angular prediction which impacts the post-handling prediction with square boundaries incorporate apart from the adjacent sample values.

C. Inter Picture Prediction

In the HEVC, the inter picture prediction can be viewed as a constant refinement furthermore, consideration of all parts familiar from past standards of video coding. An inter

prediction square combining strategy altogether rearranged the square wise movement information flagging by inducing all motion information from as of now decoded squares.

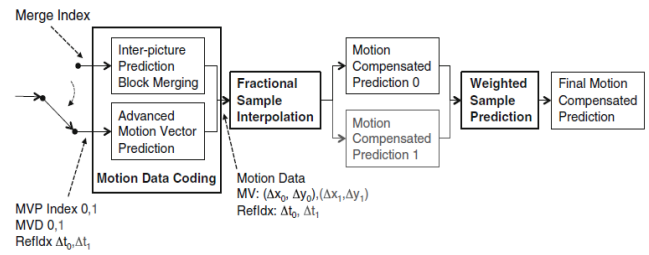


Fig. 4. Functional representation of Inter picture prediction

An outline functional representation of the HEVC inter-picture prediction appears in figure 4. The movement information of a square is related to the adjacent squares. The motion information is not straightforward converted into a bitstream, however, with prediction coded information is dependent on adjacent motion information. The different two ideas are utilized in HEVC for this inter-picture prediction. The different tool, advanced motion vector prediction (AMVP) which is used to upgrade the predictive coding of the motion vectors in HEVC, where the best predictor is selected and it is assigned to a decoder for each motion square. Also, another strategy in which inter-prediction squares combines and concludes that in H.264/AVC, the direct and angular modes are replaced by the adjacent motion information of square.

D. Integer transform and Quantization

HEVC indicates an integer transforms with two dimensions which are having different sizes from 4 x 4 to 32 x 32 that are given accurate approximations by using the discrete cosine transform (DCT). The onward alternate integer transform which is applied to residual blocks is illustrated in the below matrix.

$$A_4 = \begin{bmatrix} 29 & 55 & 74 & 84 \\ 74 & 74 & 0 & -74 \\ 84 & -29 & -74 & 55 \\ 55 & -84 & 74 & -29 \end{bmatrix}$$

The inverse transform matrix is A_4^T . Elements a_{ij} of the alternate transform matrix A_4 are the discrete sine transform with fixed point representation, which is obtained by the following equation.

$$a_{ij} = \text{round} \left\{ 128 * \frac{2}{\sqrt{2N+1}} \sin \left(\frac{(2i+1)(j+1)\pi}{2N+1} \right) \right\}$$

where $i, j = 0, \dots, N-1$,

In the intra picture prediction, the alternate integer transform gives approximately 1% reduction in bit rate, in which a square is anticipated from left and additionally first adjacent samples. Quantization comprises of division by a quantization step size (Qstep) and consequent adjusting and the multiplication by the quantization step size is carried out in inverse quantization.

In HEVC, the quantization step size is decided by a quantization parameter(QP) which is as similar as a quantization parameter in H.264/AVC. For the 8-bit video process, the value of QP is between 0 and 51. The subsequent relation among QP and the equal quantization step size for an orthonormal transform is currently defined in the below equation.

$$Qstep(QP) = (2^{1/6})^{QP-4}$$

E. Integer transform and Quantization

There are two in loop filters present in HEVC standard, one is a deblocking filter and the next one is a sample adaptive offset (SAO). In the encoding and decoding process, the in-loop filters are connected between inverse quantization and the decoded picture buffer. The first implemented filter is the deblocking filter, which reduces the lack of coherence at the prediction and transforms square ends. A next in-loop filter is the sample adaptive offset (SAO), which is connected to the next to the deblocking filter and enhance the quality of the output picture of decoder, while SAO primarily reduces the effect of ringing artifacts originated by integer transform and replaces the sample offset values which is generated by coding of motion vectors. The main objective of the in-loop filter is to improve the subjective quality of reconstructed pictures. Additionally, the presence of in-loop filters in decoder improves reference pictures quality and therefore the efficiency of compression.

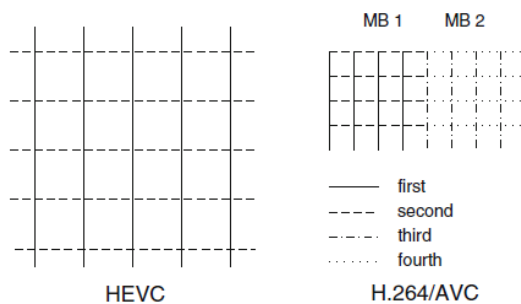


Fig. 5.Comparison of the square boundary

With compared to previous standard H.264, the process steps are different in HEVC deblocking filtering, the filter process is first carried out on the vertical boundaries of a picture and after then on the horizontal boundaries. The main element is macroblock in h.264,so in deblocking filter in which the four vertical boundaries in an MB are handled successively beginning from the furthest left MB boundary, and afterward, the four horizontal MB boundaries are prepared beginning from the top-most MB boundary. The comparison of handling the process of filtering in square boundary in HEVC and H.264/AVC is illustrates in figure 5. It can be seen that the deblocking filter in HEVC is more consistent than that in H.264/AVC.

F. Entropy Coding

In the HEVC standard - which is a novel approach, entropy coding concept is used which was not there in AVC standard. the combination is known as CABAC - Context-based Adaptive Binary Arithmetic Coding. For making this latest standard a better one - there are certain limitations which must be removed - mainly related to the

throughput while maintaining a fair amount of coding efficiency along-with obtained value of through-put, i.e., kind of managing the trade-off. CABAC - the related entropy code manages to maintain the trade-off. There are three basic tasks in entropy coding of HEVC in decoder: binarization, context modeling, and arithmetic coding.

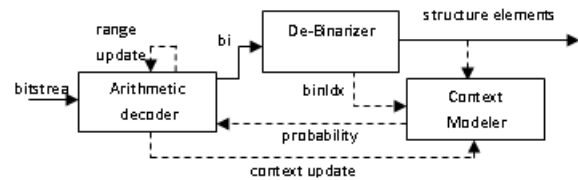


Fig. 6.Comparison of the square boundary

The functional block diagram of entropy coding is illustrates in figure 6, in which the dashed lines describes feedback loops.

III. SIMULATION RESULTS

The simulation results for compression of monochrome video using HEVC standard is carried out in MATLAB. The partitions in individual frame is carried out based on coding tree structures and there are total 35 intra picture prediction modes are used based on the content of individual frame. The simulation was carried out by DST integer transform with quantization parameter 27 are used to implement compression. The golomb code, arithmetic code are used in entropy coding. The compression results for monochrome video for HEVC standard for the frame is shown in Table 2.

Table- II: Simulation Results for monochrome video frames

Frame	Size of Frame (before compression) (KB)	Size of frame (after compression) (KB)	compression Rate (Space saving)
I	25.34	4.37	82.75%
P1	25.00	2.03	91.88%
B1	25.56	2.25	91.19%
P2	25.34	1.20	95.26%
P3	25.33	1.00	96.05%
P4	35.71	1.55	95.65%
B2	25.01	1.89	92.44%
P5	25.05	1.65	93.41%
P6	25.67	1.10	95.71%

The table - 2 gives the simulation results for a monochrome video which has been compressed using HEVC. The results obtained here shows that I-frames, when they are compressed with this method, they undergo compression with the rate of compression ranging from 86 to 83%.

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It can be seen that the frames which are P-frames, when they are compressed with this method, they almost undergo a very higher level of compression with the rate of compression more than 94% and frames which are B-frames when subjected to compression with this method, they are likely to undergo a very higher level of compression with the rate of compression above 90% to 91%. the B frames depend on the motion content of the video signal for the backward process.

The graph is shown below also expresses the above analytical detail regarding the size of actual frame and compressed frame.

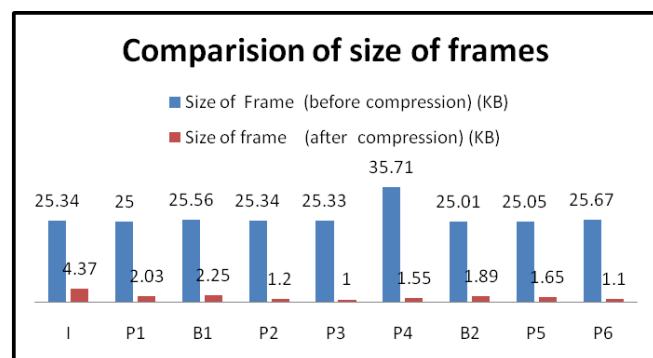


Fig. 7. Comparison of of size of frames

The actual image-size around 25Kb, the partitioned frame and the compressed image-size around 2 Kb are shown in figure 8 for two different frames. The middle picture of the results defines the partitions based on the coding tree structures, the frame first divided with 64 x 64 pixels and then according to the information content in frame it is further divided and converted into prediction square.

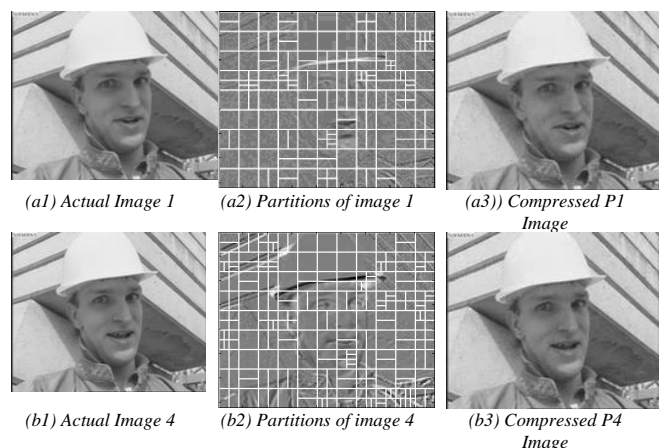


Fig. 8. The simulation results

IV. CONCLUSION

The main focus of proposed work is the implementation of HEVC compression standard on monochrome video by simulation and with the HEVC/H.265 standard the compression rate achieved is approximately 90 to 96%. So the space saving is huge and for the same, no quality compromise has been done. The performance of the proposed work is evaluated using the DST integer transform and quantization parameter 27. With this high compression rate, the HEVC standard is suitable to be used for 4G wireless

networks for HD and ultra HD video transmission.

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REFERENCES

1. "High Efficiency Video Coding (HEVC)", Springer nature, 2014.
2. Bhavin Sedani, Dipesh Kamdar, Kavita Monpara, "A novel Approach of video compression over 4g system using H.265 standard", 2017 ICICI.
3. A A Muhi, M R Pickering, M R Frater and J F Arnold "Video Coding using Elastic Motion Model and Larger Blocks," IEEE Trans. Circ. And Syst. for Video Technology, vol. 20, no. 5, pp. 661-672, 2010
4. Lee, Jung Won Kang, Jinho Lee, Jin Soo Choi, Jinwoong Kim, and Donggyu Sim "Scalable Extension of HEVC for Flexible High-Quality Digital Video Content Services" ETRI Journal 2013
5. Recommendation ITU-T H.265: High Efficiency Video Coding. Tech. rep., ITU-T (2013).
6. J. Ohm et al., "Comparison of the Coding Efficiency of Video Coding Standards—Including High Efficiency Video Coding (HEVC)," IEEE Transactions on Circuits and Systems for Video Technology, 2012
7. E. M. El-Bakary, W. El-Shafai, S. El-Rabaie, O. Zahran, F. E. Abd El-Samie. "Efficient hybrid framework for transmission enhancement of composite 3D H.264 and H.265 compressed video frames", Multimedia Tools and Applications, 2018
8. V. Sze, M. Budagavi, G. J. Sullivan (Editors), "High Efficiency Video Coding (HEVC): Algorithms and Architectures," Springer, 2014
9. Gary J. Sullivan, Jens-Rainer Ohm, Woo-Jin Han and Thomas Wiegand "Overview of the High Efficiency Video Coding (HEVC) Standard" IEEE transaction on circuits and systems for video technology, VOL. 22, NO. 12, December 2012

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