

Simulation of Interleaved Flyback Converter with Incremental Conductance MPPT for Solar PV Array using MATLAB/SIMULINK

Jebitha J, R G Nirmala

Abstract: The simulation of an interleaved flyback converter with solar system as input source is discussed in this paper. The interleaved flyback converter topology is adopted to prevent the generation of excess heat in devices, to avoid large voltage ripple in the load and also to minimise the size of the filtering elements. The control of the converter circuit is performed using Maximum Power Point Tracking (MPPT) controllers to get maximum power from the solar panel. In Maximum Power Point Tracking we used incremental conductance algorithm. This method is widely implemented because it has the higher steady-state accuracy and environmental adaptability. Simulation was performed with different duty cycle to study the output voltage variations thus the results obtained can be analysed. The design has been tested through simulation in MATLAB/SIMULINK model.

Keywords: Incremental Conductance (InC) algorithm, Maximum Power Point Tracking (MPPT).

I. INTRODUCTION

Nowadays Solar Energy have caught the attention of many as One of the most important sources of renewable energy and it is through harnessing this energy that we are meeting some of our energy demands. We know that recently non-renewable resources are on the verge of termination. On the other hand, renewable energy sources i.e., the solar energy is plentiful and it is available compared to other energy sources.

Solar energy is emissions free, clean since it does not produce pollutants or by-products harmful to environment therefore nowadays it took major part of research, where new and more efficient method for harnessing solar energy is a great challenge. It must have high input current, low input voltage and limited input current ripple.

On the other hand the cost of solar cells manufacturing is very high and about 20-25% is their efficiency.

Therefore we want to use the maximum potential of solar panel that is; Maximum Power Point (MPP) should be there in the operation of the system. Usually due to the changes in the weather and illumination on the solar cells MPP varies for each instant Due to this the output power of solar cells decreases.

Revised Manuscript Received on March 08, 2019.

Jebitha J, M.E. (Power Electronics And Drives) St. Joseph's College of Engineering,

R G Nirmala, Assistant Professor, St. Joseph's College of Engineering

Thus, to get required output power it is needed that the MPP is tracked. For this we use a MPPT converter which is used to track MPP at any instant.

The Incremental Conductance MPPT algorithm gets the maximum power point quicker than the Perturb and Observe algorithm and has improved efficiency as well as accuracy. In this paper, interleaved flyback converter with incremental conductance MPPT for solar PV array is discussed.

The simulation of proposed system has been done in MATLAB R2018a.

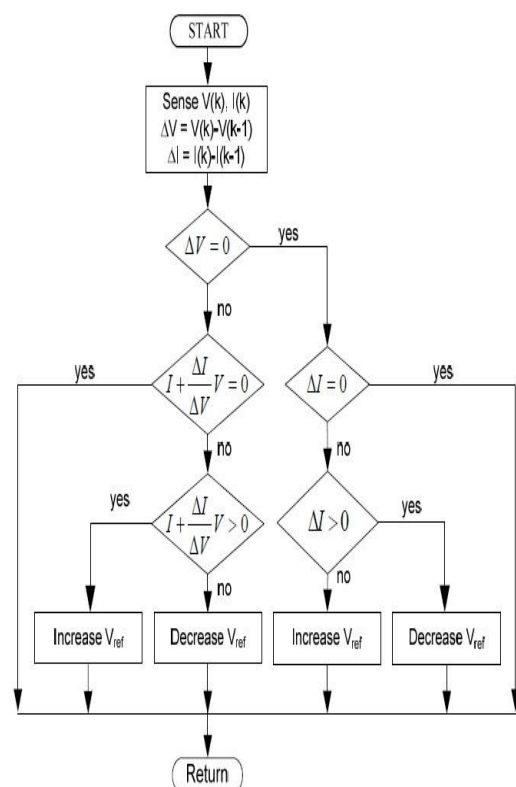


Fig. 1 incremental Conductance Method

II. INTERLEAVED FLYBACK CONVERTER

The flyback converter is connected parallel as interleaved manner in order to boost up the output power and also minimise the ripple in the input current.

Two converter are connected parallel to achieve the required output power and to achieve the reduction of the input current ripple the two converters are operated with the phase shift of 180 degree.

The proposed circuit consist of two power electronics switches, two coupled inductors, two diodes and the capacitor. The two switches are operated with the phase shift of 180 degree. The value of the load resistor R is 210Ω .

The proposed converter with the PV array is modelled in MATLAB R2018a.

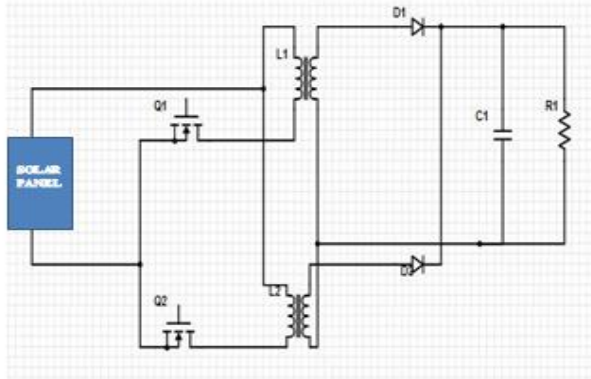


Fig. 2 Interleaved Flyback Converter

The PV module voltage, current is get by the MPPT algorithm block and modify the reference signal accordingly.

III. SIMULATION AND EXPERIMENTAL VERIFICATION

Simulation is done with MATLAB 2018a, in the simulation diagram the dc link capacitor is added in between the PV array and the proposed converter.

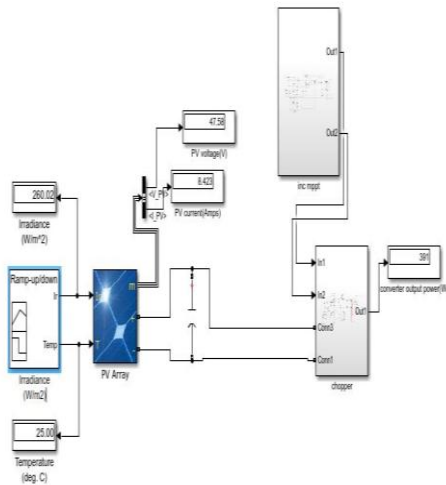


Fig. 3 Simulation Diagram of the Proposed System

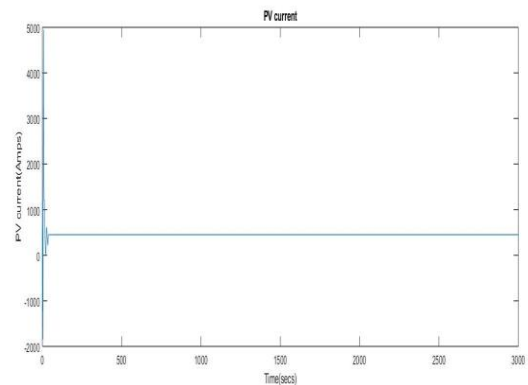


Fig. 4 PV Current Waveform

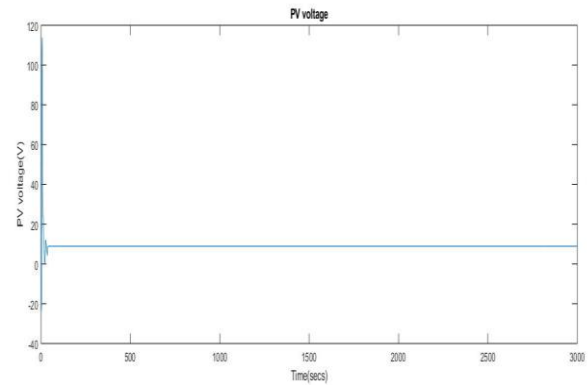


Fig. 5 PV Voltage Waveform

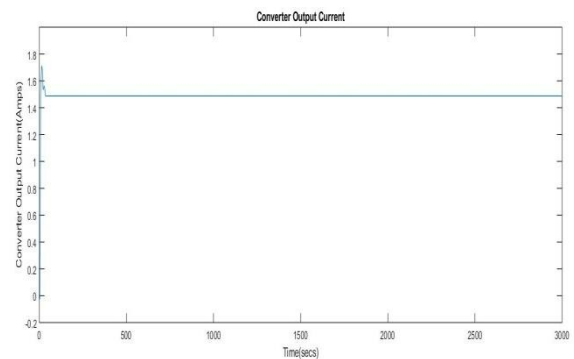


Fig. 6 converter Output Current Waveform

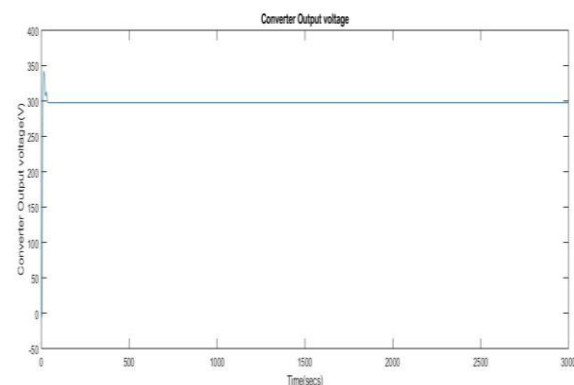


Fig. 7 Converter Output Voltage Waveform

Table.1 Efficiency of the Proposed System at Different Test Condition

CASE	TEST CONDITION	PV OUTPUT POWER	CONVERTER OUTPUT POWER	EFFICIENCY
1	Irradiance= 251.68 W/m ² Temperature= 25°C	398.9 Watts	389.3 Watts	97.59%
2	Irradiance= 491.14 W/m ² Temperature= 40°C	395.8 Watts	378.7 Watts	95.6%
3	Irradiance= 960.09 W/m ² Temperature= 40°C	422.9 Watts	403.3 Watts	95.5%
4	Irradiance= 960.09 W/m ² Temperature= 27°C	454 Watts	433.6 Watts	95.5%
5	Irradiance= 260.02 W/m ² Temperature= 25°C	400.8 Watts	391 Watts	97.75%

IV. CONCLUSION

Simulation of interleaved flyback converter with incremental conductance MPPT for solar PV array using matlab/simulink is discussed in this paper. The simulation of the paradigm is done and the outputs are analysed. The analysed report gives the efficiency of the proposed system is 95-97% and also the ripple in the input current and the ripple in the output voltage as too less.

REFERENCES

1. Double Voltage Step-up Photovoltaic Micro inverter Diana Lopez, Freddy Flores-Bahamonde, Hugues Renaudineau, Samir Kour Electronics Engineering Department, Universidad Tecnica Federico Santa Maria, Valparaiso, Chile.
2. Novel Control Scheme for Interleaved Flyback Converter Based Solar PV Micro inverter to Achieve High Efficiency Tirthasrathi Lodh; Nataraj Pragallapati, Member, IEEE and Vivek Agarwal, Fellow, IEEE.
3. Y. S. Noh, B. Y. Choi, S. R. Lee, J. K. Eom and C. Y. Won, "An Optimal Method to Design a Trap-CL Filter for a PV AC-Module Based on Flyback Inverter," IEEE Transactions on Industry Applications, vol. 52, no. 2, pp. 1632-1641, March-April 2016.
4. O. Deleage, J. C. Crebier, M. Brunet, Y. Lembeze and H. T. Manh, "Design and Realization of Highly Integrated Isolated DC/DC Microconverter," IEEE Transactions on Industry Applications, vol. 47, no. 2, pp. 930-938, March-April 2011.
5. S. Poshtkouhi and O. Trescases, "Flyback Mode for Improved Low-Power Efficiency in the Dual-Active-Bridge Converter for Bidirectional PV Microinverters With Integrated Storage," IEEE Transactions on Industry Applications, vol. 51, no. 4, pp. 3316-3324, July-Aug. 2015.
6. A. Mukherjee, M. Pahlevaninezhad and G. Moschopoulos, "Single Stage Flyback Microinverters in Solar Energy Systems," Intelec 2013; 35th International Telecommunications Energy Conference, SMART POWER AND EFFICIENCY, Hamburg, Germany, 2013, pp. 1-6.
7. Rashid H. Muhammad, Power Electronics – Circuits, Devices and Applications, Prentice Hall India, 2004.
8. Bimbora P. S., Power Electronics, Khanna Publishers, 2007.
9. Muhammad Saad Rahman, Master thesis in Electronic Devices at Linköping Institute of Technology, Buck Converter Design Issues.
10. Yali Xiong, Shan Sun, Hongwei Jia, Patrick Shea and Z. John Shen, IEEE TRANSACTIONS ON POWER ELECTRONICS, VOL. 24, NO. 2, FEBRUARY 2009, New Physical Insights on Power MOSFET Switching Losses.
11. Wilson Eberle, Zhiliang Zhang, Yan-Fei Liu and Paresh C. Sen ; IEEE TRANSACTIONS ON POWER ELECTRONICS, VOL. 24, NO. 3, MARCH 2009, A Practical Switching Loss Model for Buck Voltage Regulators.
12. J. Appelbaum- "Starting and steady-state characteristics of DC motors powered by solar generators", IEEE Trans., EC-1 (1986) 17-25.
13. H. Altas, A. M. Sharaf- "A Photovoltaic Array Simulation Model for MATLAB-Simulink GUI Environment", IEEE, (ICCEP '07), June 14-16, 2007, Ischia, Italy.
14. WZ. Faro, M. K. Balaehander- "Dynamic performance of a DC shunt motor connected to a PV array", IEEE Transaction, EC-3 (1988) 613-617.
15. M. Buresch- "PV Energy Systems Design and Installation", McGraw-Hill, New York, 1983.
16. Carratero- "A new approach to obtain I-V and P-V curves of photovoltaic modules by using DC/DC converters", Rec. IEEE PV Specialist Conference, 2005, pp. 1769-1772.