

# Improved Performance of KY Positive Output Boost Converter using Classical PI Controller

K.Ramash Kumar, S. Balakumar, S. Azath Hussain

**Abstract:** This paper studies on modeling, simulation with output potential control of KY-POBC using proportional integral controller (PIC). Due to time varying and switching characteristics of KY-POBC and also its dynamic performance is complex. Owing to increase the dynamic characteristics with output potential control of KY-POBC, a PIC is designed. The dynamic equation of the KY-POBC is derived with help of averaging method at first and then PIC is developed using Zeigler-Nichols Tuning method. The analysis of designed controller is verified at different operating conditions via. the transient region, supply voltage variation and the load variation by making MATLAB/Simulink model. The results are showed designed controller proficiency performance at various working regions.

**Keywords:** Boost converter, PIC, and KY converter.

## I. INTRODUCTION

In much low power source application needs good output voltage with minimal ripple voltage. Current days, low ripple voltage-stepping converter topologies is well presented [1-2]. But, these converters have one right-half plane zero (RHPZ) in continuous coil current that is complex in real time implementation. The KY-boost converter has currently established by K.I. Hwu and Y.T. Yau works in continuous current whereas maintaining load current is not pulsating that make the less rate of change of voltage across output capacitor resulting the low load voltage ripples in the range of mV and in addition a very good transient response in the order of few mS [3]. The state space average model for various DC-DC converters was addressed [4]. The PI and SM controllers for various DC choppers were reported [5-14].

Therefore, in this paper, it is designed to control output voltage of KY positive output boost converter (POBC) operated in continuous coil current with the proportional integral controller (PIC). The average model of the KY-POBC is derived at first and then PIC is designed. The performance of controller using KY-POBC is validated at different regions.

## II. OPERATION AND STATE SPACE AVERAGE MODEL OF KY POSITIVE OUTPUT BOOST CONVERTER

The KY-POBC (refer the Fig.1). The KY-POBC is DC-DC converter operating with voltage conversion ratio of 1 plus d, where 'd' is the time variant duty cycle of the PWM signal which is controlled by the output voltage of it. It consist of twice coils,  $S_1$  and  $S_2$  with  $D_1$ , and  $D_2$  and  $D_b$  with  $C_b$  and  $L_i$  and  $L_o$  with  $C_o$ . The KY-POBC is operating with state 1 and state 2.

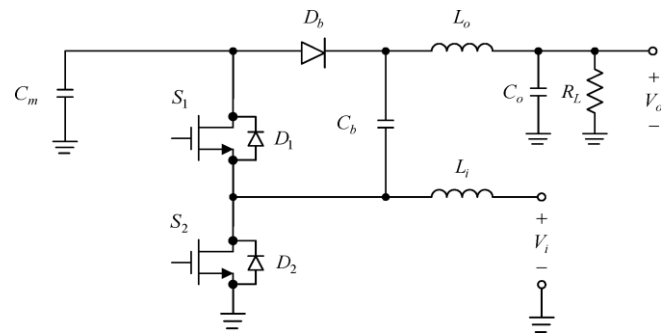


Fig. 1 Topology of KY boost converter

The power flow circuit of the state 1 KY POBC (see fig.2).

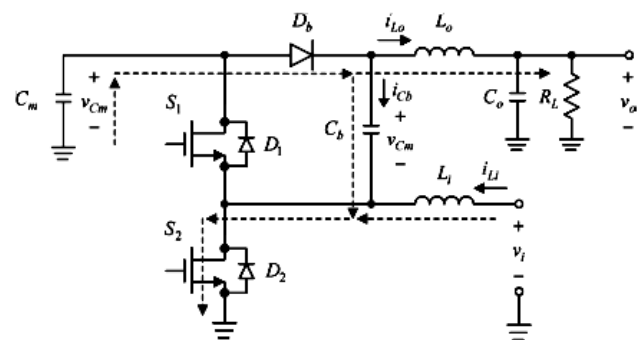


Fig. 2 Equivalent circuit of state 1 operation

In state 1, switch  $S_1$  is open and  $S_2$  is closed which ground the negative terminal of  $C_b$  is pulled to the ground that leads to  $D_b$  conduct. In this mode,  $C_m$  is released energy and  $C_b$  is charged. The  $V_{L_i} = V_i$ ,  $L_i$  is stored the energy and  $L_o$  is released energy. The  $i_{cm} = i_{cb} + i_{Lo}$ .  $i_{co} = i_{Lo} + i_o$ . The state 1 working is expressed by the state equations as (1)

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$$\begin{aligned}
 L_i \frac{\partial i_{Li}}{\partial t} &= V_i \\
 L_o \frac{\partial i_{Lo}}{\partial t} &= V_{cm} - V_o \\
 C_o \frac{\partial v_o}{\partial t} &= i_{Lo} - \frac{V_o}{R_L} \\
 C_m \frac{\partial v_{cm}}{\partial t} &= -i_{cb} - i_{Lo}
 \end{aligned} \quad (1)$$

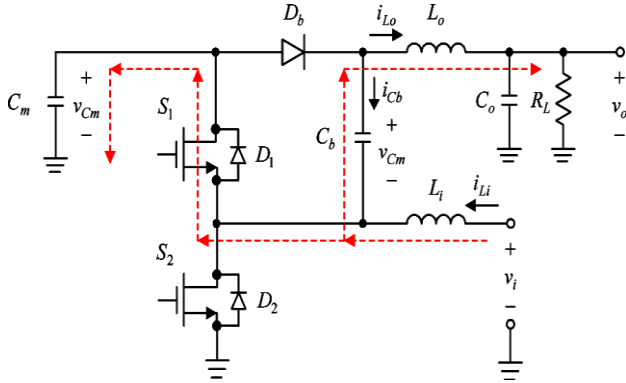


Fig. 3 Equivalent circuit of state 2 operation

In this mode 2 operation (refer Fig.3)  $S_1$  is closed and  $S_2$  is open that make  $D_b$  in reverse biased and stores  $C_m$  and release the energy  $C_b$ . The voltage across  $V_{Li} = V_i - V_{cm}$  that  $L_i$  de-energized and  $L_o$  energized, since the voltage across inductor  $V_{Lo} = 2V_{cm} - V_o$ . The  $i_{cm} = i_{Lo} - i_o$ . The  $i_{cm} = i_{Li} - i_{Lo}$ . The mode 2 working is expressed by (2)

$$\begin{aligned}
 L_i \frac{\partial i_{Li}}{\partial t} &= V_i - V_{cm} \\
 L_o \frac{\partial i_{Lo}}{\partial t} &= 2V_{cm} - V_o \\
 C_o \frac{\partial v_o}{\partial t} &= i_{Lo} - \frac{V_o}{R_L} \\
 C_m \frac{\partial v_{cm}}{\partial t} &= i_{Li} - i_{Lo}
 \end{aligned} \quad (2)$$

The mean value of voltage and current can be found from (3), where the variable  $x$  is the time-varying parameter and symbolizes voltage or current.

$$\langle x \rangle = \frac{1}{T_s} \int_0^{T_s} x d\tau \quad (3)$$

The average equation found from (1)-(3) are expressed by (4)

$$\begin{aligned}
 L_i \frac{\partial \langle i_{Li} \rangle}{\partial t} &= \langle V_i \rangle - (1-d)\langle V_{cm} \rangle \\
 L_o \frac{\partial \langle i_{Lo} \rangle}{\partial t} &= (2-d)\langle V_{cm} \rangle - \langle V_o \rangle \\
 C_o \frac{\partial \langle v_o \rangle}{\partial t} &= \langle i_{Lo} \rangle - \frac{\langle v_o \rangle}{R_L} \\
 C_m \frac{\partial \langle v_{cm} \rangle}{\partial t} &= -\langle i_{Lo} \rangle + (1-d)\langle i_{Li} \rangle - d\langle i_{cb} \rangle
 \end{aligned} \quad (4)$$

The voltage transfer gain of KY POBC is expressed as (5)

$$\frac{V_o}{V_i} = \frac{2-d}{1-d} \quad (5)$$

Let us choose the state variables of KY-POBC as  $i_{Li} = X_1, i_{Lo} = X_2, V_o = X_3, V_{cm} = X_4$  and its differential equation for the state operation of mode 1 KY-POBC is given as

$$\begin{aligned}
 \dot{X}_1 &= \frac{V_i}{L_i} \\
 \dot{X}_2 &= \frac{X_4}{L_o} - \frac{X_3}{L_o} \\
 \dot{X}_3 &= \frac{X_2}{C_o} - \frac{X_3}{C_o R_L} \\
 \dot{X}_4 &= \frac{-X_2}{C_m + C_b}
 \end{aligned} \quad (6)$$

The differential equation for the state operation of Mode 2 KY-POBC is expressed as,

$$\begin{aligned}
 \dot{X}_1 &= \frac{V_i}{L_i} - \frac{X_4}{L_i} \\
 \dot{X}_2 &= \frac{2X_4}{L_o} - \frac{X_3}{L_o} \\
 \dot{X}_3 &= \frac{X_2}{C_o} - \frac{X_3}{R_L C_o} \\
 \dot{X}_4 &= \frac{X_1}{C_m} - \frac{X_2}{C_m}
 \end{aligned} \quad (7)$$

By applying the above equations, the average model of the given system is derived by using the equation (8),

$$\begin{aligned}
 \dot{X} &= [A_{on}d + A_{off}(1-d)]X + [B_{on}d + B_{off}(1-d)]U \\
 Y &= [C_{on}d + C_{off}(1-d)]X
 \end{aligned} \quad (8)$$

Where,

$$\begin{aligned}
 [A_{on}d + A_{off}(1-d)] &= A \\
 [B_{on}d + B_{off}(1-d)] &= B \\
 [C_{on}d + C_{off}(1-d)] &= C
 \end{aligned} \quad (9)$$

The matrix equation for the A, B, C parameters are given as follows,

$$\begin{aligned}
 A &= \begin{bmatrix} 0 & 0 & 0 & 0 \\ 0 & 0 & \frac{-d}{L_o} & \frac{d}{L_o} \\ 0 & \frac{d}{C_o} & \frac{-d}{C_o R_L} & 0 \\ 0 & \frac{-d}{C_m + C_b} & 0 & 0 \end{bmatrix} \\
 B &= \begin{bmatrix} 0 & 0 & 0 & \frac{-1+d}{L_i} \\ 0 & 0 & \frac{-1+d}{L_o} & \frac{2-2d}{L_o} \\ 0 & \frac{1-d}{C_o} & \frac{-1+d}{C_o R_L} & 0 \\ \frac{1-d}{C_m} & \frac{-1+d}{C_m} & 0 & 0 \end{bmatrix} \\
 C &= [0 \quad 0 \quad 0 \quad 1]
 \end{aligned} \quad (10)$$

### III. DESIGN OF PIC

The PIC is developed to guarantee the identifying the aspiration nominal working for the KY-POBC. By regulating the KY-POBC, with the intention that it stays very closer to the nominal working point in the case of R variations, elements variations and also, for line variations.



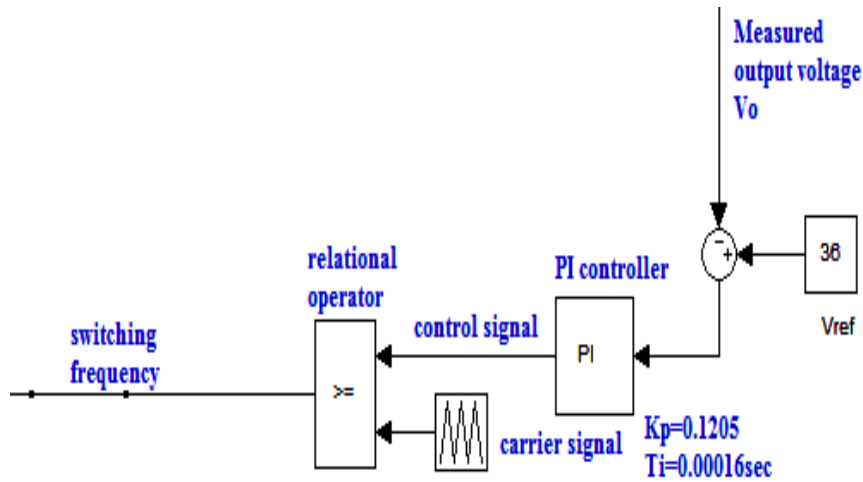


Fig. 4 Design of PIC

The PIC design has proportional gain ( $K_p$ ) and Integral time ( $T_i$ ) are designed using Zeigler-Nichols tuning method. Zeigler-Nichols suggested to mark the ranges of  $K_p = 9.316$  and  $T_i=0.0166s$  according to Table 1 suggested by Zeigler-Nichols method [14]. Simulink diagram of PIC is shown in Fig. 4.

Table. 1 Zeigler Nichols Tuning Method

PID Type	$K_p$	$T_i$	$T_d$
P	$0.5K_{cr}$	$\infty$	0
PI	$0.45K_{cr}$	$\frac{P_{cr}}{1.2}$	0
PID	$0.6K_{cr}$	$\frac{P_{cr}}{2}$	$\frac{P_{cr}}{8}$

#### IV. SIMULATION STUDY

The verification of KY-POBC with controller is done for different regions using MATLAB/Simulink (refer fig. 5). A simulation has been performed on the KY-POBC with the parameters listed in Table 2.

Table. 2 Specification of KY-POBC

PARAMETERS	SYMBOL	VALUE
Input voltage	$V_i$	12V
output voltage	$V_o$	36V
Rated load current	$I_o$	2.5A
Output inductance	$L_o$	$15\mu H$
Input inductance	$L_i$	$15\mu H$
Buffer capacitance	$C_m$	$1000\mu F$
Energy transferring capacitor	$C_b$	$680\mu F$
Output capacitance	$C_o$	$470\mu F$
load	$R_L$	14.4
Switching frequency	$f_s$	100kHz

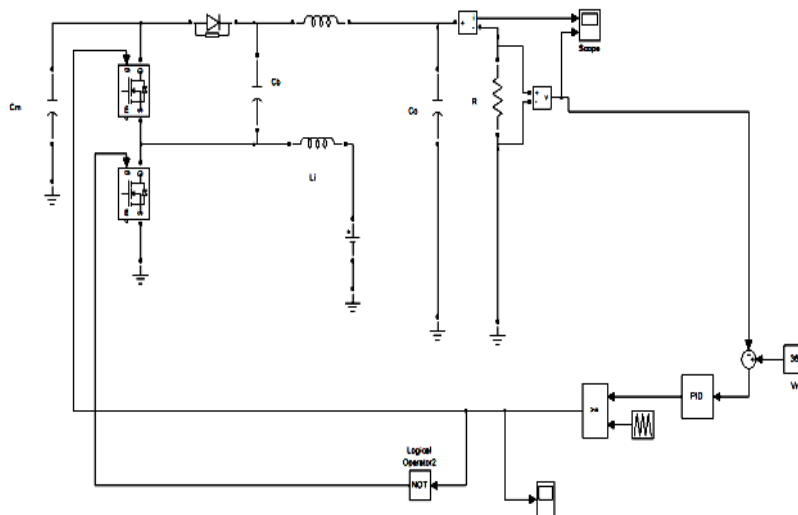
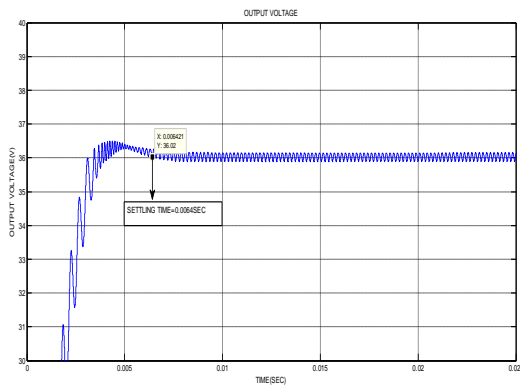
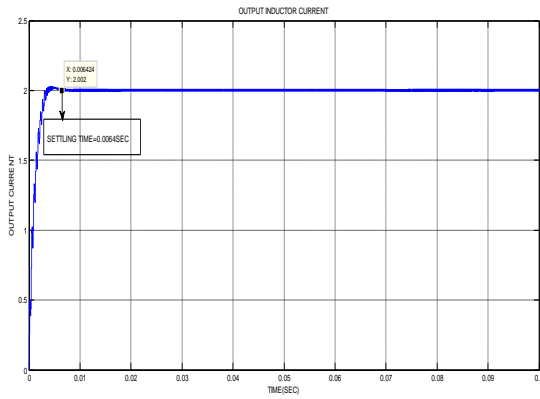


Fig. 5 MATALAB/Simulink model PIC for the KY-POBC

**A. Transient Region**

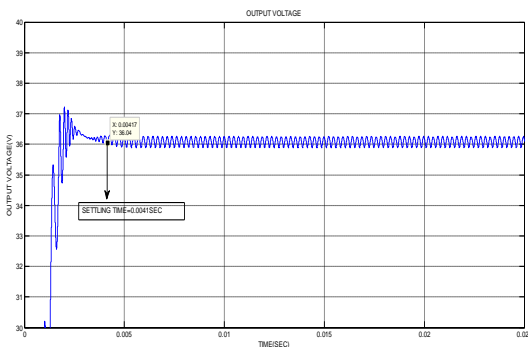


**Fig. 6 Simulated results of KY-POBC using PIC, (a) inductor current, and (b) output voltage in a transient region.**

Fig.6 (a) and (b) shows the output voltage and the inductor current of PI with KY positive boost converter in transient region. It can be seen that KY-POBC has negligible peak overshoot and settling time of 0.0064 sec.

**B. Line Variations**

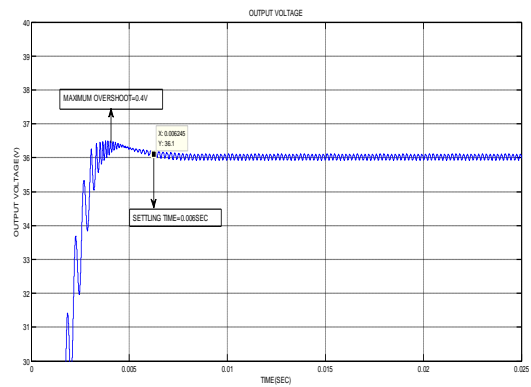
Fig.7 shows the simulated output voltage of KY-POBC with control for the input voltage from 12V to 15V. It is found that KY-POBC has maximum overshoot of 1.13 V and settling time of 0.0041 s with PIC.



**Fig. 7 Simulated output voltage of the converter using PIC when input takes step change to 12V to 15V.**

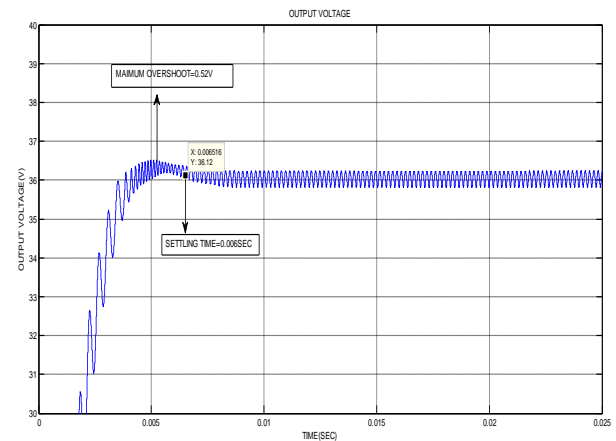
**C. Load Variations**

Fig.8 shows the simulated output voltage with R from 18 Ω to 28 Ω. It is found that KY-POBC has overshoot of 0.4V and time of 0.006 sec.



**Fig. 8 Simulated output voltage of KY-POBC using PIC for load resistance step change from 18 Ω to 28 Ω**

In Fig.9 shows that variation of output with step change from 18 Ω to 12 Ω. It could be seen that there is a small overshoot of 0.52V with settling time of 0.006 sec.



**Fig. 9 output voltage of load resistance varies from 18 Ω to 12 Ω.**

**V. CONCLUSIONS**

In this paper modelling, simulation and load voltage control of KY-POBC with PIC has been successfully demonstrated using MATLAB/Simulink. Numerical analysis and simulations are presented to show the proficient of designed PIC for the KY-POBC operated resulted in proficient regulated output voltage in line and load disturbances, good steady state and transient responses etc. It is, therefore, suitable for any stable power source real-world commercial applications.

**REFERENCES**

1. F. L. LUO AND H. YE, "Negative output multiple-lift push-pull SC Luo-converters," IEEE PESC'03, vol. 4, pp. 1571-1576, 2003.
2. FANG LIN LUO AND HONG YE, "Negative output super-lift converters," IEEE Trans. Power Electron., vol. 18, no. 5, pp. 1113-1121, 2003.
3. MAHDAVI, J., EMADI, A., TOLIYAT, H.-A., "Application of state space averaging method to sliding mode control for PWM DC/DC converters," IEEE Industry Application Society Annual Meeting, New Orleans, Louisiana, 1997, pp. 820-827



4. K. I. HWU, W. C. TU AND Y. H. CHEN "A KY Boost Converter ," *IEEE Trans. Power Electronics*, vol. 25, n.11, Nov pp. 2699 – 2703.
5. COMINES, P., MUNRO, N., "PID controllers: recent tuning methods and design to specification," *IEEE Proc. Control Theory Application*, 2002, 149, (1), pp.46-53.
6. R. KALAIVANI, K. RAMASH KUMAR, S. JEEVANANTHAN, "Implementation of VSBSMC plus PDIC for Fundamental Positive Output Super Lift-Luo Converter," *Journal of Electrical Engineering*, Vol. 16, Edition: 4, 2016, pp. 243-258.
7. K.RAMASH KUMAR, S.JEEVANANTHAN, S.RAMAMURTHY, "Improved Performance of the Positive Output Elementary Split Inductor-Type Boost Converter using Sliding Mode Controller plus Fuzzy Logic Controller," *WSEAS TRANSACTIONS on SYSTEMS and CONTROL*, Vol. 9, 2014, pp. 215-228.
8. K. RAMASH KUMAR, "Implementation of sliding mode controller plus proporotinal double integral controller for neagtive output elementary boost converter," *Alexandria Engineering Journal*, 2016, Vol. 55, No. 2, pp. 1429-1445.
9. RAMASH KUMAR, K., JEEVANANTHAN, S., "Design of sliding mode control for negative output elementary super lift Luo-Converter operated in continuous conduction mode," *ICCCCT'10*, Tamilnadu, India, pp. 138-148.
10. S.SENTAMIL SELVAN, K.RAMASH KUMAR, R.BENSRAJ, "Modeling, Simulation and Design of Variable Structure Based Sliding Mode Controller for KY-Voltage Boosting Converter," *WSEAS TRANSACTIONS on CIRCUITS and SYSTEMS*, Vol. 15, 2016, pp.143-154.
11. KUPPAN RAMASH KUMAR, SEENITHANGAM JEEVANANTHAN," Sliding mode control design for current distribution control in Paralled Positive Output Elementary Super Lift Luo Converters", *Journal of Power Electronics (JPE) Korea*, Vol.11, No.5, September 2011, pp. 639-654.
12. K. RAMASH KUMAR, S. JEEVANANTHAN, "A Sliding Mode Control for Positive Output Elementary Luo Converter," *Journal of Electrical Engineering*, Volume 10/4, December 2010, pp. 115-127.
13. K. RAMASH KUMAR, S. JEEVANANTHAN, "Analysis, Design and Implementation Of Hysteresis modulation sliding mode controller for negative output elementary boost converter", *Journal of Electric Power Components and Systems*, Vol.40, No.3, 2012, pp. 292-311.
14. K. RAMASH KUMAR, S. JEEVANANTHAN, "PI Control for Positive Output Elementary Super Lift Luo- Converter," *International Journal of Electrical and Electronics Engineering*, 4:7 2010, pp. 440-446.
15. T. PADMAPRIYA AND V. SAMINADAN, "Improving Throughput for Downlink Multi user MIMO-LTE Advanced Networks using SINR approximation and Hierarchical CSI feedback", *International Journal of Mobile Design Network and Innovation- Inderscience Publisher*, ISSN : 1744-2850 vol. 6, no. 1, pp. 14-23, May 2015.
16. T. PADMAPRIYA AND V. SAMINADAN, "Inter-cell Load Balancing technique for multi-class traffic in MIMO-LTE-A Networks", *International Journal of Electrical, Electronics and Data Communication (IJEEDC)*, ISSN: 2320- 2084, vol.3, no.8, pp. 22-26, Aug 2015.
17. S.V.MANIKANTHAN AND V.RAMA" Optimal Performance Of Key Predistribution Protocol In Wireless Sensor Networks" *International Innovative Research Journal of Engineering and Technology* ,ISSN NO: 2456-1983,Vol-2,Issue –Special –March 2017.
18. S.V.MANIKANTHAN AND T.PADMAPRIYA "Recent Trends In M2m Communications In 4g Networks And Evolution Towards 5g" *International Journal of Pure and Applied Mathematics*, ISSN NO:1314-3395, Vol-115, Issue -8, Sep 2017.
19. RAJESH, M., AND J. M. GNANASEKAR. & quot; GCCover Heterogeneous Wireless Ad hoc Networks .& quot; *Journal of Chemical and Pharmaceutical Sciences* (2015): 195-200.
20. RAJESH, M., AND J. M. GNANASEKAR. & quot; An optimized congestion control and error management system for OCCEM. & quot; *International Journal of Advanced Research in IT and Engineering* 4.4 (2015): 1-10.