# FPGA Based DNA Cryptography System for Medical Image Data Analysis Process

Fazal Noorbasha, S. Mohit Srinath, SK. Khadir Bhasha, P. Jagadish, K Hari Kishore

Abstract: Field Programmable Gated Array (FPGA) has been utilized for various prototyping and cryptographic algorithm and system applications. Because of the parallel and reprogrammable engineering of FPGAs, the adaptability of cryptographic algorithm can be exploited to accomplish high throughputs to the detriment of very small chip area. New methods have come into the scene to diminish the activity while maintain up the adequate level of security. Partial encryption is one of the methods which specifically encrypt and decrypt the bulky medical images. Meanwhile, if a similar medical image is should have been reused for another diagnosis, at that point it is prescribed to secure the whole medical image. In this research, we propose low cost FPGA based DNA cryptosystem gives high throughput to region proportion for medical data getting to process.

Keywords: FPGA, DNA, Cryptography, Image, Medical.

## I. INTRODUCTION

Data security has been an issue of real interest since decades. In the process of communicating data, the systems of data swapping has been reformed consequently the need of data validness and integrity has additionally essential. Different cryptosystems and algorithms have been anticipated in such manner. A cryptosystem is hardware or programming that can change the data from its unique comprehensible form into a mixed form so that the original data can be uncovered to some chosen people only [1]. Cryptosystems have developed throughout the years from Ceaser's figure, which depended on simply moving of letters, to the modern AES (Advanced Encryption Standard) proposed by Vincent Rijmen and Joan Daemen. Cryptographic hardware designs have been one more field of enthusiasm for some analysts [2]. Several of hardware cryptosystems have been implemented in which the inclination of hardware might be microprocessors, microcontrollers, and custom ASICs based cryptosystems [3]. Swift development in the field of information technology and drug has expanded the utilization of digital medical images in a few applications, for example, telediagnosis, tele-surgery and so on.

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The expensive use of medical image over the open system has made the researchers to carry out intensive research on processing, compacting/compressing and securing the medical images. Cryptography, the field of computer science and mathematics gives various algorithms to overcome the security challenges of a variety of data including text documents, multimedia records and medical pictures [4]. Cryptographic systems have been used for a long time starting now and into the foreseeable future. That is cryptography consists in processing plain data by applying a figure and acquiring encoded output, which would seem meaningless to a third party who does not know anything about the key includes at least one keys.

The quantity of cryptographic algorithm as far as encryption and digital signature has been accounted for in the literature. The traditional encryptions like DES (Date Encryption Standard), AES (Advanced Encryption Standard) as well as Triple DES have done great work over the textual data and digital image. But these techniques cannot be used over medical images because of their several behaviors like bulky data size and high redundancy [5]. Specific images encryption will in general limit the handling time while keeping up the adequate dimension of security. This has attracted researched to develop some specific image encryption schemes especially for securing medical images. These algorithms are helpful in several medical applications like mobile health care and wireless medical networking for securing medical images [6]. FPGA is an incorporated circuit that can be reconfigured by planners themselves. With every reconfiguration, which takes just a small amount of a second, a coordinated circuit can play out a totally different function. FPGA applications incorporate DSP applications, imaging, discourse acknowledgment, cryptography, equipment imitating and for some other application explicit employments. We proposed a DNA based cryptosystem to be the rising field for encoding the digital images. The mainly advantage of DNA based algorithm are monstrous/massive parallelism, extremely low power utilization, large data storage and offers unbreakable cryptosystem.

# II. DNA BASED ENCODING AND KEY GENERATION PROCESS

DNA means deoxyribonucleic acid formed using 4 basic nucleic acids namely Adenine (A), Cytosine (C), Guanine (G), Thymine (T), the pairs as (A,T) as well as (C,G) are complement each other. Figure 1 shows the structure of DNA and Binary values assigned to A, C, G and T.



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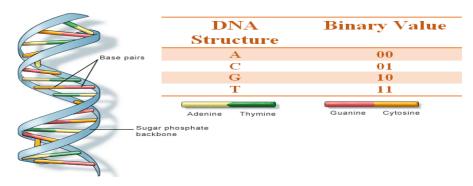


Fig. 1 Structure of DNA and Binary Equivalent Value

By using these DNA bases, one can formulate 24 kinds of encoding rule. But, out of 24 rules, only 8 coding rules which satisfy Watson-Crick complement rule [7]. The 8 encoding rule sets to encode the image pixels as shown in Table 1.

Table. 1 Eight Sets of DNA Encoding Rule

Rule 1	Rule 2	Rule 3	Rule 4	Rule 5	Rule 6	Rule 7	Rule 8
00-A	00-A	00-T	00-T	00-C	00-C	00-G	00-G
01-C	01-G	01-G	01-C	01-A	01-T	01-A	01-T
10-G	10-C	10-C	10-G	10-T	10-A	10-T	10-A
11-T	11-T	11-A	11-A	11-G	11-G	11-C	11-C

The Advanced Encryption Standard (AES) is 6 times faster than triple DES. A modification for DES was required as its key size was very small. With expanding processing power, it was considered vulnerable against exhaustive key search attach [8]. Triple DES was designed to overcome this drawback but it was found slow. The highlights of AES are symmetric key symmetric block cipher, 128-bit data, 128/192/256-bit keys, Stronger and faster than Triple-DES. AES is an iterative instead of feistel cipher. It depends on 'substitutionpermutation network'. It includes progression of connected activities, some of which include

replacing inputs by explicit yields (substitutions) and others include rearranging bits around (stages). Interestingly, AES performs all its computations on bytes instead of bits. Consequently, AES treats the 128 bits of a plaintext block as 16 bytes. These 16 bytes are arranged in four sections and four columns for handling as a framework [9-10]. Unlike DES, the quantity of rounds in AES is variable and depends upon the length of the key. AES utilizes 10 rounds for 128piece keys, 12 rounds for 192-piece keys and 14 rounds for 256-piece keys. Every one of these rounds utilizes an alternate 128-piece round key, which is determined from the first AES key.

#### III. DNA BASED IMAGE ENCRYPTION AND DECRYPTION PROCESS

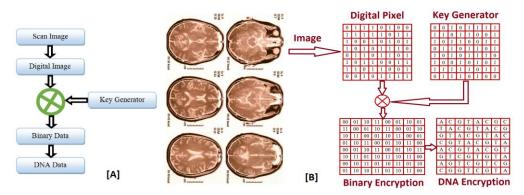


Fig. 2 DNA Encryption process [A] Flowchart and [B] Process Flow

The MRI scan image is converted into pixels and then into binary values and then it is converted into DNA code. The process of encryption is shown in Figure 2A. Figure 2B, shows the Image to DNA encryption cryptography process. The scanned image is converted to pixels using MATLAB and then binary to DNA process.

Steps of involved in the DNA based encryption process:

STEP1: Conversion of analog image into digital pixels

STEP2: Assigning the binary values to each pixel.

STEP3: Binary Key generation (AES algorithm).

STEP4: Encryption: X-OR operation between image binary data and binary key

STEP 5: Binary encryption data is converted into DNA code in the form of A, C, G and T, Assigning binary as A=00, C=01, G=10, T=11 respectively.



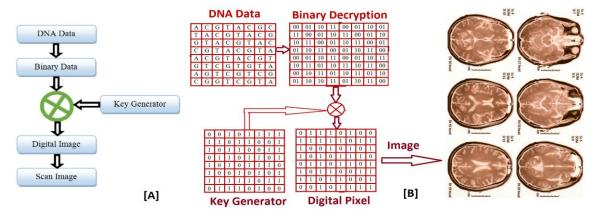


Fig. 3 Decryption process of RGB image. A) Flowchart B) Diagrammatic representation of flow

At receiver end during decryption process encrypted DNA code of MRI image is taken as input. Complete flow for decryption process shown in Figure 3A and 3B. DNA code is converted to binary code, then to equivalent decimal and combines to get image.

# IV. FPGA SYNTHESIS ANALYSIS AND SIMULATION RESULTS

FPGAs are becoming a major part of every system. For this DMA cryptography design we used Xilinx (Spartan-3). We developed hardware part by using Verilog HDL code. Figure 4 and Figure 5 are shows the RTL (FPGA) schematic view of Encryption and Decryption modules. The encryption and decryption FPGA device utilization is used as LUTs are 96, input buffers are 84, output buffers 72, number of slices 30 and Global Clocks (GCLKs) are 1. The average connection delay for encryption is 4.8ns. The average connection delay for decryption design is 3.5ns. The total power consumed at encryption side is 11.493 W. The total power consumed at the decryption side is 3.619 W. Figure 6 and Figure 7 shows the simulation timing results of encryption and decryption modules. Table 2 gives the device utilization report.

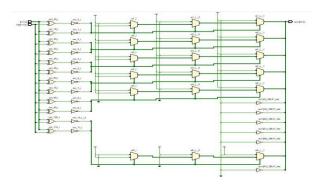


Fig. 4 RTL Schematic of Encryption module

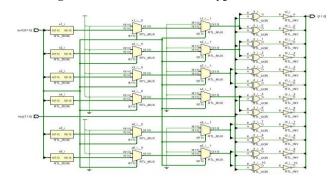


Fig. 5 RTL Schematic of Decryption module

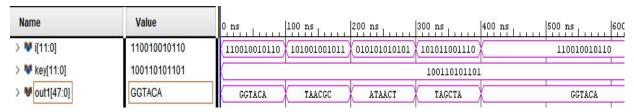


Fig. 6 DNA encryption of MRI image

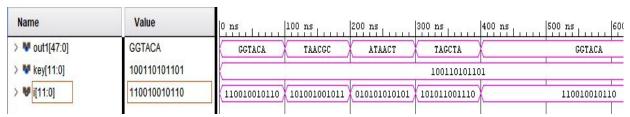


Fig. 7 Decryption of DNA code to pixel values of the image

Table. 2 Device Utilization report

Name of the Device	Encryption Module	Decryption Module
No. of LUT's	24	72
No. of Input Buffer	24	60
No. of Output Buffer	60	12
No. of Slices	12	18

# V. CONCLUSIONS

We have proposed a DNA based cryptography algorithm using AES key, that increase complexity in each round thus it can increase the security. So that it increases the efficiency and gives high accuracy. By using same security algorithm, we are optimizing the resource utilization. To implement this designed we have used Verilog HDL. The total process is tested on FPGA Spartan 3 Kit. The total power consumed by encryption module is 11.493 W. The total power consumed by decryption module is 3.619 W. The memory utilization of encryption module is 682.5 MB with the gain of 383.3 as well as for the decryption module the memory utilization is 685.7 MB with the gain of 386.3. For medical images data analysis this algorithm secures the data. New methodologies can help this algorithm in future to diminish the activity while keeping up the adequate level of security.

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