A Novel Technique for Fast Reacting MPPT for Soft Switching Interleaved Boost Converter in SPV System

D Ravi Kishore

***Abstract:** In this paper, a MPPT calculation for delicate exchanging adjusted Interleaved lift converter for sun oriented PV framework is proposed to diminish the exchanging misfortunes in converter. Ordinary DC-DC converters will work at high frequencies which results in high exchanging voltage stress, high yield voltage swell and clamor. These issues break down the execution of traditional lift converters and lead to low voltage gain. To conquer the above issues, delicate exchanging changed interleaved lift converter with double coupled Inductors is utilized. Current swell at the information side is limited by parallel associated essential windings of two coupled inductors by sharing the information current. Yield voltage swell and switch voltage stress is limited by arrangement associated yield capacitors. Also, auxiliary of two coupled inductors are associated in arrangement to regenerative capacitor by a diode for boosting the information voltage from PV cluster and adjusting the essential parallel flows through coupled inductors.*

***Index Terms:** PV System, MPPT Algorithm, DC-DC Converter, Soft Switching Interleaved Boost Converter*

I. INTRODUCTION

Presently multi day's numerous businesses requires high voltage gain DC-DC converters. For instance Fuel-cells and sunlight based photovoltaic framework requires high advance up and huge info current DC-DC converters to support the low voltage to high voltage for a matrix associated frameworks. High force release lights, (HID), electric vehicles and back-up vitality frameworks requires high voltage gain DC-DC converters to raise a battery voltage of 12v upto 100v at relentless activity. [1],[2]. A low battery voltage of 48v needs to change over to 380v in a few media communications frameworks and UPS by high advance up converters [3], [4]. For the most part, in a lift converter the voltage gain is restricted by the parasitic components of the power gadgets, Inductor and capacitor. Additionally, a high obligation proportion task may incorporate genuine switch recuperation issue of diode rectifier and expansive swell current which expands the conduction misfortunes .

Many exchanging topologies dependent on the traditional lift converter had been introduced for high advance up voltage gain [5]-[9]. So as to deal with the high info flows and to diminish the swell flows, interleaved lift control is presented in lift converters. Voltage worry of the power gadgets is identical to yield voltage in lift converters. In this way high switch voltage stress, diode turn around recuperation issue are as yet real difficulties. To explain previously mentioned disadvantages, creators proposed an altered interleaved lift converter with double Inductors and voltage multiplier to fulfill the high advance up applications and low information current swell. This paper proposes altered interleaved lift converters with double Inductor for high advance up and high power applications. This design has the upsides of high voltage increase, low yield swell and a low voltage worry over the switches. The proposed converter can turn ON the dynamic switches at zero current and limits the invert recuperation issue of diodes by sensible spillage Inductances of the coupled Inductors.

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Dr. D Ravi Kishore, Electrical and Electronics Engineering Department, Godavari Institute of Engineering and Technology (A), Rajahmundry, Andhra Pradesh, India.

II. SYSTEM MODELING

A. 2.1 Mathematical Modeling of PV Array

A photovoltaic framework changes over sun based vitality into power. The PV innovation utilizes photovoltaic cells which retains photons of light and discharge electron charges. A few PV cells are associated in arrangement and parallel mix comprises a PV board or PV module to create wanted yield control. The inverter is utilized to change over the DC control into AC. The vitality removed from the PV framework is one of the dependable characteristic vitality sources due to its eco well disposed nature. To accomplish the most extreme productivity, it is important to separate the greatest power from the PV boards .

The basic comparable circuit of a PV cell comprises of a perfect current source in parallel with a perfect diode is appeared in Figure 1.

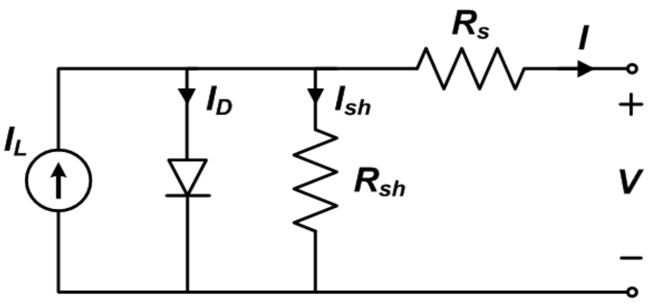

Figure 1 Equivalent model of the PV Cell

The arrangement opposition Rs speaks to the inner protection from the present stream. The shunt opposition Rsh is contrarily identified with spillage current to the ground. For a perfect PV cell, Rs = 0 (no arrangement misfortune) and Rsh = limitless (no spillage to ground). The run of the mill estimations of RS = 0:05 to 0.10ω and Rsh = 200 to 300

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Ω. The vitality transformation effectiveness of PV cell is delicate to little varieties in Rs, however is heartless to varieties in Rsh. A little increment in Rs can diminish the PV yield altogether.

The way toward demonstrating of sun powered cell is created dependent on the accompanying conditions [10], [11]. The yield terminal current I is equivalent to

$$I = I_L - I_D - I_{sh} \qquad (1)$$

where,
I_L, Light generatedcurrent
I_D, DiodeCurrent
I_{sh}, Shunt Leakagecurrent

$$I = I_L - I_{0cell}\,(e^{\frac{q*v}{\alpha*k*T}} - 1) \qquad (2)$$

where,
$I_{0,cell}$: Reverse saturationcurrent of the diode[A].
q: Electron charge[1.60217646×10^{-19}c].
k : Boltzmannconstant [1.3806503×10^{-23}J/k].
T : Temperature of thep-n junction.
α : Diode identity factorwhich lies between 1&2 for monocrystaline silicon.

B. 2.2 I-V and P-V Characteristics:

The V-I bend of the BP SX 150S PV module mimicked with the MATLAB demonstrate as indicated Figure 2. The V-I bend of PV module has one of a kind working point called most extreme power point (MPP). The directions of this working point are the working voltage and current. At this MPP, the module works with the most extreme proficiency and produces the greatest yield control.

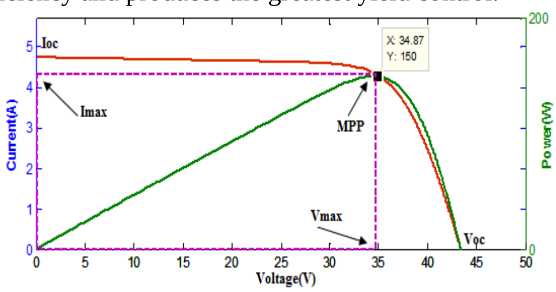

Figure 2 V-I curves of BPSX 150s PV module with MPP

C. 2.3 Maximum Power Point Tracking Algorithm

The photovoltaic vitality is a limitless and smooth vitality asset. In any case, the capital expense of PV institution is high. Henceforth, it is essential to increase the yield manage barring increasing the volume of PV modules. The yield control from the PV cluster differs with sun oriented irradiance and temperature. Subsequently, to enlarge the skillability of the sustainable strength supply framework, it is essential to observe the best strength reason of the PV show off talked about in [12], [13]. The PV showcase has an notable working point that can grant most intense potential to the heap. This point is recognized as the most severe strength point (MPP). The locus of this factor has a nonlinear variety with solar oriented irradiance and the mobile temperature. Accordingly, so as to work the PV show off at its MPP, the PV framework ought to include a most extreme power point following (MPPT) controller.

In MPPT controller, responsibility cycle is the control parameter. The MPPT controller tune the responsibility cycle (α) to its ideal incentive beneath sorts in sun oriented irradiance and temperature. The MPPT circuit contains of depth circuit and the controller. The two arrangements of MPPT conspire are appeared in Figure 5. In. For this situation, the duty cycle (α) is persistently tuned till the MPP is come to [14-15].

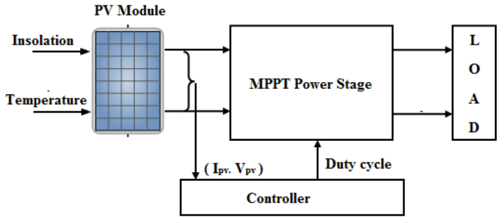

Figure 3 MPPT control through input parameters

D. 2.4 DC-DC Converter

The yield intensity of PV exhibit shifts with encompassing conditions, for example, change in illumination and temperature and so on. This influences the effectiveness of PV framework. Consequently DC/DC converter is required to exchange control from PV exhibit to stack with high proficiency. In this examination work changed interleaved lift converters with double Inductor is structured.

III. Proposed Soft Switching Interleaved Boost Converter(SSIBC)

The fundamental piece of MPPT equipment is a DC-DC converter the square graph appeared in Figure 4. It tracks the MPP and ensures the DC connect voltage under low irradiance condition. The customary DC/DC converters, for example, buck, help, buck-support, cuk, sepic, and zeta converters and so forth are worked under high exchanging frequencies bringing about high exchanging misfortunes, clamors, and part pushes. These issues break down the execution of regular lift converters and prompts the decrease of yield control. To settle previously mentioned disadvantages, creators proposed an altered interleaved lift converter with double Inductors and voltage multiplier to fulfill the high advance up applications and low info current swell.

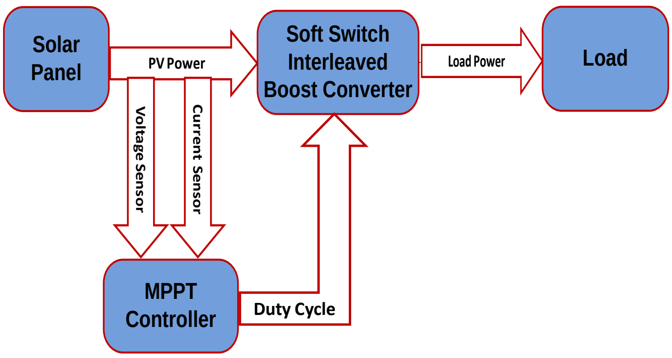

Figure 4 Block diagram of Proposed DC/DC conversion

A. Principle of operation of SSIBC

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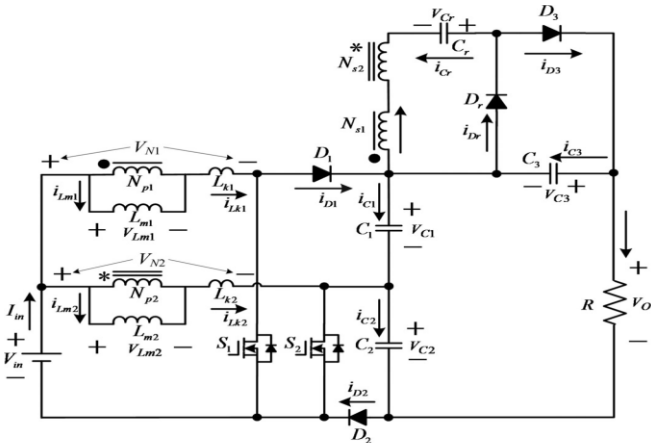
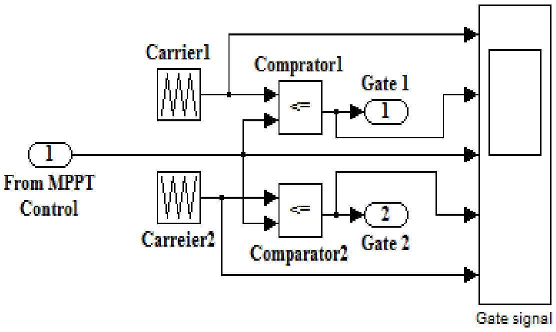
The proposed topology is appeared in figure. The SSIBC of two single-stage support inverters that are connected in parallel and inverters working 180 degree out of stage with 30 kHz exchanging recurrence, relating circuit and entryway control charts as appeared in Figure 5(a) and 5(b). This circuit having two sections (I) Modified Interleaved lift converter and (ii) voltage multiplier. The primary elements of adjusted Interleaved lift control: (ii) low yield swell because of interleaved arrangement associated capacitors (iii) low switch voltage stress



(a)



(b)

Figure 5 (a) Circuit diagram of SSIBC (b) Gate control signal

The operating tiers can be defined as follows

First stage( ):- At t= , Power swap S1 is turn ON with ZCS due to leakage Inductance Lk1, while S2 stays turned ON, all the diodes are became OFF barring D3. The present day falling rate thru D3 can be controlled with the aid of leakage Inductances and minimizes the reverse healing problem. The magnetizing Inductances LM1 and LM2, leakage inductances LK1 and two LK2 are linearly charged through the input voltage supply Vin

Second stage ( ):- At t=t1 swap S2 is flip OFF, diodes D2 and Dr flip ON. The input voltage source, magnetizing Inductances two LK2 discharges the electricity to C2 via diode D2. When whole strength of leakage Inductance LK2 discharges completely to the capacitor and magnetizing Inductance LM2 nonetheless discharges electricity to secondary aspect charging the capacitor Cr via diode Dr .

Second stage ( ):- A t= t2 change S2 is grew to become ON with ZCS condition. S1 stays in ON state. The modern-day flowing thru Dr is managed through two LK1 two and LK2 which minimizes the diode reverse restoration problem .

B. Voltage Gain Expression

At stage second

$$V_0 = V_{c1} + V_{c2} + V_{c3} \qquad (1)$$

At stage three

$$V_{cr} = V_{s1} - V_{s2} = KNV_{c2} \qquad (2)$$

$$V_{c3} = V_{cr} + V_{s2} - V_{s1} = KN(V_{c1} + V_{c2}) \qquad (3)$$

Voltage across the capacitors C_1 and C_2 are obtained as

$$V_{c1} = V_{c2} = \frac{V_{in}}{1-D} \qquad (4)$$

Voltage across the capacitors C_3 and C_r are obtained as

$$V_{c3} = \frac{2KN}{1-D} V_{in} \qquad (5)$$

$$V_{cr} = \frac{KN}{1-D} V_{in} \qquad (6)$$

Substituting (4) and (5) in (1) to obtain output voltage

$$V_0 = \frac{V_{in}}{1-D} + \frac{V_{in}}{1-D} + \frac{2KN}{1-D} V_{in} \qquad (7)$$

Voltage gain is obtained as the ration of output voltage to the input voltage

$$\frac{V_0}{V_{in}} = \frac{2}{1-D}(1+KN) \qquad (8)$$

If the impact of leakage inductances is neglected the coefficient of coupling K=1.Then voltage gain is rewritten as

$$\frac{V_0}{V_{in}} = \frac{2(1+N)}{1-D} \qquad (9)$$

The voltage stress on the power switches S_1 and S_2 are derived from

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$$V_{s1-stress} = V_{s2-stress} = \frac{V_{in}}{1-D} = \frac{V_0}{2(1+N)} \qquad (10)$$

The voltage stress on the diodes D_1, D_2, D_3 and D_r related to the turns ratio and the output voltage can be derived as

$$V_{D1-stress} = \frac{2V_{in}}{1-D} = \frac{V_0}{(1+N)} \qquad (11)$$

$$V_{D2-stress} = \frac{V_{in}}{1-D} = \frac{V_0}{2(1+N)} \qquad (12)$$

$$V_{D3-stress} = V_{Dr-stress} = \frac{2NV_{in}}{1-D} = \frac{NV_0}{(1+N)} \qquad (13)$$

IV. RESULTS OF SIMULATION AND DISCUSSION

In this segment proposed interleaved lift converter is associated between the SPR-305-WHT module and opposition. The lift converter is constrained by a MOSFET door flag which are created by considering the obligation cycle from MPPT calculation .

This paper goes for showing the execution and productivity of the proposed Boost converter and contrasting and traditional one .PV module was picked to change the sun oriented radiation, keeping up the temperature as steady at 250C and the resistive burden is 20ω

To begin with, MPPT Algorithm with gradual conductance was tried under the fluctuating irradiance profile. Second, the proposed lift converter with MPPT Algorithm was likewise tried under same conditions. The fluctuating radiations with the two techniques are uncovered in fig 7 (an) and 7 (b).

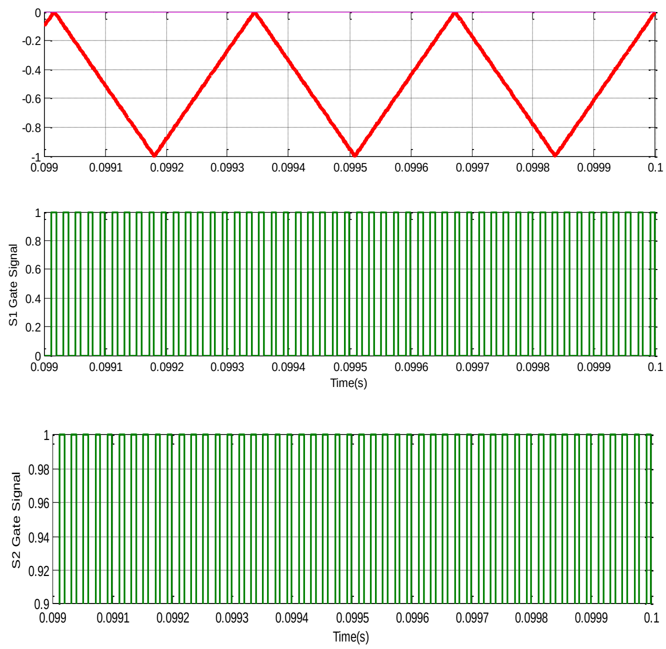
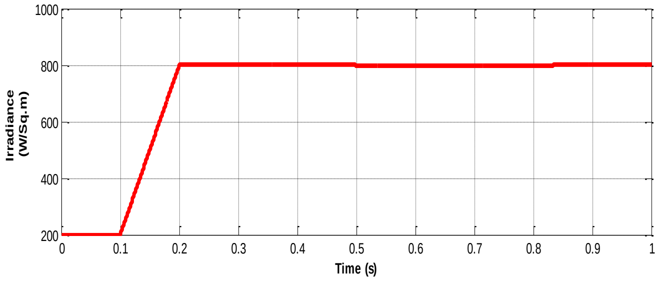
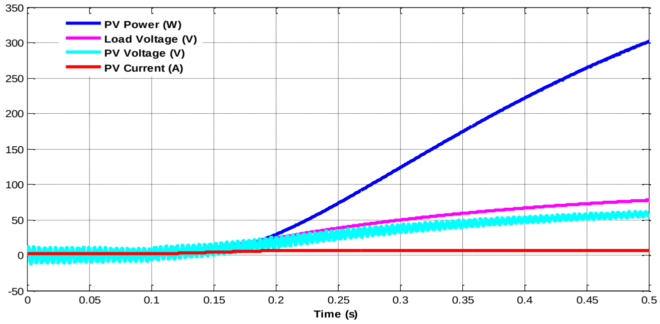


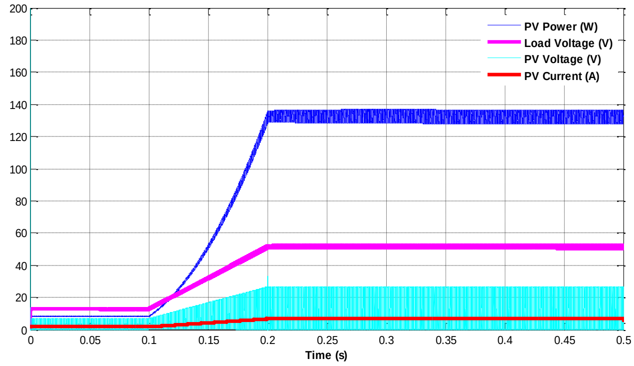
Fig 5: Control Gate signal of soft switch interleaved boost converter



(a)



(b)



(c)

Fig 7: Results of simulation: (a) irradiance, (b) with basic Incremental Conductance algorithm, (c) with interleaved boost converter.

From these results it can be established that Ipv , Ppv are appreciably affected via the variant of radiation than Vpv which is slightly effected. Also, through comparing Vpv and VL, it can be proven that the gentle switch interleaved raise converter is step-up voltage when in contrast with conventional raise converter .

Under fashionable take a look at stipulations (G=1000w/m2, T=25o C) the PV module generates 150W of power, 64.2 V of voltage and 5.96A of current. These output values range when radiation degree decreases or increases.

V. CONCLUSION

The proposed SSIBC is appropriate to interface for PV cells to convert lowvoltage enter into a high voltageoutput. The proposed improve interleaved converter with MPPT algorithm has blessings when compared to the classical boostconverter are reduces voltage stress, faster transient response for various irradiation, low enter current ripple, excessive efficiency, discount in electromagnetic emission and increased reliability .

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AUTHORS PROFILE

Dr.Dondapati Ravi kishore currently working as Professor in Department of Electrical and Electronics Engineering, GIET(A). He completed his B.E (EEE) from Andhra university and M.Tech(Energy systems) from JNTUH, Hyderabad. He completed his Ph.D from JNTUH, Hyderabad. His areas of interest are Renewable energy systems, Smart grid technologies, Power systems and Power electronics. **Email id: dr.ravikishore@giet.ac.in**