Optimization of Microstrip Patch Antenna with Zig-Zag Slot in Broadband Communication

Pritam Singha Roy, Moumita Guha

Abstract: In this paper designed and analysis of the rectangular Microstrip patch antenna for GSM band with frequency band 5.1 GHz. Primarily he designed a conventional unslotted rectangular microstrip patch antenna and measure its different parameters using simulation tools again introduced a Zig–Zag slotted antenna with dielectric substrate 2.2 and used the centre resonance frequency of 5.1 GHz. The substrate height of the FR4 was 0.787 mm. The comparison results of slotted and unslotted antenna are achieved large bandwidth and gain. The bandwidth of 35.2 % and achieved gain is 7.18 dBi in this proposed designed antenna. To model the probe feed for the microstrip patch we used a lumped port with port impedance equal to 50 ohm. The Zig-Zag antenna developed and its performance are utilized by Zealand IESD tools. This antenna is used in GSM application and many other communication systems.

Index Terms: Bandwidth, Gain, zig-zag slot, Return loss, wireless communication.

I. INTRODUCTION

Most common antenna used in different range of frequency field of application are Microstrip antennas, and they are sometimes applied in the millimetre-wave frequency band as well as broadband applications [1, 2, 3, 4]. Microstrip patch antennas having the metal conductive patch that is placed on the head of a grounded dielectric material of thickness h, with relative permittivity and permeability $\varepsilon_r$ and $\mu_r$, respectively are shown in Figure 1. The metallic microstrip patch may be of different shapes like circle, oval, rectangular, cube etc. The common rectangular type structure as shown in Figure 1. The major advantages of this microstrip patch antenna is that it has low profile structure, easily constructed and overall it can be suitable for all feeding techniques [5, 6]. If the dielectric substrate is very thin, the antenna may be broken or bent like a cylindrical structure. Generally a substrate thickness is about 0.02 $\lambda_0$. The metallic patch is generally fabricated by a photolithographic etching process in microstrip antenna design. Not only that the construction relatively easy and Other advantages include the fact that the microstrip antenna is usually lightweight (for thin substrates) and durable.

II. PATCH ANTENNA DESIGN METHODOLOGY

Three most important parameters to design of the patch antenna are:

Frequency of operation ($f_0$): This design based on GSM frequency on 5.1GHz. Hence, the designed antenna has been operated in this frequency range. The centre resonance frequency used to design the antenna is 5.1 GHz. The electromagnetic field propagation is considered in free space path, where it travels at the speed of light $v_o = 3 \times 10^8$ m/s. In UHF band, the following expression is used:

$$\lambda = \frac{300}{f_{(MHz)}}$$

Hence, the wavelength of the antenna when operating at 5.1GHz is 5.9 cm

Dielectric constant of the substrate ($\varepsilon_r$): Generally the value of the Dielectric substrate of the antenna are normally in the range of $2.2 < \varepsilon_r < 12$ [7, 8, 9]. The dielectric substrate can be selected for this antenna is RF Duiroid 5880 substrate having the value of dielectric constant ($\varepsilon_r$) of 2.2 A substrate with a low dielectric value has been used since it will increase the bandwidth of the antenna. The thickness of dielectric substrate ($h$): To design a compact microstrip patch antenna it is most desirable that it should not provides large antenna size and not provides undesired radiation. So thickness of the substrate and dielectric value are most carefully taken in this work, it is most important requirement that the antenna is not bulky. Hence the thickness of the substrate, resonance frequency and dielectric substrate are:

- $f_0 = 5.1$ GHz
- $\varepsilon_r = 2.2$
- $h = 0.787$ mm

Fig.1. Microstrip antenna geometry

Step 1: Design of the Patch width ($W$): The width of the antenna is found by the equation [10]:

Revised Manuscript Received on June 28, 2019.

Pritam Singha Roy, Research Scholar, Electronics Engineering Department, Pacific University, Rajasthan, India

Moumita Guha, M. Tech Student, Instrumentation Engineering Department, Jadavpur University, Kolkata, India
Optimization of Microstrip Patch Antenna with Zig-Zag Slot in Broadband Communication

\[ W = \frac{c}{2f_0 \sqrt{(\varepsilon_r+1)/2}} \]

Substituting \( c = 3 \times 10^8 \text{m/s}, \varepsilon_r = 2.2 \) and \( f_0 = 5.1 \text{GHz} \), we get \( W = 23.25 \text{mm} \)

Step 2: Calculation of Effective dielectric constant \( (\varepsilon_{reff}) \):

\[ \varepsilon_{reff} = \frac{s_r+1}{2} + \frac{s_r-1}{2} \left[ 1 + 12 \frac{n}{W} \right]^{-1/2} \]

Ref. to (10), we get: \( \varepsilon_{reff} = 2.1 \)

Step 3: Calculation of the Effective length \( (L_{eff}) \):

\[ L_{eff} = \frac{c}{2f_0 \varepsilon_{reff}} \]

Substituting \( \varepsilon_{reff} = 2.1 \), \( c=3 \times 10^8 \text{m/s} \) and \( f_0=5.1 \text{GHz} \), we get: \( L_{eff} = 20.28 \text{ mm} \)

Step 4: Find the extended length \( (\Delta L) \): The length extension as:

\[ (\Delta L) = 0.412h \left( \frac{\varepsilon_{reff}+0.3}{\varepsilon_{reff}} \right) \left( \frac{W}{h}+0.264 \right) \]

\[ \Delta L = 0.4146 \text{ mm} \]

Step 5: Determination of the Patch length \( (L) \):

\[ L = L_{eff} - 2\Delta L \]

\[ L = 19.43 \text{ mm} \]

Step 6: Selection of the feed location \( (X_f, Y_f) \).

It is found that at feed location, the value of the input impedance is approximately 50 \( \Omega \) and the feed location as \( Y_f = \frac{W}{2} \) and \( X_f = \frac{L}{2} \sqrt{\varepsilon_{reff}} \)

Using this above equations and trial method to match at feed position is \((X_f = 6.7, Y_f = 11.62)\). [10]

III. SIMULATION AND RESULTS

USING IE3D SIMULATOR SOFTWARE

The Zig-Zag slotted antenna is simulated by IE3D software tools whose function is works using MoM techniques. The different values of return loss according to the variation of resonance frequency at 5.1GHz is found minimum return loss is -25.98 dB. fig.2.

<table>
<thead>
<tr>
<th>Feed point ((X_f, Y_f))</th>
<th>Return Loss (dB)</th>
<th>Frequency (GHz)</th>
<th>BW (MHz)</th>
<th>Gain (dB)</th>
</tr>
</thead>
<tbody>
<tr>
<td>2.8,1</td>
<td>-18</td>
<td>5.1</td>
<td>68.3</td>
<td>6.91</td>
</tr>
<tr>
<td>3,1</td>
<td>-25.98</td>
<td>5.1</td>
<td>70.1</td>
<td>7.07</td>
</tr>
<tr>
<td>3,1.5</td>
<td>-24.29</td>
<td>5.1</td>
<td>69.2</td>
<td>6.81</td>
</tr>
<tr>
<td>3.5,2</td>
<td>-25.03</td>
<td>5.1</td>
<td>69.8</td>
<td>6.62</td>
</tr>
</tbody>
</table>

The frequency vs. VSWR graph for antenna shows at frequency 5 GHz, VSWR is 1.0dB fig.3

The antenna efficiency vs. frequency characteristics of the rectangular patch antenna shown in Figure.4. Simulated efficiency is 86.76%

Fig.2. Return Loss with Frequency characteristics for

Fig.3. VSWR vs. Frequency characteristics

Fig.4. Efficiency vs. Frequency
V. PARAMETER ANALYSIS

Here the analysis the effect of various antenna parameters on the performance of the zig-zag shaped Micro strip Patch antenna. We obtained that the various antenna parameters such as antenna gain, bandwidth, and return loss and radiation pattern of antenna are varied with the different position of feed line.

VI. EXPERIMENTAL RESULTS

The designed Zig-Zag shaped antenna and simulated results are analyzed and optimized the various values of the antenna dimensions and corresponding values of gain and bandwidth.

TABLE III

Performance for zig-zag slotted Antenna

<table>
<thead>
<tr>
<th>Feed point</th>
<th>Return Loss (dB)</th>
<th>Frequency (GHz)</th>
<th>Bandwidth (MHz)</th>
<th>Gain (dBi)</th>
</tr>
</thead>
<tbody>
<tr>
<td>0,11.1</td>
<td>-27.35</td>
<td>5.4</td>
<td>70</td>
<td>7.10</td>
</tr>
<tr>
<td>0,-11</td>
<td>-28.42</td>
<td>5.4</td>
<td>90</td>
<td>7.08</td>
</tr>
<tr>
<td>0,10.9</td>
<td>-24.15</td>
<td>5.4</td>
<td>80</td>
<td>6.52</td>
</tr>
<tr>
<td>0,11</td>
<td>-36.18</td>
<td>5.4</td>
<td>100</td>
<td>7.18</td>
</tr>
<tr>
<td>0,10.7</td>
<td>-19.50</td>
<td>5.4</td>
<td>78</td>
<td>6.10</td>
</tr>
</tbody>
</table>

We obtained that the minimum return loss is -36.18 dB at 5.4 GHz. Shown in fig.8.

Simulated bandwidth is 100 MHz which is 42.6% more than that of the unslotted patch antenna. The maximum gain is 7.18dBi shown in fig.9.
Optimization of Microstrip Patch Antenna with Zig-Zag Slot in Broadband Communication

Figure 9.3-D Radiation pattern at 5.4MHz

Figure 10.2-D Radiation pattern at 5.4MHz

Figure 11 The calculated VSWR is 1.01 at 5.4MHz

VII. CONCLUSION
The conventional rectangular Patch antenna and zig-zag slotted antenna are analysed and observed from the result of simulation that the bandwidth of the designed zig--zag shaped antenna has been enhanced increased by 35.2% and maximum achieved gain is 7.18dBi .This zig-zag antenna has been improved the VSWR which is 1.01 at operating frequency 5.4MHz. This design antenna can be use in wireless communication such as mobile communication, space communication etc.

REFERENCES

AUTHORS PROFILE
Pritam Singha Roy was born at Berhampore, West Bengal, December 21, 1981. He received M.Tech degree in Electronics and Control Engineering from Calcutta University, India, in 2009. He joined as Assistant professor of Govt College of engineering and Textile technology, Berhampore, West Bengal. His current areas of interest are design, development, optimization of microstrip antennas.

Moumita Guha has received M.Tech degree in Electronics and Instrumentation Engineering from Jadavpur University, India, in 2012. She has joined as Assistant professor of Dumkal Engineering College, Berhampore, Bengal. Her current areas of interest are design, development, optimization of microstrip antennas and QCM based Electronic Nose system design.