Wind Based Doubly Fed Induction Generator for Effective Rotor Side Converter Control

K. Naresh, P. Umapathi Reddy, P. Sujatha

Abstract: The presence of voltage swells over the DC connection of the successful rotor side converter of a Doubly Fed Induction Generator (DFIG) is natural because of vulnerability in twist vitality and in addition the variety of rotor precise speed. This can weaken the execution of the consecutive converter associated on the rotor side of the DFIG. Subsequently, the principle goal of this paper is to plan a criticism linearization procedure to dispose of the dc-interface voltage swell and additionally acquire solidarity control factor. In this paper, the dynamic demonstrating of DFIG alongside the viable rotor side converter is performed. The criticism linearization strategy controls the inward elements of the successful rotor side converter by considering the rotor q-pivot current and DC connect voltage. The MATLAB recreation results portray the viability of the voltage control strategy, through the varieties of rotor side channel, DC interface capacitance and vulnerabilities in the DC connect voltage.

Index Terms: Effective rotor side converters, DC connect voltage, feedback linearization, voltage control.

I. INTRODUCTION

Wind control ages have been becoming quickly everywhere throughout the world and have turned out to be a standout amongst the most encouraging inexhaustible age innovations. Among the diverse sorts of the breeze vitality change framework (WECS), doubly sustained acceptance generator (DFIG) [1] based breeze vitality transformation frameworks have picked up the expanding extent because of the huge advantages, which incorporate the variable speed steady recurrence task, four-quadrant dynamic and receptive power capacities, littler converters rating (around 30% of the generator rating), bring down expense and power misfortunes, contrasted and any settled speed enlistment synchronous generators. and DFIG-based WECSs are provided by the rotor with consecutive converter. Consecutive converter comprises of compelling rotor side converter and matrix side converter [2], [3]. The two level viable rotor side converters bolsters rotor of DFIG. The control of viable rotor side converter alongside DC connect is underscored in this paper. An arrived at the midpoint of little flag method has been connected by close ideal feed-forward compensator in [4]. Consistent DC connect voltage and in addition solidarity control factor under various load conditions has been gotten for PWM based AC to DC converters in [5]-[11]. A significant decrease of DC capacitance is acquired by utilizing a state criticism based control technique in [7]. A current-control-based control procedure used a corresponding in addition to basic controller to create a direction motion for the information line current adequacy in [8]. The info yield criticism linearization technique has been connected in [9]-[11].

This paper proposed the utilization of criticism linearization procedure from differential geometric hypothesis to the execution of steady DC connect voltage [12], [13], and solidarity control factor control of compelling rotor side converter in doubly nourished acceptance generator. In this investigation nitty gritty examination of applying input linearization procedure to the control [14]-[16] of viable rotor side converter is performed in view of the dynamic demonstrating of converter. Here, the control system is picked as voltage control to linearize the successful rotor side converter with yield factors as rotor side q-hub current and DC connect voltage [16]. Later inner elements [17]-[19], of the compelling rotor side converter are connected through the voltage control. At last adequacy of the proposed voltage control technique is exhibited through the reenactment results. This paper will be sorted out as pursues. To start with, Section II portrays the dynamic displaying of the DFIG and viable rotor side converter. At that point, the control plans for the RSC, utilizing criticism linearization procedure and its nitty gritty examination of information yield linearization is exhibited and Implementation of interior elements of the successful rotor side converter through voltage control is given in segment III. Recreation results are exhibited in Section IV. At last, area V outlines the ends.

II. STEADY STATE & DYNAMIC DEMONSTRATION OF DFIG

The schematic graph of DFIG based breeze transformation framework with powerful rotor side converter is appeared in Figure 1. The breeze framework comprises of wind turbine, DFIG, rotor side converter, DC connection and lattice side converter. DFIG essentially comprises of stator and rotor three stage windings which are invigorated independently and the two windings can supply vitality bidirectional. The rotor windings can be associated in delta or star.

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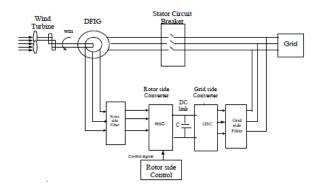


Figure 1: Representation of DFIG wind system.

Stator is empowered by the matrix at steady adjusted three stage supply and the rotor is stimulated by consecutive converter at steady adjusted three stage supply however autonomously from the stator. Relentless state voltage conditions of stator and rotor are exhibited in following conditions Stator voltage conditions:

$$V_{as} = R_s i_{as} + j w_s \varphi_{as} \tag{1}$$

$$V_{hs} = R_s i_{hs} + j w_s \varphi_{hs}$$
 2)

$$V_{cs} = R_s i_{cs} + j w_s \varphi_{cs} \tag{3}$$

$$V_{ar} = R_r i_{ar} + j w_r \varphi_{ar} \tag{4}$$

$$V_{br} = R_r i_{br} + j w_r \varphi_{br} \tag{5}$$

$$V_{cr} = R_r i_{cr} + j w_r \varphi_{cr} \tag{6}$$

$$V_{ds} = R_s i_{ds} + \frac{d\varphi_{ds}}{dt} - j w_s \varphi_{qs}$$
 (7)

$$V_{qs} = R_s i_{qs} + \frac{d\varphi_{qs}}{dt} + j w_s \varphi_{ds}$$
 (8)

$$V_{dr} = R_r i_{dr} + \frac{d\varphi_{dr}}{dt} + j(w_s - w_r)\varphi_{qr}$$
 (9)

$$V_{qr} = R_r i_{qr} + \frac{d\varphi_{qr}}{dt} - j(w_s - w_r)\varphi_{dr}$$
 (10)

$$V_{dr} = R_f i_{dr} + L_{dc} \left[\frac{a l_{dr}}{dt} \right] - w_r L_{dc} i_{qr} + V_{do}$$
 (11)

III. MATHEMATICAL DEMONSTRATION OF RSC

The power circuit of RSC topology appeared in Figure 2 is made out of six controlled switches and rotor input channel. Air conditioning side sources of info are perfect three-stage symmetrical rotor voltages, which are separated by obstruction and inductance, at that point associated with three stage compelling rotor side converter comprise of six IGBTs and diodes in against parallel [1]-[5]. The yield is made out of DC connect capacitance and inverter parameters. The model of RSC is completed under the accompanying suspicions. 1. The control converter switches are perfect gadgets. 2. All the parameters are linear time invariant. 3. The three stage voltages are adjusted.

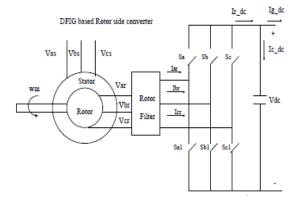


Figure 2: Rotor side converter representation

Equations related to Rotor side Converter are:

$$V_{qr} = R_f i_{qr} + L_{dc} \left[\frac{di_{qr}}{dt} \right] + w_r L_{dc} i_{dr} + V_{qo}$$
 (12)

$$C\frac{dv_{dc}}{dt} = \left(u_d i_{dr} + u_q i_{qr}\right) - \left(i_{g_dc}\right) \tag{13}$$

$$V_{do} = u_d V_{dc} V_{do} = u_a V_{dc} \tag{14}$$

$$\frac{di_{dr}}{dt} = -\left[\frac{R_f}{L_{dc}}\right]i_{dr} + w_r i_{qr} + \frac{V_m}{L_{dc}} - \frac{(u_d V_{dc})}{L_{dc}}$$
(15)

$$V_{do} = u_d V_{dc} V_{qo} = u_q V_{dc}$$
(15)

$$V_{do} = u_d V_{dc} V_{qo} = u_q V_{dc}$$
(14)

$$\frac{di_{dr}}{dt} = -\left[\frac{R_f}{L_{dc}}\right] i_{dr} + w_r i_{qr} + \frac{V_m}{L_{dc}} - \frac{(u_d V_{dc})}{L_{dc}}$$
(15)

$$\frac{di_{qr}}{dt} = -\left[\frac{R_f}{L_{dc}}\right] i_{qr} - w_r i_{dr} - \frac{(u_q V_{dc})}{L_{dc}}$$
(16)

$$\frac{dV_{dc}}{dt} = \frac{1}{c} \left(u_d i_{dr} + u_q i_{qr}\right) - \frac{1}{c} \left(i_{g_dc}\right)$$
(17)

$$\frac{dV_{dc}}{dt} = \frac{1}{c} \left(u_d i_{dr} + u_q i_{qr} \right) - \frac{1}{c} \left(i_{g_dc} \right) \tag{17}$$

Where,

V_m is Maximum estimation of the rotor input voltage. C_{dc} interface capacitor.

R_f is Resistance of the rotor side channel. L_{dc} Inductance of the rotor side channel.

In the changed state conditions from (15) to (17), the space vector is characterized as $X = [i_{dr} i_{qr} V_{dc}]$ and the control input vector $\mathbf{u} = [\mathbf{u}_d \ \mathbf{u}_q]$ is the exchanging capacities $\mathbf{u} = [\mathbf{u}_a \ \mathbf{u}_b]$ u_c] is synchronously turning d-q amounts. The point of the model is to accomplish the consistent DC interface voltage and solidarity control factor. It tends to be gotten by controlling the estimation of q-hub current to zero and dc connect voltage follows the given dc interface reference voltage Vdc ref¹⁰. From the control discernment, demonstrating the rotor side converter in d-q reference outline has the upside of the decreasing the time fluctuating amounts in required state conditions.

$$f(x) = \begin{bmatrix} -\left[\frac{R_f}{L_{dc}}\right]i_{dr} + w_r i_{qr} + \frac{V_m}{L_{dc}} \\ -\left[\frac{R_f}{L_{dc}}\right]i_{qr} - w_r i_{dr} \\ -\frac{1}{c}(i_{g_{dc}}) \end{bmatrix} g(x) = \begin{bmatrix} -\frac{(V_{dc})}{L_{dc}} & 0 \\ 0 & \frac{(V_{dc})}{L_{dc}} \\ \frac{1}{c}(i_{dr}) & \frac{1}{c}(i_{qr}) \end{bmatrix} \\ X = \begin{bmatrix} -\left[\frac{R_f}{L_{dc}}\right]i_{dr} + w_r i_{qr} + \frac{V_m}{L_{dc}} \\ -\left[\frac{R_f}{L_{dc}}\right]i_{qr} - w_r i_{dr} \\ -\frac{1}{c}(i_{g_{-dc}}) \end{bmatrix} + \begin{bmatrix} -\frac{(V_{dc})}{L_{dc}} & 0 \\ 0 & \frac{(V_{dc})}{L_{dc}} \\ 0 & \frac{1}{c}(i_{qr}) \end{bmatrix} \begin{bmatrix} u_d \\ u_q \end{bmatrix} \\ X = f(x) + g(x)u \end{bmatrix}$$
(19)



IV. FEEDBACK LINEARIZATION TECHNIQUE

A. Input yield criticism linearization

At present, the info yield linearization strategy is used as a plan technique to linearize the non-direct framework. Not at all like direct approximate particles of the interior elements, in info are yield linearization strategy correct state changes utilized to convey the procedure of linearization. Besides the real framework is changed over into least complex shape [11], [23]. In information yield linearization system, choosing the new helper control factors which encourage lessening the following blunder to invalidate the non-linearity [17]-[19]. The non-direct state conditions (15) to (17) of the powerful rotor side converter are revised in the accompanying condition (18). Three amounts f(x), g(x) and X are orchestrated in grid frame and toward the end they are appeared as total condition. This state vector and yield vector y assume an imperative job in finding the estimations of the control signals and there by exchanging signals for rotor side converter. As per the hypothesis of input yield linearization one must pick a spurious yield vector y= [y1 y2], reference vector $y^* = [y1^* y2^*]$ and following blunder $e = [y - y^*]$.

B. Voltage Control

Utilizing the hypothetical outcomes portrayed in the past segment, we present here an information yield linearizing arrangement, indicated as Iqr Iqr_ref=0 Vdc_ref voltage control [2], [4], [6], [9], for the dc-transport voltage control of compelling rotor side converter with solidarity control factor by picking two diverse sham yield factors. The spurious yield factors are picked as y = [i qr Vdc] since the point is to keep up the DC interface voltage Vdc is equivalent to voltage reference Vdc_ref with q-pivot current iqr is zero [13], [16]. At that point reference yield vector must be $y^* = [0]$ Vdc_ref] as per the strategy of info yield linearization system.

$$\begin{bmatrix} u_d \\ u_q \end{bmatrix} = \begin{bmatrix} 0 & -\frac{V_{dc}}{L_{dc}} \\ \frac{1}{c} (i_{dr}) & \frac{1}{c} (i_{dr}) \end{bmatrix}^{-1} \left(\begin{bmatrix} \omega_r i_{qr} + \left(\frac{R_f}{L_{dc}}\right) i_{dr} \\ \frac{1}{c} (i_{g-dc}) \end{bmatrix} + K10iar - K20(Vdc - Vdc - ref) \quad (20) \end{bmatrix}$$

$$\frac{di_{dr}}{dt} = -\left(\frac{R_f}{L_{dc}}\right)i_{dc} + \frac{V_m}{L_{dc}} - \frac{(V_{dc})}{L_{dc}}\left(\frac{i_{g_dc}}{i_{dr}}\right) + \left(\frac{K_2}{i_{dr}}\right)(CV_{dc})\left(V_{dc} - V_{dc}\right)(V_{dc} - V_{dc})(V_{dc})$$
(21)

Following blunder e to zero where igr tends to zero^{4,12} and DC connect voltage Vdc is ways to deal with DC interface voltage reference. In the wake of getting the zero elements of the framework, two harmony focuses are:

the namework, two namony rocuses are:
$$i_{dr_ref} = \frac{1}{2} \left[\frac{V_m}{R_f} \pm \sqrt{\left(\frac{V_m}{R_f}\right)^2 - \left(\frac{4V_{dc_ref} i_{g_dc}}{R_f}\right)} \right] \qquad (22)$$
Effective equilibrium point is chosen as⁴

$$i_{dr_ref} = \frac{1}{2} \left[\frac{V_m}{R_f} - \sqrt{\left(\frac{V_m}{R_f}\right)^2 - \left(\frac{4V_{dc_ref} i_{g_dc}}{R_f}\right)}}{\left(\frac{4V_{dc_ref} i_{g_dc}}{R_f}\right)} \right] \qquad (23)$$

$$i_{dr_ref} = \frac{1}{2} \left[\frac{V_m}{R_f} - \sqrt{\left(\frac{V_m}{R_f}\right)^2 - \left(\frac{4V_{dc_ref}i_{g_dc}}{R_f}\right)} \right]$$
 (23)

The controlling circuit of rotor side converter [3], with information yield direct voltage control is exhibited in Figure 3. The Voltage control outline of RSC utilizing Input yield linearization is appeared in Figure 3. Rotor abc voltages and stage edges are considered from stage voltages of DFIG

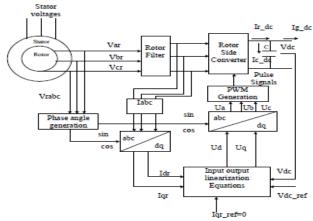


Figure 3: Input Output Linearization Voltage Control of RSC

The three stage abc amounts are changed over into d-q. Rotor d and q-pivot streams are contrasted and d-q reference ebbs and flows. q-hub reference current is taken as zero to encourage solidarity control factor and d-hub reference current is acquired by looking at Vdc and Vdc ref esteem. From condition (19), ud and uq are found and after that changed to ua, ub and uc. These heartbeat signals are sustained to the Rotor side Converter. The Rotor side converter and Grid side converter switching pattern is represented as

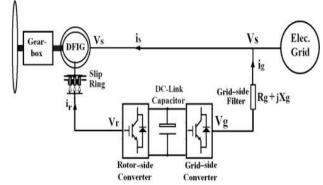


Figure 4: Rotor side converter and Grid side converter switching

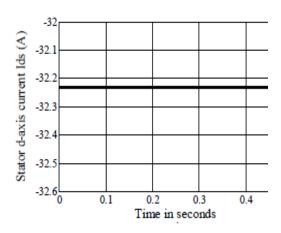
V. SIMULATION RESULTS

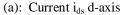
The details and circuit parameters utilized in the simulation are given in Table 1. Reenactment aftereffects of unfaltering state and dynamic demonstrating of DFIG, variety of DC connect voltage of rotor side converter with and without controller and impact of parameter vulnerabilities on rotor coordinate hub current and DC interface voltage are displayed in following segment.

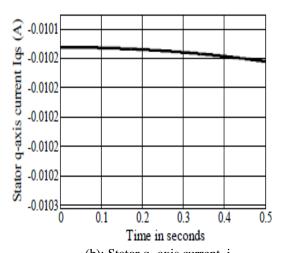
A. Steady state and dynamic modeling of the DFIG

Consistent state reaction of DFIG framework is appeared in Figure 5 (a) to Fig.5 (f).

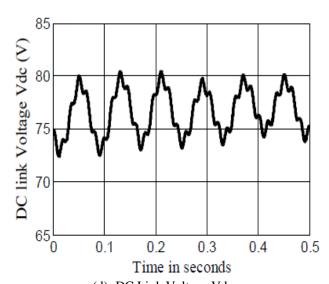


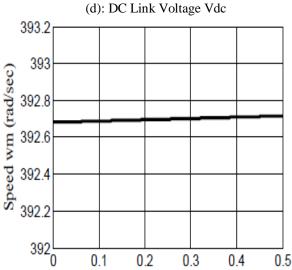




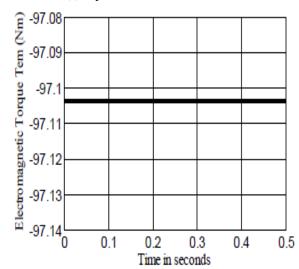


(b): Stator q- axis current iqs 34.5 Rotor d-axis current Idr (A) 34.4 34.3 34.2 34.1 0 0.1 0.2 0.3 0.4 0.5 Time in seconds (c): Rotor d- axis current idr





Time in seconds (e): Speed w_m of the DFIG Rotor



(f): Electromagnetic Torque T_m Fig.5 Consistent state reaction of DFIG framework

B. Voltage with and without controller of DC interface

The reactions of the rotor side converter with and without controllers are appeared in Figure 4. In real framework there

are numerous swells in DC interface voltage where as they are disposed of in the voltage controller. Yield wave shape is smoothened and keeps up the steady DC connect voltage all through the framework independent of parameter varieties.

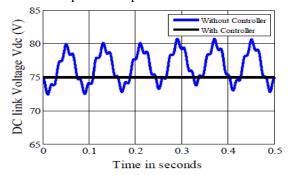
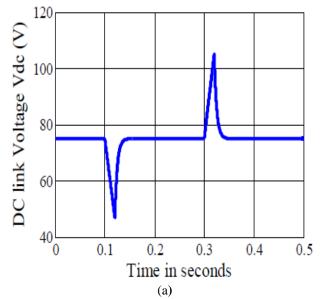
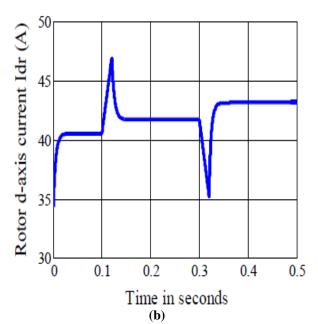


Figure 6: Variation of DC Link Voltage V_{dc} with and without Controller

C. Disturbance thought and parameter uncertainties

At the point when unsettling influence has happened in the framework from 0.1 to 0.12 sec and furthermore between 0.3 to 0.32 seconds the DC connects voltage is fluctuated in a flash amid that period which is appeared in Figure 7(a). If there should be an occurrence of rotor d-hub Current, the impact of unsettling influence is appeared in Figure 7 (b) amid 0.1 to 0.12 sec and 0.3 to 0.32 sec.





 $\label{eq:connect} Figure.7~(a)~DC~connect~Voltage~V_{dc}~for~annoyance~in~channel~parameter,~(b)~Rotor~d-hub~Current~idr~for~perturbation~in~filter~parameters$

Table.1 specifications of the rotor side converter

Tubical specifications of the fotor side converter	
Power rating	5KW
Source voltage	77.85V
DC Link voltage	75V
Inductor L _{dc}	0.08H
Resistor R _f	0.01Ω
DC Link capacitor	2.9μF
Frequency	50Hz
Grid side inductor	0.08H
Grid side resistor	0.1Ω

VI. CONCLUSION

This paper proposes a voltage control of DC connect voltage for Effective rotor side converter. Consistent DC interface voltage and solidarity control factor of the framework is accomplished. Dynamic displaying of DFIG and numerical demonstrating of the rotor side converter is introduced. The execution of the viable rotor side converter with and without controller is obviously appeared in recreation results. It is plainly discovered that the control methodology is hearty for the varieties of compelling rotor side channel, DC connect capacitance, uncertainties in the DC interface voltage.

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