

ANN Fed High Gain Luo Converter based Reduced Switch Multilevel Inverter for Single Phase Network Bridged PV Arrangement

S. Prakash, N. P. Gopinath, B. Kalaiselvi

Abstract: In the present scenario photovoltaic (PV) systems feature resonant interest in the recent researches. The amount from the PV frame is highly oscillated due to temperature variation. In this proposed work, the ANN based MPPT algorithm extracts maximum energy from the PV system by benefit of high gain DC-DC Luo converter, the Luo converter produced voltage is given to the single phase grid through reduced switch seven level inverter. The proposed seven level inverter is controlled by high frequency technique multicarrier modulation technique. The PI controller based closed loop grid synchronization is implemented for solving the power quality issues. The multilevel inverter reduce the total harmonics distortion in the output AC voltage waveform. The developed work is carried out using Matlab software-based simulation with various solar irradiation and degree.

Keywords: Artificial Neural Network (ANN), Cascade H-Bridge Multilevel inverter (CHB-MLI), Maximum power point tracking (MPPT), Luo Converter, Photovoltaic (PV)

I. INTRODUCTION

Solar source of energy has become highly significant for power generation across the photovoltaic (PV) system. Even though the primary fault of system is minimal efficiency of conversion into electricity. Henceforth the environmental factors such as solar radiation and atmospheric temperature plays a vital role in power generation by PV modules [1]. Need for solar energy is rising approximately by 20-25% every year, during the past two decades [2]. The potential for direct transformation of AC energy in affixing to reduced functioning cost and low emissions are the major merits. Hence the unimaginable penetration of PV systems in the power conversion system is understandable [3]. Despite the various benefits of the PV system, its non linear characteristics form a major challenge. A PV array tracking is an important issue for increasing the total efficiency of the system. The output of the PV array varies often with respect to the climate conditions. So a technique called Maximal Power Point Tracking is utilized for extricating the extreme power from the PV panels. In the past, several methods have

been presented for evaluating the maximum power point whereas most widely used theorems are Perturb & Observe (P&O) and Increased Conductance [4].

Traditional MPPT techniques achieve moderate performance with few complexities in implementation. Developed artificial intelligence based MPPT technique is advised for better changeable and constant frame action specifically under partial shading and rapidly changing environment [5]. Various Artificial Intelligence based MPPT techniques are developed. In that fuzzy logic and Artificial Neural Network controllers are utilized nowadays, whereas Artificial Neural Network has shown better performance under frequently changing irradiance, specifically with respect to efficiency and response time [6]. The developed MPPT with ANN is trained to evaluate the MPP voltage as reference voltage. Thus obtained reference voltage drive the DC to DC Luo converter to function near to MPP [7]. The generated voltage from PV module is not appropriate for rating of network voltage. Hence Luo converter is utilized to improve the rating of voltage through ANN based MPPT method. Thus regulated voltage from Luo converter is DC, but for economic desire, conversion of DC to AC is essential.

Topologies like cascaded H-Bridge inverter, Diode clamped and flying capacitor are used for DC to AC conversion. [8, 10]. Various types of PV inverters have been developed and designed in the recent years. Among which cascaded H-bridge MLI is developed in this system. Thus suggested methodology allows the usage of DC sources with distinct values of voltages and high resolution multilevel waveforms can be generated with reduced count of components [11]. Less Total Harmonic Distortion (THD), less electromagnetic interference can be achieved with reduced cost of components [12, 19].

Various techniques [12] are available in the present scenario for controlling the multilevel inverters like sinusoidal pulse width modulation and vector pulse width modulation. From paper, advanced selected harmonic elimination methodology is utilized for minimizing certain harmonics of lower order like 3rd, 5th and 7th from the output voltage. Distinct non linear equations are solved by this method and a better optimized result can be produced. ANN methodology is utilized for solving non linear equation to reduce THD [6]. The evaluated information is utilized for designing seven level inverter of single phase and simulation model at MATLAB platform.

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In order to overcome all the existing issues, the proposed Luo converter based seven level inverter based single phase connected to solar has designed. The Luo converter having high output voltage gain and internal regenerative snubber circuit. The seven level inverter is achieved with common single DC source with reduced switches. The grid synchronization also achieved using PI controller.

II. COMPOSITION OF PV SYSTEM

The solar tracker receives power from the sun and converts daylight into DC energy. A significant part of the PV arrangement is the solar chamber, the solar chamber of the bundled cells panels. Generally the PV cell consists of semiconductor object. The terminal of PV device produce voltage and current to small loads likewise DC motors and lighting systems [2, 13]. Mathematical layout of PV cell is designed using Mat lab. From the theory the mathematical VI characteristics of PV cell is detailed by [13], [12].

$$I = I_{pv,cell} - I_d \tag{1}$$

$$I_d = I_{o,cell} \tag{2}$$

$$I = I_{pv,cell} - I_{o,cell} \tag{3}$$

Experimental Arrays are made of various PV coupled cell and to find the attributes at PV array output the additional parameters have to be included is displayed in fig 1.

The primary mathematical statement after adding characters is

$$I = I_{pv} - I_o \left[\exp \left(\frac{V + IR_s}{V_t n} \right) \right] - \frac{V + IR_s}{R_p} \tag{4}$$

Where,

- $I_{pv,cell}$ = Current produced from the daylight
- I_d = Shotkey equation of diode represented in equation (2)
- I = PV cell output current
- q = Magnitude of elementary charge ($1.602176 \cdot 10^{-19} C$)
- V = Solar Cell Voltage
- k = Boltzmann factor ($1.3806503 \cdot 10^{-23} J/K$)
- R_s = Series Resistance of PV cell
- V_t = Output voltage
- n = Ideality factor of diode
- R_p = Parallel Resistance of PV cell
- $I_{o,cell}$ = Leakage current of diode
- T = Temperature of P-N

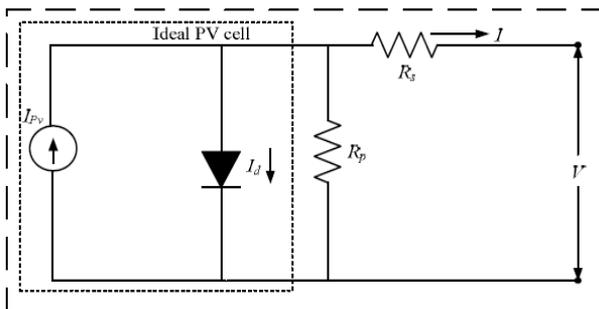


Fig. 1. Solar cell circuit diagram

R_p is maximum for solar cell with high quality and R_s is minimum. Scrutinize the 3 significant parameters (ie.,) open circuit voltage (V_{oc}), fault current (I_{sc}) and MPP. The PV apparatus is primly affected by irradiation level and degree and the characteristics of internal device [14]. Fig. 2 exhibits the V-I characteristics and P-V characteristics of Solar rely on MPPT, fault current, open circuit voltage.

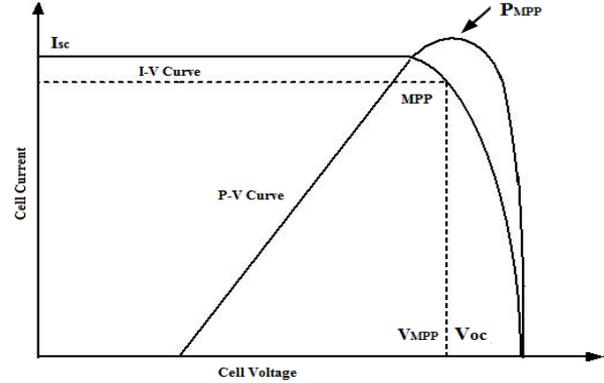


Fig. 2. VI and PV characteristics curve

III. LUO CONVERTER DESIGN

This converter is utilized for converting DC power at one level of voltage to another level. At present power electronic devices need supply of power with high reliability, high efficiency, and low input ripple. Parasitic elements in DC-DC converter limit the potential and efficiency. To outperform the above mentioned limitations DC-DC Luo converter is constructed. Fig. 3 displays the circuit figure of Luo converter. In power circuit the power switch IGBT is denoted as S and the freewheeling diode is denoted by D. the passive elements used for power depository are Inductors L1, L2, and capacitors C1, C2, R is the load. This operates in both operations.

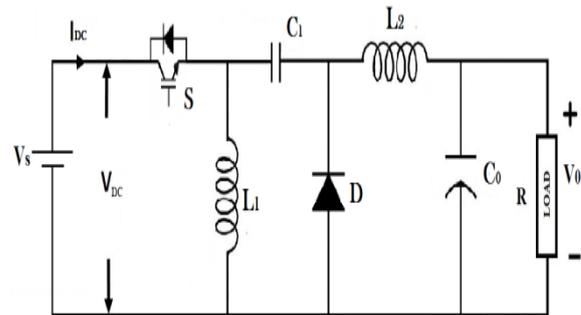


Fig. 3. Design of DC-DC Luo converter

The switch of the converter is ON while in operation mode, supply voltage charges inductor L1. At same time energy from source is absorbed by inductor L2 and capacitor C1. When switch is off, source current is zero, whereas current in L_1 flows through diode to charge capacitor C1, current in L2 flows through C2, R and D to keep it in progressive way.

In discontinuous conduction mode, Diode is in OFF state, whereas inductor discharges through V0 and L2.

The energy is stored and delivered to the load from output stage and the switching voltage is smoothened to generate a stable output voltage. Inductors in Luo converter have a basic DC resistance (DCR) which affects the outcome voltage. Hence inductor with low DCR is used to enhance the performance of converter. Output capacitance value of Luo converter is increased to minimize the voltage ripple [15]. Formula calculation for duty cycle and output voltage

Duty cycle

$$D = \frac{T_{on}}{T} \quad (5)$$

Output voltage

$$V_o = \frac{D}{1-D} V_{in} \quad (6)$$

IV. ANN MAXIMAL POWER POINT TRACKING

MPPT is utilized to extricate the highest energy of solar frame with a view to PV irradiation and climate changes at a specific timing with MPPT controller. A lot of formula have been experimented to check the MPP. Many of the prevailing algorithms experiences fault tracking, slow tracking and oscillations which minimize the efficiency.

To avoid the above mentioned cons ANN based MPPT is utilized in the developed method. The execution and efficiency of the system is maximized by utilizing ANN based MPPT. A trained Artificial Neural Network is coupled to evaluate the irradiation and climatic change from PV voltage and current signals [16].

Artificial Neural Networks (ANN) is nonlinear structures used widely in control systems. ANN is computational models inspired by biological neurons. It uses cascaded integrated nodes which applies the mathematical operations to input or output. Many learning mechanisms are present to make the ANN gain knowledge. Artificial Neural Networks are designed by using basic principles [18-19]. For detection of data ANN are trained and also comprehend the output and input relationship. The operation of ANN is carried by Initialization, training, validation and testing. ANN constitutes of three layers with many integrated nodes with an activation function. Flow chart of ANN algorithm is shown in fig. 4.

The three layers are namely input layer, hidden layer, output layer. The input character is fetched for evaluation and it does not change the contents in the input data. In hidden layer the input nodes are coupled to each node of layer, the process takes place by weighted connection. In output layer the active nodes generating the output integrate and transform the data to produce output values. Activation function is used for non linear operations. For monitoring output from reaching maximum value, transfer function is utilized. Thus ANN computes the value of switching angles to get low THD. The algorithm for ANN explains the operation of the system.

Step 1: Selection of changing angle for input data set.

Step 2: Input data are trained in ANN network and differentiate the actual output with desired output.

Step 3: Accuracy of trained ANN is evaluated and the new samples of data are tested. ANN testing plays a vital role in

ANN execution.

Step4: After evaluation best switching angle is obtained else process is repeated.

Artificial Neural Network is developed and experimented. In MPPT the ANN input can be PV module parameters such as PV voltage or PV current. Output is referred as Reference signal. There might be mono or multiple references signals. Among which duty cycle signal is used for DC-DC Luo converter to operate at or near to MPP. The result of the simulation we can get input and output data.

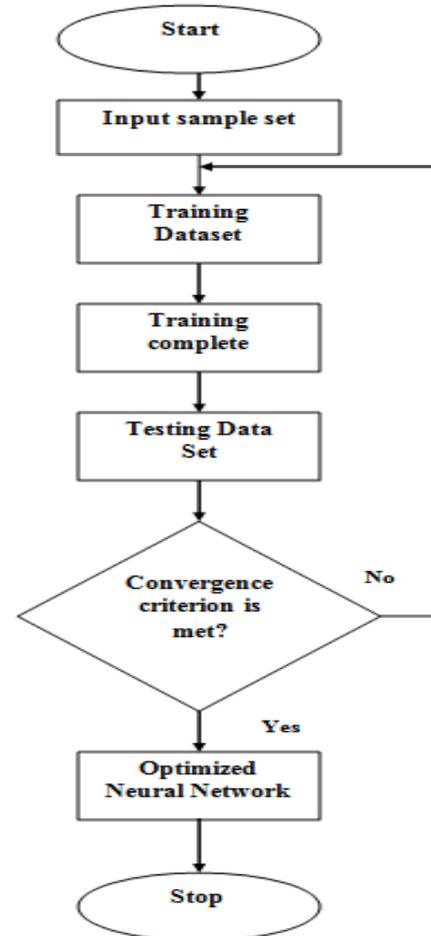


Fig. 4. Flow chart of ANN algorithm

Neural Network Toolbox from MATLAB software tool is utilized to produce trained and test data of the Neural Network. Using the matlab tool the data of current and voltage of PV are used to compute switching angles.

V. SEVEN LEVEL INVERTER TOPOLOGY H-BRIDGE

H-bridge seven level inverter which is single phase was designed. It constitutes traditional single phase H-bridge seven level inverter, IGBT and a capacitor voltage divider formed from C_1, C_2, C_3 . The transformed H-bridge methodology is primarily beneficial over other methodology shown in fig .5, whereas it has minimum number of power IGBT switches, power diodes and least number of capacitors connected to the grid.

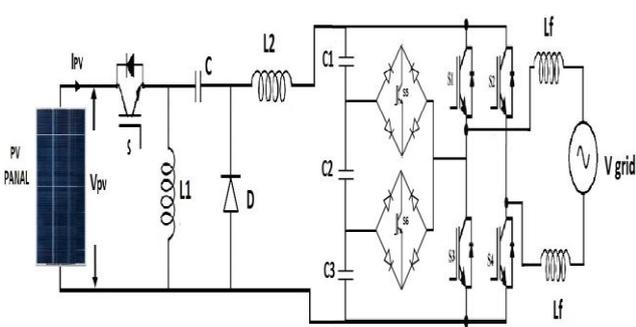


Fig. 5. Grid connected single phase seven level inverter

The PV module is integrated to the inverters with DC-DC Luo converter. The inverter generates the power and delivers to the network. Voltage from PV module is very low when compared to grid voltage hence DC-DC Luo converter is used. DC bus with high voltage is required to make sure whether power flows from PV module to grid. In order to drain the injected current into network a filtering inductance L_f was utilized. Appropriate inverter switching can generate seven levels of output voltage ($V_{dc}, \frac{2V_{dc}}{3}, \frac{V_{dc}}{3}, 0, -V_{dc}, -\frac{2V_{dc}}{3}, -\frac{V_{dc}}{3}$) for dc supply voltage. In the MLI all the voltage sources are in series with other voltage sources by H-bridge network. Table-I exhibits the switching functions which produce seven levels of output voltage.

Table- I: Switching condition of output voltage

V_0	S_1	S_2	S_3	S_4	S_5	S_6
V_{dc}	on	off	off	on	off	off
$\frac{2V_{dc}}{3}$	off	off	off	on	on	off
$\frac{V_{dc}}{3}$	off	off	off	on	off	on
0	off	off	on	on	off	off
0^*	on	on	off	off	off	off
$-V_{dc}$	off	on	off	off	on	off
$-\frac{2V_{dc}}{3}$	off	on	off	off	off	on
$-\frac{V_{dc}}{3}$	off	on	on	off	off	off

A typical technique for modulation is implemented to produce PWM switching signal. A carrier signal ($V_{carrier}$) is differentiated with three reference signals (V_{ref}). The offset value of reference signal is in phase, which is as same as the carrier signal amplitude. Reference signals have similar frequency and amplitude. All the relating signals are differentiated with transmitter signals. If one reference signal surpass the crest extent of carrier signal, then other reference was differentiated with carrier signal until it outreach the crest extent of transmitter signals. Hence forth third reference signal will be in action and differentiated with carrier signal till zero is attained. After this second reference will be differentiated till zero is achieved and the same occurs for the first reference signal.

For fundamental frequency with one cycle the developed inverter functions in six modes. Fig. 6 shows the output voltage signal for one cycle.

The six modes are explained as given below

Mode 1: $0 < \omega t < \theta_1$ and $\theta_4 < \omega t < \pi$

Mode 2: $\theta_1 < \omega t < \theta_2$ and $\theta_3 < \omega t < \theta_4$

Mode 3: $\theta_2 < \omega t < \theta_3$

Mode 4: $\pi < \omega t < \theta_5$ and $\theta_6 < \omega t < 2\pi$

Mode 5: $\theta_5 < \omega t < \theta_6$ and $\theta_7 < \omega t < \theta_8$

Mode 6: $\theta_6 < \omega t < \theta_7$

The phase angle of the inverter rely on modulation index M_a , for mono reference signal and mono transmitter signal the modulation indices is defined as

$$M_a = \frac{A_m}{A_c} \quad (7)$$

Hence for mono reference signal and two transmitter sign the modulation indices is

$$M_a = \frac{A_m}{2A_c} \quad (8)$$

So the developed seven level PWM MLI uses three transmitter signals, the modulation indices will be

$$M_a = \frac{A_m}{3A_c} \quad (9)$$

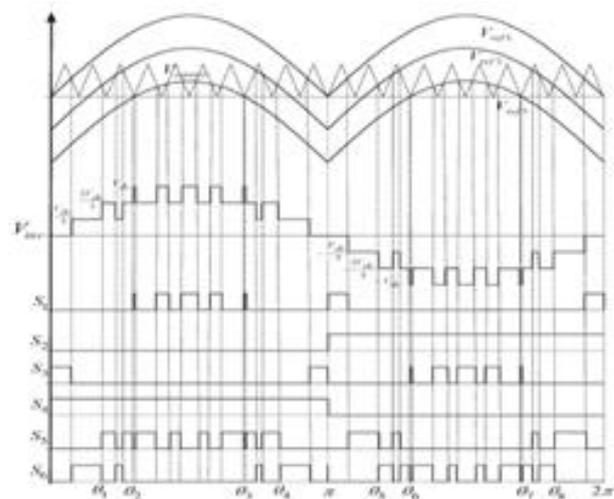


Fig. 6. Switching level of seven level inverter

Fig. 7 depicts the pattern of inverter changing. Switches S_1, S_3, S_5, S_6 will operate at the rate of messenger signal frequency. S_2 and S_4 will operate at a oscillation similar to base recurrence.

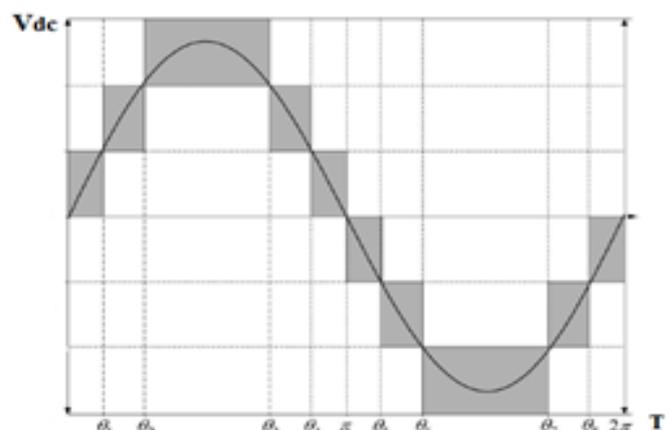


Fig. 7. Output voltage of seven level inverter

Where,

A =crest to crest value of messenger signal

A_m =crest value of reference voltage signal

Total Harmonic distortion and switching angle are evaluated and given to the PWM pulse generators of IGBT switches in order to get reduced THD. The developed inverter is used to decrease the THD and to achieve the multiple desired voltage levels using ANN based MPPT.

VI. SIMULATION RESULTS

In this system is validated with matlab simulation software. The high gain Luo converter is self regulation by ANN based MPPT formula to track the maximum energy from the solar system.

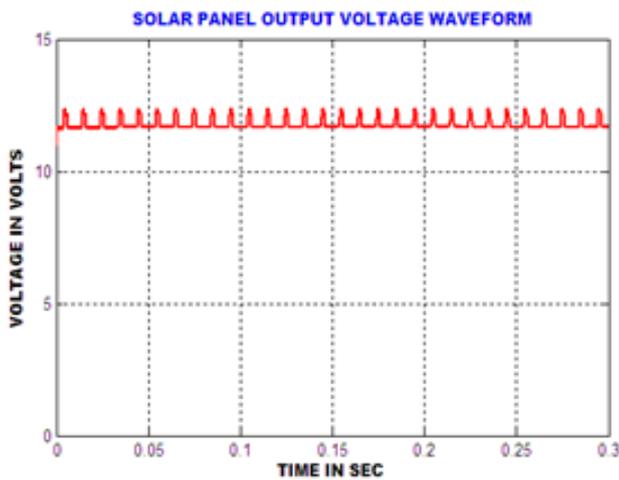


Fig. 8. Gross voltages from the PV system

Fig. 8. Depicts the desired voltage from the PV system, this voltage contains maximum order harmonics and fluctuations due to temperature and irradiance variation.

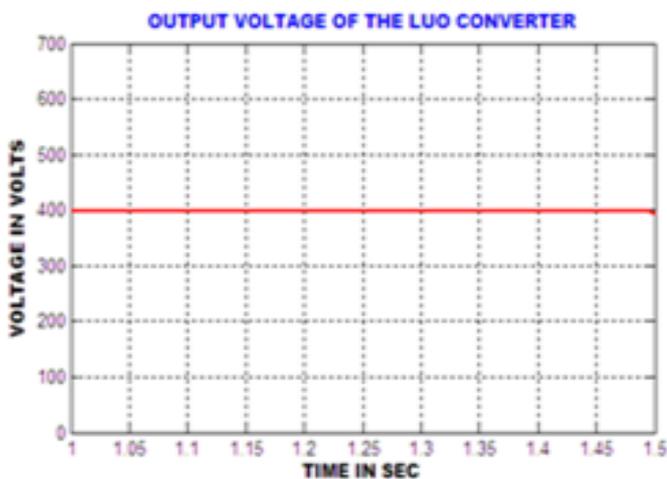


Fig. 9. Output voltage of the LUO converter

Fig 9. Depicts the desired voltage of the Luo converter using ANN based MPPT algorithm, this formula retain constant voltage to the seven level cascaded MLI.

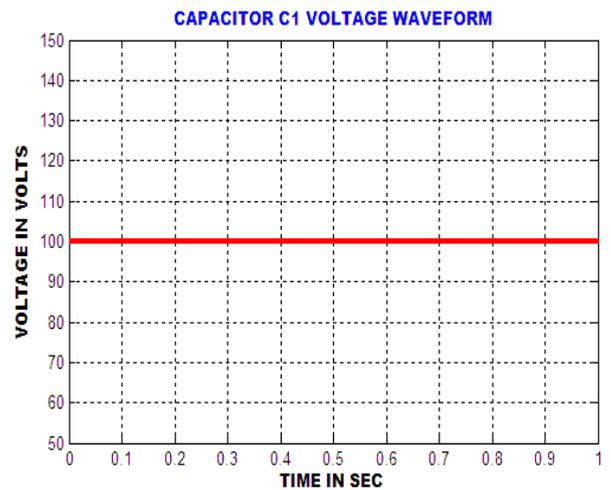


Fig. 10. Voltage across the capacitor C1 in the MLI

Electrical power is converted to mechanical power and addition to losses of frictional as well as power losses (Joules/sec).

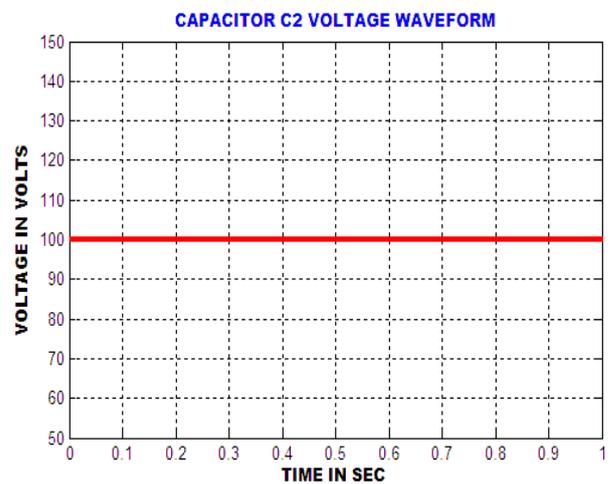


Fig. 11. Voltage across the capacitor C2 in the MLI

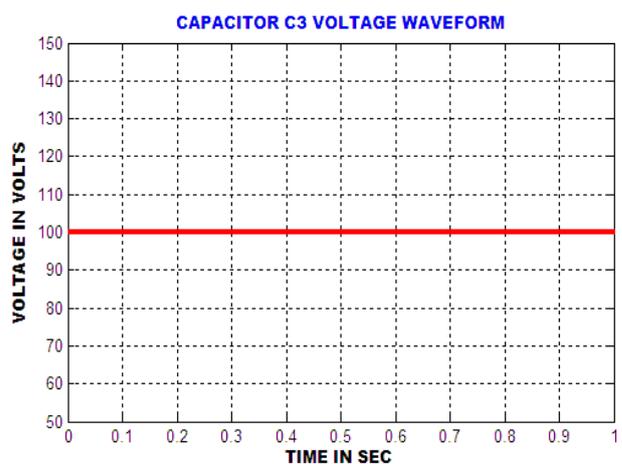


Fig. 12. Voltage across the capacitor C3 in the MLI

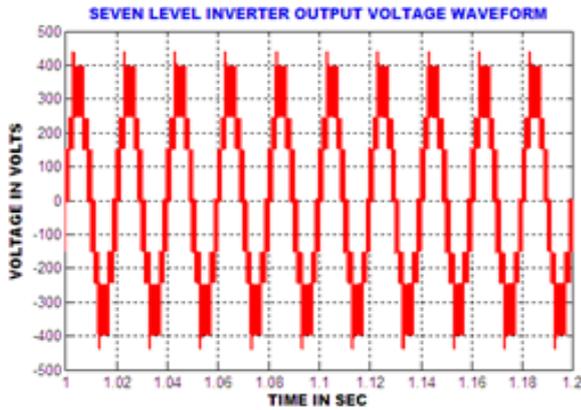


Fig. 13. Seven level inverter output voltage waveform

Fig. 13 Shows seven level output voltage of the proposed MLI using multi carrier modulation.

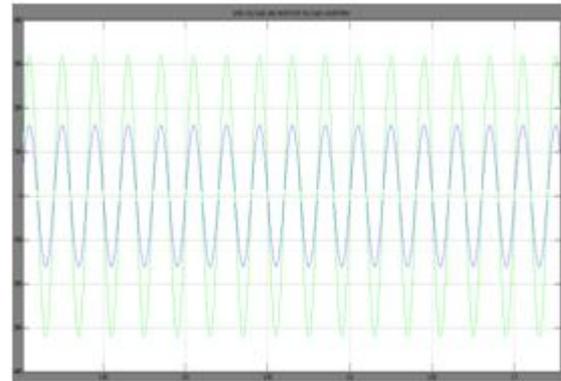


Fig. 16. Single phase grid output voltage and synchronization waveform

VII. CONCLUSION

The suggested high gain converter decreases the potential ripples coming through the PV system. The ANN-based MPPT formula receives greater energy from the solar system. The proposed reduced switch multilevel inverter has several advantages compared to conventional schemes, the common DC link voltage is divided into three DC sources using split capacitor to get the output voltage of seven level. The multicarrier based PWM control technique is meant to get the output voltage of seven levels. Phase synchronization is achieved using the PI controller. The proposed inverter has reduced the THD with only 7.34% and also maintains less energy condition issues in the network. The Matlab simulation is validated the output results for the proposed reduced switch Multi-stage inverter for PV structure linked to a phase.

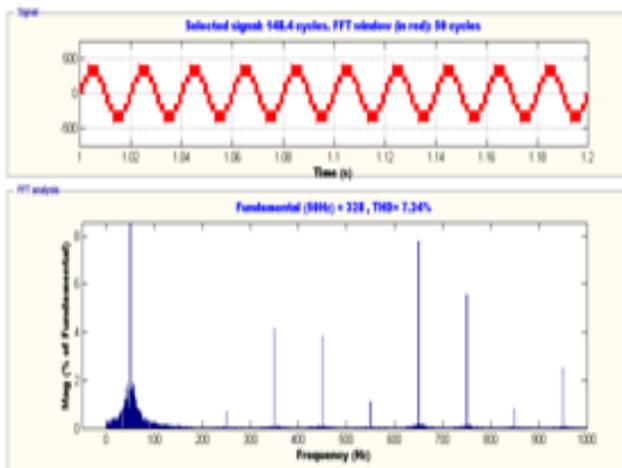


Fig. 14. Seven level inverter output voltage THD waveform

The figure 14. Shows the THD value of cascaded seven level inverter desired voltage THD adopting Multi carrier modulation techniques.

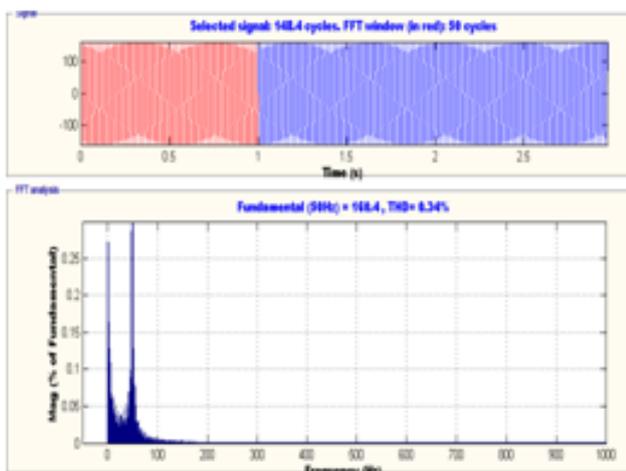


Fig. 15. Single phase grid output voltage THD waveform

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