

Design of closed-loop control for Solar PV based Multi-Level Inverter

P.Guruvulu Naidu, S.Satyanarayana, Ch.Saibabu

Abstract: Photovoltaic (PV) based multilevel inverters (MLI) have emerged as one of the best alternatives for grid/standalone applications. MLIs offer high range power handling capability with low current and voltage distortion and lesser switching losses as compare to the traditional two-level inverter. Major challenges with generally used multilevel inverter topologies relates to capacitor voltage variation, modulation techniques, and control. The major center of attention of research in this paper is to build up sophisticated modulation and voltage balancing methods for multilevel inverter topologies, competent to reach capacitor voltage parameter and to decrease power switching losses of the inverters. The present paper focuses on closed-loop control of multilevel flying capacitor inverter (FCI). In FCI, the phase shift (PS) based pulse width modulation method is designed and investigated. The closed-loop controller is designed with input voltage control of MLI, whose source is solar PV based Boost converter. Reported simulation results prove the superiority of the closed loop control in maintaining the desired output voltage for various reference values.

Keywords: Multi-Level Inverter, Phase-shifted Pulse-width modulation (PWM), Closed-loop control, Boost converter, Solar Photovoltaic.

I. INTRODUCTION

Modern semiconductor devices lead to the use of multilevel inverters in high range power electronic devices application. In this, the major problem of the traditional two-level power electronic inverter is the requirement of a huge number of power electronic devices connected in parallel and/or series in the direction of is competent to arrangement with high voltage/currents. These associations may generate over-voltages and/or over-currents to the power devices proper to their different characteristics. Additionally, oversized filtering components are essential for the reason with the purpose of the small feature of the output voltages. On the other hand, it will also add to the switching power losses of the inverter. All these problems are less considerable while using MLI. Multilevel inverters permit high power devices usage potential with condensed harmonics as well as low switching power losses while compared through the two-level matching part. Here Multilevel inverters be capable of development large amounts of power, this is why these topologies are attractive all the rage in applications such as oversized scale wind and solar photovoltaic power plants. Even though the multilevel conversion technology is comparatively

grown-up, various improvements preserve be prepared to these complex topologies. Various do research areas are the control of the voltages in the capacitors, and the enhancement of efficiency and reliability. Electrolytic capacitors are generally used in multilevel inverters; on the other hand, these components have a somewhat short lifespan. This is why they are being replaced by thin-film capacitors, which offer a slighter capacitance but a better life. The use of small capacitors implies closer control loops, as well as the voltage balancing loops. Additionally, the switching frequency of power inverters is likely to get higher shortly for the reason that of the use of the innovative wide-gap semiconductor devices, such as those based on silicon carbide (SiC). Higher switching frequencies involve in addition quicker and more efficient control loops. The multilevel inverter based on flying capacitors (FCs) topologies, i.e. in first case, flying capacitor inverter (FCI) device and second case stacked multi-cell inverter (SMC), have been paying attention including easier expansion to a quantity levels are higher than three (n greater than 3), while compared to diode clamped inverter (DCC) devices. Moreover, voltages in the FCs included into the topologies can be controlled on a per-phase basis dissimilar in the DCC, where all the phases influence the capacitor voltages. Even if there are various research publications on the multilevel FCI topologies, there are motionless a few aspects that have need of promote awareness. The most important research areas are the progress of computationally efficient modulation and voltage balancing techniques capable to make available solid FC voltages, the decrease of switching losses and capacitor voltage ripples, and the most important pre-charging of the FCs. Hence, this paper has been focused on all these topics.

II. MULTILEVEL FCI

Multilevel inverters have gained major attention here medium and high power electronic devices. By using power semiconductor devices based multi-level inverter circuits bear more voltage and power quaintly, lesser total harmonic distortion value, along with lesser losses with respective to the usual two-level inverter [1-2]. Along with various the multi-level inverter topologies, the mainly accepted methods are the Cascaded H-Bridge Inverters (CHBC), Modular Multilevel Inverter (MMC), DCC, and FCI [3]. Designed for the three-level FCI, the be in command of the FC voltages be comparatively easy; it be able to performed separately per every phase by purely irregular the two offered unnecessary switching states. An analogous voltage matching process is able in the direction of be apply to FCIs through an elevated quantity of levels (n greater than 3).

Revised Manuscript Received on December 13, 2019.

* Correspondence Author

P.Guruvulu Naidu, PhD scholar, Department of EEE, J.N.T.U University, Kakinada, Andhra Pradesh, India

Dr.S.Satyanarayana, Professor, Department of EEE, Raghu Institute of Technology, Visakhapatnam, Andhra Pradesh, India

Dr.Ch.Saibabu, Professor, Department of EEE, J.N.T.U University, Kakinada, Andhra Pradesh, India

On other hand, during this case, here a new unnecessary condition to compact with, additional FC voltages are to be restricted. Every unnecessary state produces unusual things of the capacitor voltages on the charging/discharging. Afterward, calculating the voltages from corner to corner the capacitors is a challenging in MLI inverter.

Here FCI has gained major awareness for the reason that of its enhanced show while complete to a elevated quantity of number (n greater than 3), with respective the DCC. In addition, the FCI offers some dc to dc applications such as a HVDC inverters ,VAR compensator and hybrid electric vehicle (HEV) as it provide good output performance , efficiency , output voltage and current[4]. On the other hand, there are various issues on the subject of the FCI which require to be measured during operating period of the various stage, while the early across FC charging, the capacitor voltage has ripples and reconfiguration for the period of faults, absolute voltage level process and voltage balance of the FCIs. A quantity of work regarding the reconfiguration and the comprehensive voltage level procedure was reported in [7].

Multilevel inverters contain major importance in medium and high power electronic devices such as motor drives, power generation, and distribution, power quality, and power conditioning. This is for the reason that power electronic multi-level inverter methods allow more voltage or power , produce voltages with low THD, as well as have low power losses with respective to the usual two-level inverter. However, multilevel inverters need a more number of power electronic switching devices and as a result their control becomes difficult and complex. In addition, they have need of a number of dc voltage sources which are frequently provided by capacitors. The capacitor voltages require to be balanced, which is demanding, especially for inverters with a large number of levels.

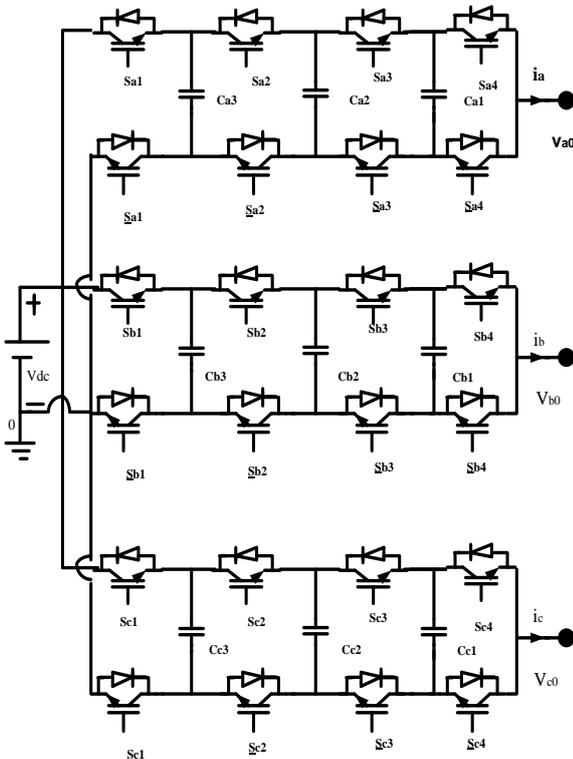


Fig.1 Three-phase five-level FCI inverter

The mainly conventional multilevel inverter topologies are viz. CHBC, MMC, DCC, FCI and

SMC. This paper is paying attention on the FC multilevel topologies, i.e. the FCI. These topologies are discussed in detail, at the same time as the other ones are discussed in a few words. In 1991, a single-phase, series connection of full-bridge inverter was accessible in [5] and used for a plasma stabilization application. Afterward, the proposal was absolute to consist of three-phase systems. The basic three-phase three-level and n-level structures used in the cascade inverter. The MMC was first projected in [4] and it has already been used in an HVDC application by Siemens. The DCC was introduced in [6]. The FCI was introduced in [7].

Figure 1 show a three-phase five-level FCI inverter, here three FCs be included with every phase-leg. for the duration of usual procedure, the stand for voltage values of the FCI inverter C_{x1} , C_{x2} , and C_{x3} , for $X=[a, b, c]$, should be maintained at $V_{dc}/4$, $V_{dc}/2$, and $3V_{dc}/4$, correspondingly, where V_{dc} is Dc link voltage from boost converter output voltage . In this circuit topology, here maximum voltage apply to every switching operating state is one-fourth of the dc-link voltage ($V_{dc}/4$). The switching state control utility is S_{xy} , here y defined switch one of the phase leg x of the FCI ($Y=[1...4]$). The switch manage functions preserve acquire two values $S_{xy} = [0, 1]$, i.e “0” and “1” the switch is off and on, in order. The switches another pairs of each phase-leg (S_{xy} and S_{xy}) activate and balancing the circuit. Each phase-leg can able to generate various output voltage levels. The various the potential output voltage levels are 0, $V_{dc}/4$, $V_{dc}/2$, $3V_{dc}/4$, and V_{dc} .

Through the FCs (ic_{x1} , ic_{x2} and ic_{x3}) can be written as:

$$V_{x0} = S_{x4}V_{dc} + (S_{x3} - S_{x4})VC_{x3} + (S_{x2} - S_{x3})VC_{x2} + (S_{x1} - S_{x2})VC_{x1} \quad (1)$$

$$ic_{x1} = (S_{x2} - S_{x1})i_x \quad (2)$$

$$ic_{x2} = (S_{x3} - S_{x2})i_x \quad (3)$$

The successive switching states levels of all the possible combinations of various voltage from [0000] {0} to [1111] {15} for a five-level FCI inverter. The operation states between two successive switching states, as individual's transitions entail varying only one bit. at the same time as a result, they generate the minimum number of switching.

2.1 Multilevel Modulation Techniques:

Multilevel PWM techniques be able to be classifying as shown in Fig. 2, In the technical literature, three main modulation techniques are broadly used and adopted; multicarrier PWM, elimination selective harmonic, and space- vector PWM. Every one of them is extension of the usual two-level PWM method to higher number of levels. Among all these techniques, the multicarrier PWM is easier to put into practice and provides extremely excellent outcome. The multicarrier PWM method is advance classified into two categories, i.e. PSPWM and level shifted PWM (LSPWM).

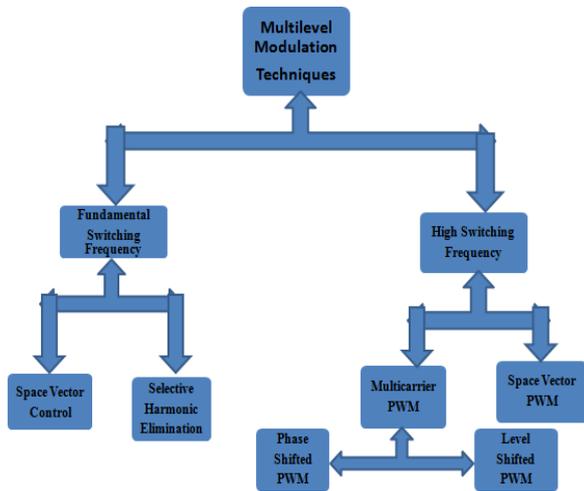


Fig. 2 classification of multilevel inverters and

$$i_{Cx3} = (S_{x4} - S_{x3})i_x \quad (4)$$

Table .1 Five-Level FCI: During switching states the inverter operation Voltage, current condition and Effects on the FC Voltages

Switching States (V_{x0})	Inverter Currents states		Inverter Voltages states			Voltage condition		
	S_{x4}	S_{x3} S_{x2} S_{x1} (State)	i_{Cx3}	i_{Cx2}	i_{Cx3}	VC_{x3}	VC_{x2}	VC_{x1}
5		[1 1 1 1] (15)	0	0	0	N	N	N
4	$3/4V_{dc}$	[1 1 1 0] (14)	0	0	i_x	Y	N	N
		[1 1 0 1] (13)	0	i_x	$-i_x$	N	↑	↓
		[1 0 1 1] (11)	i_x	$-i_x$	0	↑	↓	N
		[0 1 1 1] (7)	$-i_x$	0	0	↓	N	N
3	$V_{dc}/2$	[1 1 0 0] (12)	0	i_x	0	N	↑	N
		[1 0 1 0] (10)	i_x	$-i_x$	i_x	↑	↓	↑
		[0 1 1 0] (6)	$-i_x$	0	i_x	↓	N	↑
		[1 0 0 1] (9)	i_x	0	$-i_x$	↑	N	↓
		[0 1 0 1] (5)	$-i_x$	i_x	$-i_x$	↓	↑	↓
		[0 0 1 1] (3)	0	$-i_x$	0	↑	N	N
2	$V_{dc}/4$	[1 0 0 0] (8)	i_x	0	0	↑	N	N
		[0 1 0 0] (4)	$-i_x$	i_x	0	↓	↑	N
		[0 0 1 0] (2)	0	$-i_x$	i_x	N	↑	↓
		[0 0 0 1] (1)	0	0	i_x	N	N	N
1	0	[0 0 0 0] (0)	0	0	0	N	N	N

Reminder:
The charge/discharge effects in the FCI inverter is
 ↑ increase
 ↓ decrease
 N No modify in the capacitor voltage
 Y Modify in the capacitor voltage
 Assuming that i_x is positive (i_x greater than 0)

2.2 PS-PWM:

In multilevel inverters similar to the FCI and the SMC, each carrier of PS-PWM is related to a exacting power cell or pair of switches. In the case of a general n-level FCI, the carriers are phase shifted by $360^0=(n-1)$ in arrange to supply best possible stepped multilevel output voltage waveforms. By using control signal modulation like reference wave and carrier signals i.e PSPWM in the case of a five-level FCI. In this inverter, PSPWM requires four carriers of the same amplitude and frequency, which are phase-shifted by 90^0 (Fig.3, 4) among successive carrier signals.

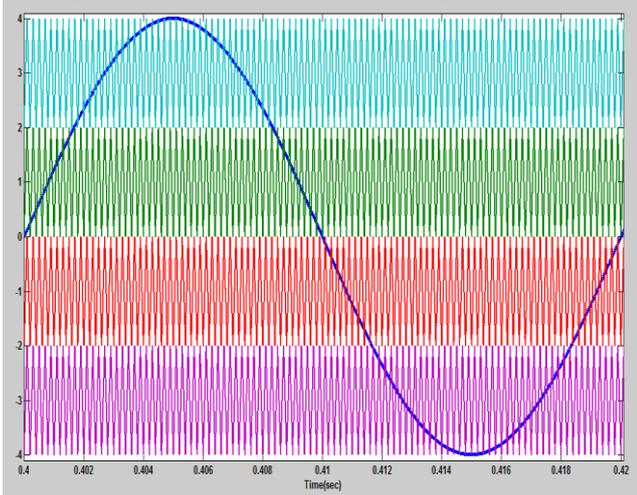


Fig.3 PSPWM method for five- level FCI, sinusoidal reference signal with carrier wave.

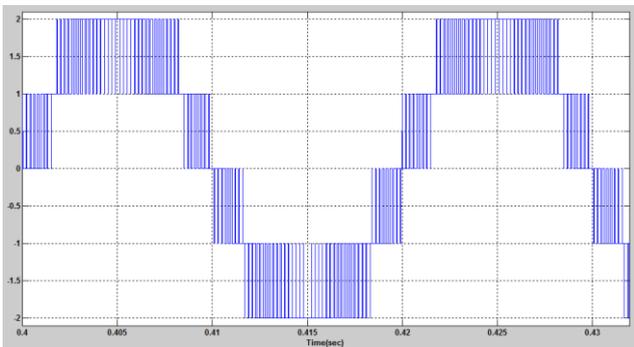


Fig.4 PSPWM Technique for five- level FCI, phases out put voltage.

Reference wave signal (V_{xref}) normalize in the period [-1, 1] is compare among all six triangular carriers signals modulated ,later connected FCI MLI inverter ,it has to be generated as the output voltage. With PSPWM, the capacitor voltages are obviously balanced. On the other hand, the voltage matching progression is generally time-consuming parameters are depends on the loading setting. Hence, energetic flying capacitor voltage balancing methods can be investigated and recovered. The FC voltages to their reference levels through enhanced dynamics performance, particularly under transient conditions and non-linear loads.

2.3 Fundamentals of the Voltage Balancing Method:

The planned voltage matching process is developed based going on the investigation a generic cell sector of the FCI as given away in Fig. 5. From Equations (1.1) to (1.4), the current during a capacitor is represented by:

$$i_{C_{xy}} = (S_{xy+1} - S_{xy})i_x \tag{5}$$

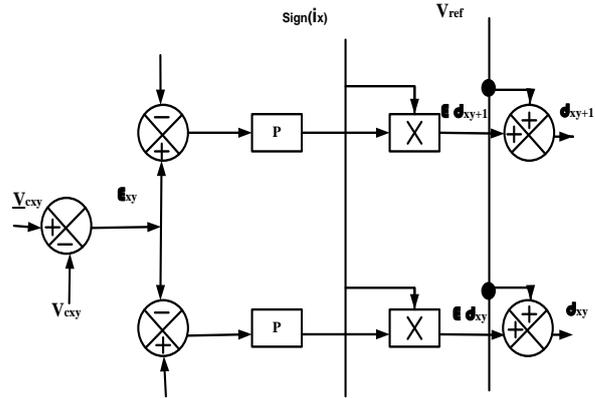


Fig. 5 FCs voltage balancing method based equations . It knows how to be observed that the current from end to end a capacitor is affect with the control signals related to the two contiguous switches. The locally-averaged illustration of the capacitor current in Equation (1.5) is designed over a switching period and is shown as:

$$i_{C_{xy}} = (d_{xy+1} - d_{xy})i_x \tag{6}$$

where $i_{C_{xy}}$ and i_x are the averaged currents of the capacitor C_{xy} and the output current, correspondingly, and d_{xy+1} and d_{xy} are the duty cycles of the switch S_{xy+1} and S_{xy} , in that order.

Table. 2 FCI simulation circuit parameters

FCI MLI Parameter	Value
Dc-link bus voltage, Vdc	48v
FCs(C_{x1}, C_{x2} and C_{x3})	110uf
Load resistance, R_L	20 ohms
Inductance, L	8mh
Carrier frequency(f_c)	5kHz
Fundamental frequency, f	50Hz
Frequency modulation index, f_m	40Hz

2.4 Dc-Dc Boost converter:

The dc-dc boost converter is a well-known method; It has been analyzed with references [4]. The expand of the boost converter is:

$$M_{boost} = \frac{V_{dc}}{V_{pv}} = \frac{1}{(1-D_{boost})} \tag{7}$$

Where V_{pv} is from solar PV panel of input voltage, V_{dc} is the dc link bus voltage. V_{dc} must give the suitable modulation indicator of the inverter. The voltage from PV-Array (V_{pv}) is between V_{pv} minimum and V_{pv} maximum which obtained the $D_{boost(min)}$ and $D_{boost(max)}$ as follow:

$$D_{boost(min)} = \frac{(V_{dc} - V_{pv(max)})}{V_{dc}} \quad (8)$$

$$D_{boost(max)} = \frac{(V_{dc} - V_{pv(min)})}{V_{dc}} \quad (9)$$

To minimization the dc to dc boost converter ripple current and voltage placed inductance and capacitor respectively like a passive filter placed from the PV-array.

Table 3. Specifications for 48v PV Panel 500W

P _{max} (Watts)	500
OC voltage	58.95
Power at max. Voltage	48.63
SC current	10.87
Current at Max.power current	10.28

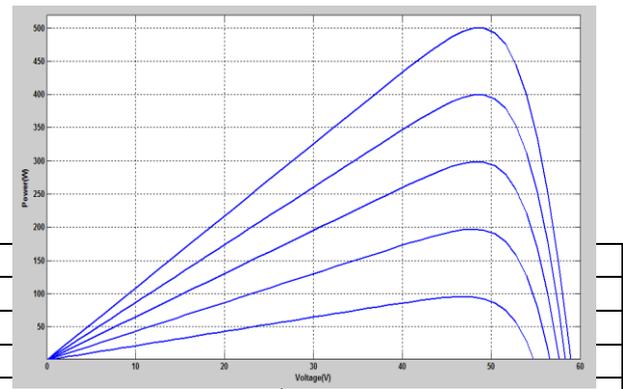


Fig. 10(b) Solar PV plots for different Irradiations (i.e 1000; 800; 600; 400; 200 W/m²), From solar PV module modeling simulation results are obtained at different irradiation condition I-V plot.

III. Closed-loop control of Multi-Level Inverter using Boost converter:

In the present work, Boost converter based closed-loop control is proposed for MLI. The closed-loop control of MLI is achieved through the variation of the input voltage. It is a simple external control strategy for MLI using a solar PV based boost converter. In the work input voltage of 48v is obtained using solar panel with 1000W/m² solar irradiation, 25 °C temperature. The desired out is compared with the input voltage given to the MLI. The error voltage is given to the PID controller. The control voltage is used to drive the IGBT of the dc to dc Boost converter. The closed loop control strategy is shown in Fig. 6.

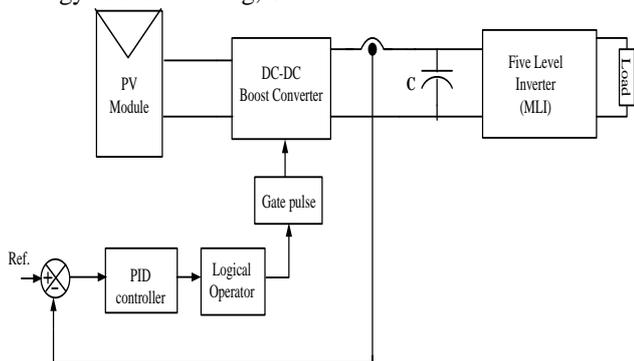


Fig.6 Closed-loop control strategy of MLI using Solar PV based Boost converter.

IV. SIMULATION RESULTS AND ANALYSIS:

The proposed work is demonstrated with MATLAB simulations. Below figure shows the output voltage of the Solar PV based dc-dc boost converter.

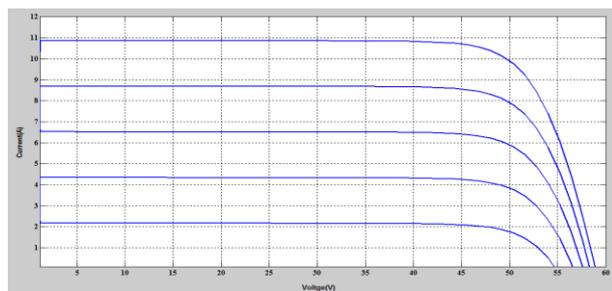


Fig. 10 (a) Solar PV I-V plots for different Irradiations (i.e 1000; 800; 600; 400; 200 W/m²) From solar PV module modeling simulation results are obtained at different irradiation condition I-V plot.

Keeping the fixed input voltage i.e. 48v from solar PV module and the closed-loop is demonstrated for different reference voltages.

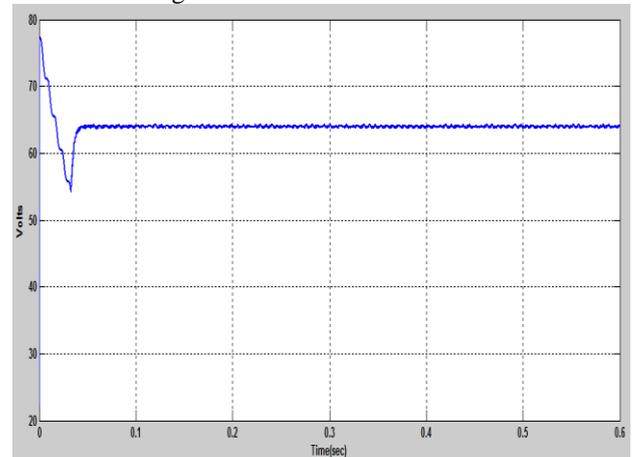


Fig.7 (a) Solar PV based Boost converter output voltage for reference voltage 64V From PV module maximum input voltage getting 48V by using Boost converter obtained a constant V_{dc} = 64V.

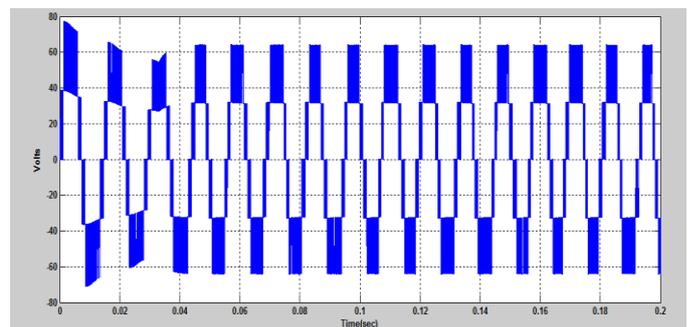


Fig.7 (b) First case FCI, boost converter input voltage is 48V and FCI closed loop control corresponding voltage is 64V.

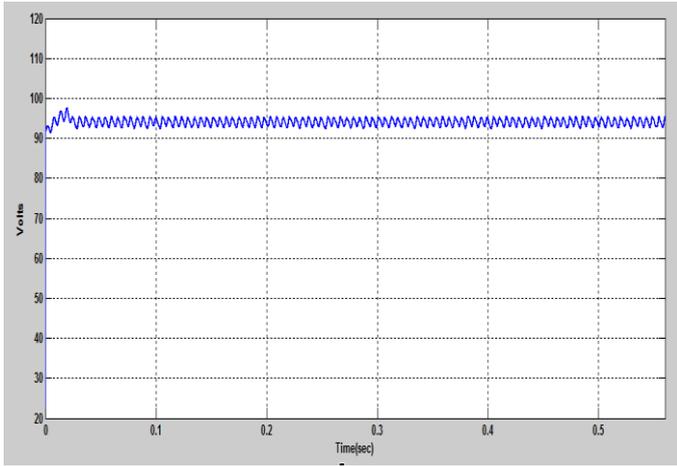


Fig.8 (a) Solar PV based Boost converter output voltage for reference voltage 94V From PV module maximum input voltage getting 48v by using Boost converter obtained a constant $V_{dc} = 94V$.

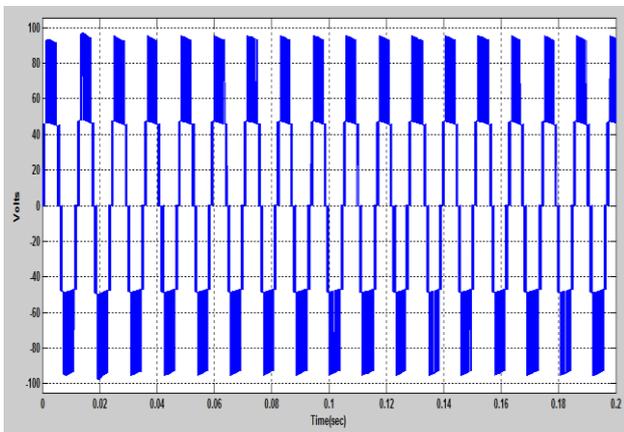


Fig.8 (b) Corresponding closed-loop control of MLI for 94V voltage Second case FCI, boost converter input voltage is 48v and FCI closed loop control corresponding voltage is 94V.

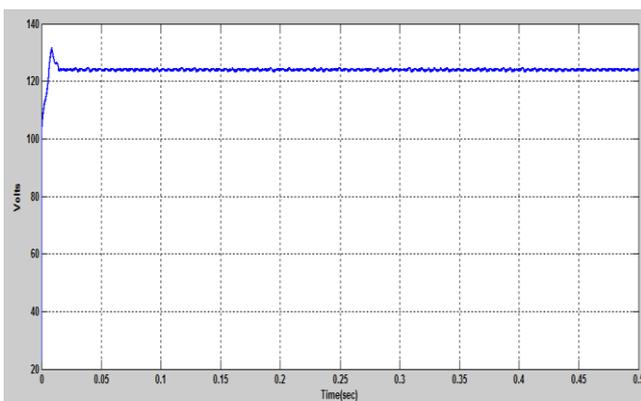


Fig.9 (a) Solar PV based Boost converter output voltage for reference voltage 124V From PV module maximum input voltage getting 48V by using Boost converter obtained a constant $V_{dc} = 124V$.

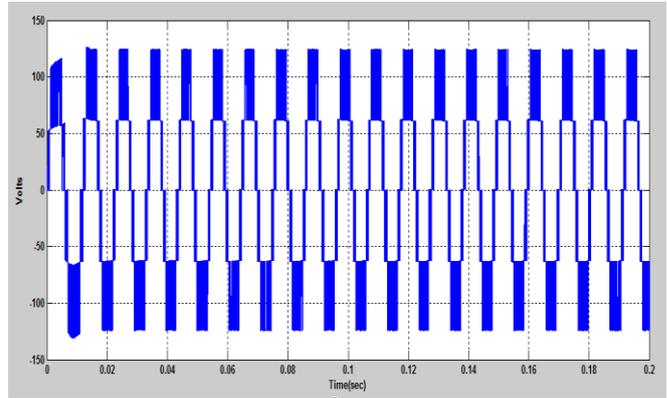


Fig.9(b) Corresponding closed loop control of MLI for 124 voltage Third case FCI, boost converter input voltage is 48V and FCI closed loop control corresponding voltage is 124V.

V. CONCLUSION:

MLIs are growing importance in recent times due to their advantages of having high level output with minimized distortion levels. Among all MLIs, flying capacitor based MLIs are important for solar PV applications due to their inherent advantage of less distorting and more flexibility for closed loop operations. In this regard, FC based MLI with sinusoidal PWM technique has been simulated and a controller for voltage regulation has been designed. The closed-loop operation is carried out with input voltage control variation using a Solar PV based Boost converter. The simulation results using MATLAB/SIMULINK have been presented to prove the effectiveness of closed loop controller FC based MLI for solar applications for various changes in irradiance levels.

References:

1. MohanaSundar,Manoharan , Ashraf,ahmed , Joung-Hu Park,"a pv power conditioning system using nonregenerative single-sourced trinary asymmetric multilevel inverter with hybrid control scheme and reduced leakage current" IEEE Transactions on Power Electronics , Volume: 32 , Issue: 10 , pp: 7602 – 7614, Oct. 2017 .
2. Moacyr Aureliano Gomes de Brito ; Luigi Galotto ; Leonardo Poltronieri Sampaio ; Guilherme de Azevedo, "Evaluation of the Main MPPT Techniques for Photovoltaic Applications": IEEE Transactions on IndustrialElectronics , Volume:60 , Issue:3 , pp: 1156 – 1167, March 2013.
3. Liwei Zhou ; Feng Gao ; Tao Xu," A Family of Neutral-Point-Clamped Circuits of Single-Phase PV Inverters:Generalized Principle and Implementation" IEEE Transactions on Power Electronics (Volume: 32 , Issue: 6 , pp: 4307 – 4319, June 2017)
4. Priyabrata Shaw,"Modelling and analysis of an analogue MPPT-based PV battery charging system utilising dc–dc boost converter" IET Renewable Power Generation (Volume: 13 , Issue: 11 , pp: 1958 – 1967, 8 19 2019.
5. Arpan Hota , Sachin Jain , Vivek Agarwal ,” An Improved Three-Phase Five-Level Inverter Topology With Reduced Number of Switching Power Devices” IEEE Transactions on Industrial Electronics ,Volume: 65 , Issue: 4 , pp: 3296 – 3305, April 2018.
6. Jianzhong Zhang ; Shuai Xu ; Xing Hu ; Yaodong Zhu," Voltage Balancing Control of Hybrid Stacked Multicell Inverters Based on Modified Phase-Shifted PWM" IEEE Access , Volume:7 pp: 25589 – 25602, 21 February 2019.

7. Jianzhong Zhang ; Shuai Xu ; Xing Hu ; Yaodong Zhu," Voltage Balancing Control of Hybrid Stacked Multicell Inverters Based on Modified Phase-Shifted PWM" IEEE Access ,Volume: 7 , pp: 25589 – 25602,21 February 2019.
8. Potnuru, Devendra, Javvaji Anilkumar, and A. P. Srikakulam. "A DC-DC inverter for low power applications using Arduino UnoMicrocontroller." International Journal of Pure and Applied Mathematics 114, no. 7 2017: 123-127, 2017.