

Analysis and Design of Micro Strip Patch Antenna for Bandwidth Enhancement

Ramji Gupta, Ram Soni



Abstract: Microstrip patch antenna for enhancement bandwidth has been presented based on stacked configuration. The microstrip patch antenna is a wide shaft narrowband antenna. it is made effectively by the printed circuit innovation and have the metallic layer at the two sides of the substrate, a transmitting patch on the upper side though metal plate on ground. For the improvements of bandwidth, there is influenced a more no. of slot on the ground plate to accomplish Ultra wide band (UWB). The UWB antennas is based on the micro strip patch antenna concept which is based on stacking. For the enhancement of bandwidth of the antenna, stacking is used in the designing of UWB antenna. In this research manuscript stack parasitic patch antenna is presented to enhance the bandwidth of the antennas different slot at ground. The proposed design is implemented in the CADFEKO software. For the design and analysis of a wide range of electromagnetic problem CADFEKO software is very useful having many applications to simulate 3D electromagnetic circuit included antenna design, micro strip antenna and circuits. The simulation results shows the enhanced antenna bandwidth of 14.5 GHz from 2.6 to 16.8 GHz..

Keywords: Ultra wide band (UWB), micro strip patch antenna, voltage standing wave ratio (VSWR), High data rate (HDR).

I. INTRODUCTION

The premise of the different technique for wireless communication is ultra-wide band (UWB). Heinrich Hertz in 1886, thought of the primary spark gap transmitter and confirmed Maxwell's condition. The first model for communicating two post office was invented in 1896, in London [1]. They were a mile apart. Further advancement will take place in transmitter, a new framework for UWB was presented by U.S. military. The utilization of this transmitter is from heartbeat transmissions to cover imaging, stealth and communication [2]. As indicated Shannon-Hartley hypothesis, channel capacity related to bandwidth is the primary advantage of the UWB communication system.

Revised Manuscript Received on February 28, 2020.

* Correspondence Author

Ramji Gupta*, Department of Electronics & Communication, Parul Institute of Engineering & Technology, Parul University, Vadodara, Gujarat, India. E-mail: ramjigupta38@gmail.com, ramji.gupta270086@paruluniversity.ac.in

© The Authors. Published by Blue Eyes Intelligence Engineering and Sciences Publication (BEIESP). This is an open access article under the CC-BY-NC-ND license http://creativecommons.org/licenses/by-nc-nd/4.0/

UWB frameworks work at exceedingly low levels of energy transmission. Likewise, The UWB can handle extra capacity of many Mbps as a result of it is vast bandwidth. Because of low energy density, UWB framework can offer a great degree sheltered and dependable communication framework and it makes incidental discovery rather troublesome.

There is various UWB correspondence applications are available, among which Low Data Rate (LDR) applications are exceptionally straightforward transmitters that restrict the unreasonable utilization of energy, in this manner empowering the battery to last longer. By and large, in UWB the pulse has extremely narrow range, typically a couple of nanoseconds, so it creates an ultra-wideband frequency spectrum [3]. This is utilized fundamentally in low information rate systems, for example, those utilized in armed forces networks, and these are difficult to distinguish and are likewise uncommonly up to the marks at obstructing protection. Additionally UWB is utilized in to the application areas where High Data Rate (HDR) is required, for example, True random number generator, [11-13], access of web and mixed media services, area specific services, home administration systems and gadgets [4], Super regenerative receiver [13].

II. MICROSTRIP PATCH ANTENNA

Microstrip patch antenna has arrived in mid twentieth century after the innovation of printed circuit technology and it is shown in Fig 1 while the side view of this antenna is given in Fig 2. Many researchers have done work in year 1950 to 1955 at the same time Deschamps present idea of radiator in microstrip [5-7]. Gutton and Baissinot registered a patent in 1955 in France [8]. To use of microstrip antenna beneficial for least loss tangent with emanate proficiently. Around then the main focus has come for stripline microwave planer structure and it has transverse electromagnetic wave (TEM) [8].

Microstrip radiator has come in knowledge due to its low loss tangent and better thermal and mechanical properties. These were currently named as microstrip antenna. The main reason of the real advantages of microstrip antenna is that they are extremely comfortable to planar and nonplanar surfaces can be effortlessly mounted on that and some application was done by scientist for example, flying machine, rocket, satellite correspondence led the cause of inspiration to the researchers for exploration of microstrip antennas. Narrow bandwidth was additionally an extreme issue for microstrip antenna.



Analysis and Design of Micro Strip Patch Antenna for Bandwidth Enhancement

After the IEEE invention an extra ordinary growth has come for this antenna all issue in present in IEEE Transaction on Antenna and propagation [8].

The substrate is sandwiched between radiating patch and ground plane. Two thin metallic layers (t << W) of copper and gold are use as metallic layer.

Generally most transmitting patch has square, rectangular, dipole, triangular, elliptical, and circular. Dielectric constants $2.2 \le \varepsilon_r \ge 12$ is accessible for substrate [9-10]. Thickness of substrates assumes an essential part in antenna attributes by and large are in the range $.03 \le L \ge 0.05$ cm utilized as ground plane.

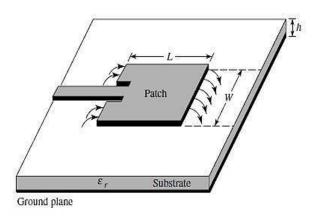


Fig: 1 Microstrip patch

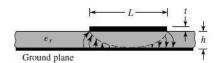


Fig: 2 Side view of Microstrip Patch Antenna

III. METHODOLOGY OF UWB MICROSTRIP PATCH ANTENNA

In this manuscript, the design methodology of Ultra Wide Band antenna (UWB) with CADFEKO software is presented. For the given dielectric substrate and length a patch antenna effective dielectric constant ϵ_{reff} and width \boldsymbol{w} is calculated by formula in eq. 1 and eq. 2 respectively

$$\varepsilon_{\rm reff} = \frac{\varepsilon_r + 1}{2} + \frac{\varepsilon_{\rm r} - 1}{2} \left(1 + 12 \frac{h}{w}\right)^{-1/2} \tag{1}$$

$$w = \frac{c}{2f_r} (\frac{\epsilon_r + 1}{2})^{-1/2}$$
(2)

and solution frequency is

Journal Website: www.ijitee.org

$$f_r = \frac{c_0}{2L\sqrt{\epsilon_{\text{reff}}}} \tag{3}$$

Where L is the length of patch and ε_{reff} is the effective relative dielectric constant of the substrate. In Table 1 the effective dielectric constant two different substrate materials is given. c_0 is the speed of light in free space.

Table 1 Effective dielectric constant of substrate

Parameter	Symbol	value
Dielectric	εreff_1	4.2

constant of		
FR4-epoxy		
Dielectric	ereff_2	2.1
constant of Rogers		
RT duroid 5880		

A. Frame work of proposed Enhanced bandwidth microstrip patch Antenna

The Fig 3 gives the flow chart of the proposed design for microstrip patch antenna of enhancement bandwidth. After doing literature survey of the antenna patch we choose a patch dimension and carried out the simulation using the CADFEKO software and check it results.

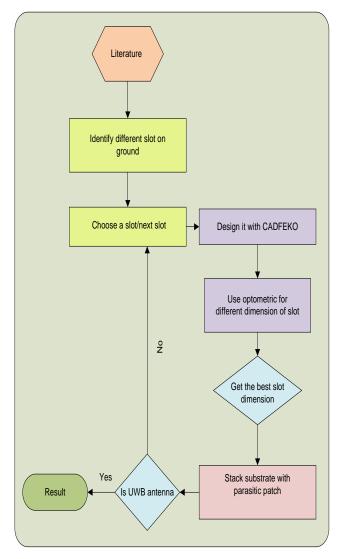


Fig. 3 Flowchart of proposed methodology of enhanced bandwidth (UWB) microstrip patch Antenna.

In the proposed designed impedance matching is done with the use of slot on the ground and stacked patch is presented to enhance the bandwidth. There is use of the proposed antenna in the all FCC standard band i.e. 3.1-10.6 GHz. Further enhanced bandwidth of antenna can be use in X and Ku band of antenna.





The X and Ku band antenna is very helpful in communication ranges, X band is use for Radar, Satellite communication and wireless computer networks. Whereas Ku band has the application in the satellite communication in the Europe, also for direct broadcast satellite services.

B. Simulation Results of enhanced bandwidth (UWB) microstrip patch Antenna.

The microstrip patch antenna using the FR4-epoxy substrate is shown in Fig 4.1. In the proposed design, microstrip feed patch is designed with the rectangular shape patch with FR4 epoxy substrate and then feeding it with 50 ohm feed line having multi resonant frequency obtained. The 50 ohm feed line is use to make return loss minimum so it produces efficient high gain of antenna. Further improvement of its S₁₁ parameter by introducing slot on the ground is designed.

According to IEEE standard the acceptable range for S₁₁ parameter for a perfect design of antenna is less than -10 dB. Here simulation of S_{11} parameter has been done at the resonance frequency of 7.8 GHz. The different parameter of the patch and substrate is given in Table 2. In all design for all dielectric substrate material thickness is taken 1.6 mm.

Table 2 Parameter of substrate and patch

Parameter	Value (in mm)
L	23
W	35
1	10
b	10
h	22
\mathbf{W}_{f}	3
h_g	20

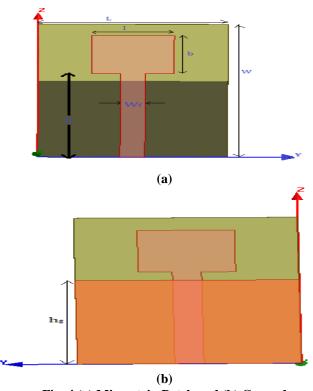


Fig. 4 (a) Microstrip Patch and (b) Ground

Where W and L is the length and width of dielectric substrate as shown in Fig 4 respectively. The ground dimension is h_g and W which is same as dielectric substrate. W_f and h is the feed dimension which is combined with patch. Source is used at the bottom side of feed line.

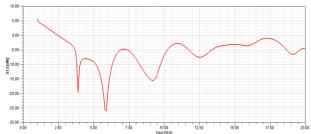


Fig 5 S_{11} parameter of microstrip patch

The result of S_{11} parameter is shown in Fig 5. Here more than one frequency has come in the acceptable range of -10 dB. The frequency in GHz is shown on the x axis whereas reflection coefficient parameter in y axis in dB. Further, the parametric analysis of the patch is done below.

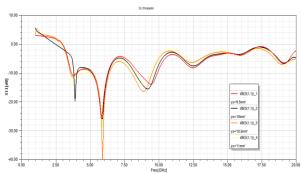


Fig 6 Parametric y dimension variation of microstrip patch

Here parametric analysis is shown for reflection parameter and found that the y dimension of the patch at 10.5 mm gives the sharp patches. The patch y dimension ys are shown in Fig.6. The value of ys has varied from 9.5 to 11 mm.

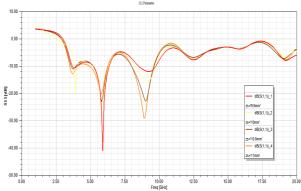


Fig 7 Parametric z dimension variation of microstrip patch

Here parametric analysis is shown and found that the z dimension of the parametric solution gives the sharp frequency at 9 mm. The patch z dimension zs are shown in Fig.7 The value of zs also has varied from 9.5 to 11 mm. For the increase of bandwidth of antenna different slot on the ground has been introduced. First simulate triangular slot has

shown in Fig 8.

Analysis and Design of Micro Strip Patch Antenna for Bandwidth Enhancement

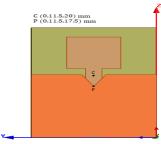


Fig. 8 Microstrip patch with triangular slot

In the given slot, point 'c' and 'p' is shown with the coordinate (0,11.5,20) mm and (0,11.5,17.5) mm respectively. Parametric solution of the triangular slot patch is calculated and by seeing result found out the exact distance between slot. The efficient S_{11} parameter using given coordinate for the triangular slot is shown in Fig 4.7, in parametric analysis here height of slot that is distance between c and p point varied from 16.5mm to 18mm.

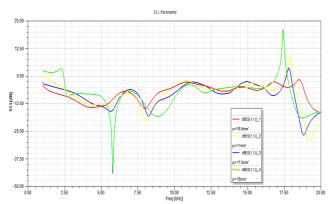


Fig. 9 Parametric solution of S_{11} parameter for triangular slot

Equivalent S_{11} parameter is given in Fig 9, at slot height is taken as 18 mm. From the result obtained it is clear that, it is not suitable for the wide band antenna because many ranges of frequencies have the S_{11} parameter above the -10 dB band. In the first two UWB antennas design triangular slot has introduced and analysis is given. While at last, stack patch concept has been used for UWB application.

IV. CONCLUSION

The microstip patch antenna for the enhancement bandwidth has been presented for the the application of UWB in this manuscript. The proposed design of the enhancement bandwidth antenna is based on the stack configuration or multi-layer antenna. The proposed antennas has wide band of operation and they are satisfying all the design standards of microstrip patch antenna design. We got the antenna bandwidth of 14.5 GHz from 2.6 to 16.8 GHz. So we can clearly say that the antenna is ultra-wide band in nature.

REFERENCES

- H. Nikookar and R. Prasad, "Introduction to ultra-wideband for wireless communications" Springer, vol.200, 2009.
- M. Mehranpour, J. Nourinia, C. Ghobadi, and M. Ojaroudi, "Dual band-notched square monopole antenna for ultrawideband applications," Antennas and Wireless Propagation Letters, IEEE, vol.11, pp. 172-175, 2012.

- R. Azim, M. T. Islam and N. Misran, "Compact Tapered-Shape Slot Antenna for UWB Applications," *IEEE Antennas and Wireless Propagation Letters*, vol. 10, pp. 1190-1193, 2011.
- H. Jiang, Z. Xue, W. Li and W. Ren, "Broad beamwidth stacked patch antenna with wide circularly polarised bandwidth," *Electronics Letters*, vol. 51, no. 1, pp. 10-12, 1 8 2015.
- Q. Zhu, S. Yang and Z. Chen, "Modified corner-fed dual-polarised stacked patch antenna for micro-base station applications," Electronics Letters, vol. 51, no. 8, pp. 604-606, 4 16 2015.
- A. Katyal and A. Basu, "Compact and Broadband Stacked Microstrip Patch Antenna for Target Scanning Applications," *IEEE Antennas and Wireless Propagation Letters*, vol. 16, pp. 381-384, 2017.
- Z. Yang, K. C. Browning and K. F. Warnick, "High-Efficiency Stacked Shorted Annular Patch Antenna Feed for Ku-Band Satellite Communications," IEEE Transactions on Antennas and Propagation, vol. 64, no. 6, pp. 2568-2572, June 2016.
- L. Qiu, H. Y. Qi, F. Zhao, K. Xiao and S. L. Chai, "A Shaped-Beam Stripline-Fed Aperture Coupled Stacked Patch Array," IEEE Transactions on Antennas and Propagation, vol. 64, no. 7, pp. 3172-3176, July 2016.
- A. Katyal and A. Basu, "Analysis and optimisation of broadband stacked microstrip antennas using transmission line model," IET Microwaves, Antennas & Propagation, vol. 11, no. 1, pp. 81-91, Aug., 2017
- N. Ramli, M. T. Ali, M. T. Islam, A. L. Yusof and S. Muhamud-Kayat, "Aperture-Coupled Frequency and Patterns Reconfigurable Microstrip Stacked Array Antenna", IEEE Transactions on Antennas and Propagation, vol. 63, no. 3, pp. 1067-1074, March 2015.
- Ramji Gupta, A. Pandey, R. K. Baghel, (2019). FPGA Implementation of Chaos based High-Speed True Random Number Generator, International Journal of Numerical Modeling. https://doi.org/10.1002/jnm.2604.
- Ramji Gupta, A. Pandey, R. K. Baghel, (2018). Efficient design of chaos based 4 bit true random number generator on FPGA, International journal of Engineering & Technology, 7 (3) pp.1783-1785. https://doi.org/10.14419/ijet.v7i3.16586.
- M. Priya, S. Giritha, Ramji Gupta, A. Pandey, (2017). Compact Chaotic oscillator using 180 nm CMOS technology for its use in True Random Number Generator, Proceeding International conference on Recent Innovations is Signal Processing and Embedded Systems (RISE-2017), IEEE conference, pp. 366-370. https://doi.org/10.1109/RISE.2017.8378183.
- R. Soni, Ramji Gupta, D.Sen, (2020), 'Efficient design of micro strip patch antenna for the ultra-wideband (UWB) applications' International Journal of Recent Technology and Engineering (IJRTE), v8 (5). DOI:10.35940/ijrte.E6822.018520.

AUTHORS PROFILE



Ramji Gupta received the Bachelors degree in Instrumentation & Control Engineering from Rajiv Gandhi Technical University, Bhopal, M.P. India, in 2006. He has done his M.Tech and Ph.D. degree in the Department of Electronics and Communication Engineering at Maulana Azad National Institute of Technology, Bhopal, M.P., India in 2012 and 2019 respectively. He has been a Assistant Professor of

Department of Electronics and Communication Engineering at Parul Institute of Engineering & Technology, Parul University, Vadodara, Gujarat, India since 2019 His research interests include Chaos communication, VLSI design, true random number generator circuits.



Ram Soni received the Bachelors degree in Electronics & Communication Engineering from Rajiv Gandhi Technical University, Bhopal, M.P., India, in 2014. Currently he is M.Tech scholar at Technocrats Institute of Technology Bhopal. His research area includes digital communication and circuits.



Journal Website: www.ijitee.org